1. Description

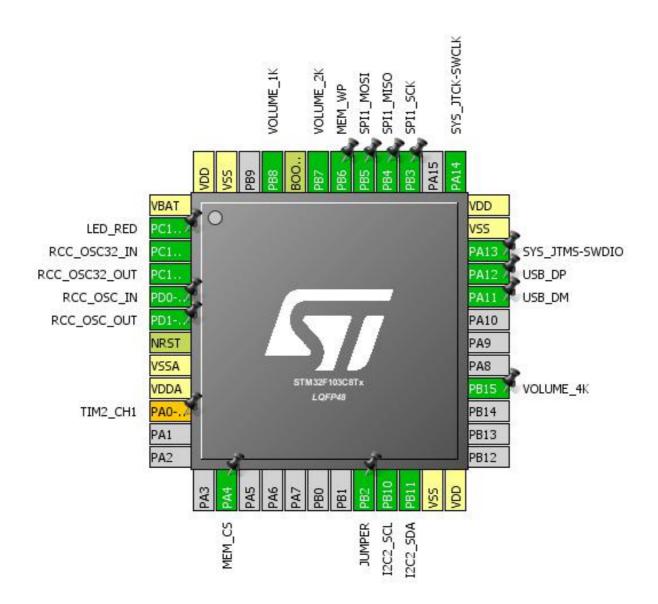
1.1. Project

Project Name	sound103
Board Name	sound103
Generated with:	STM32CubeMX 4.23.0
Date	12/26/2017

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



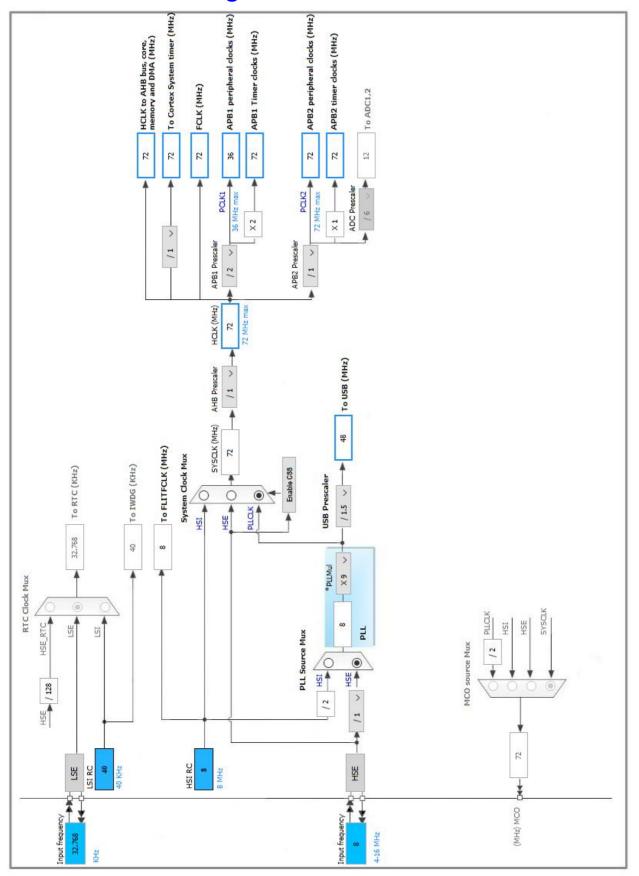
3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP48	(function after		Function(s)	
	reset)			
1	VBAT	Power		
2	PC13-TAMPER-RTC *	I/O	GPIO_Output	LED_RED
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP **	I/O	TIM2_CH1	
14	PA4 *	I/O	GPIO_Output	MEM_CS
20	PB2 *	I/O	GPIO_Input	JUMPER
21	PB10	I/O	I2C2_SCL	
22	PB11	I/O	I2C2_SDA	
23	VSS	Power		
24	VDD	Power		
28	PB15 *	I/O	GPIO_Output	VOLUME_4K
32	PA11	I/O	USB_DM	
33	PA12	I/O	USB_DP	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
39	PB3	I/O	SPI1_SCK	
40	PB4	I/O	SPI1_MISO	
41	PB5	I/O	SPI1_MOSI	
42	PB6 *	I/O	GPIO_Output	MEM_WP
43	PB7 *	I/O	GPIO_Output	VOLUME_2K
44	BOOT0	Boot		
45	PB8 *	I/O	GPIO_Output	VOLUME_1K
47	VSS	Power		
48	VDD	Power		

^{*} The pin is affected with an I/O function



4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. I2C2

12C: 12C

5.1.1. Parameter Settings:

Master Features:

I2C Speed Mode Fast Mode *

I2C Clock Speed (Hz) 400000

Fast Mode Duty Cycle Duty cycle Tlow/Thigh = 2

Slave Features:

Clock No Stretch Mode Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0
General Call address detection Disabled

5.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

5.2.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

5.3. SPI1

Mode: Full-Duplex Master

5.3.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 4 *

Baud Rate 18.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

5.4. SYS

Debug: Serial Wire

Timebase Source: TIM3

5.5. USB

mode: Device (FS)

5.5.1. Parameter Settings:

Basic Parameters:

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 64 Bytes *

Power Parameters:

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

5.6. FATFS

mode: User-defined

5.6.1. Set Defines:

Version:

FATFS version R0.11

Function Parameters:

FS_READONLY (Read-only mode) Disabled

FS_MINIMIZE (Minimization level) Enabled with 12 functions removed *

USE_STRFUNC (String functions)

USE_FIND (Find functions)

USE_MKFS (Make filesystem function)

Enabled

USE_LABEL (Volume label functions)

Disabled
USE_FORWARD (Forward function)

Disabled

Locale and Namespace Parameters:

CODE_PAGE (Code page on target) Cyrillic (OEM) *

USE_LFN (Use Long Filename) Disabled
MAX_LFN (Max Long Filename) 12 *

LFN_UNICODE (Enable Unicode) ANSI/OEM

STRF_ENCODE (Character encoding) ANSI/OEM *

FS_RPATH (Relative Path) Disabled

Physical Drive Parameters:

VOLUMES (Logical drives) 1

MAX_SS (Maximum Sector Size)

MIN_SS (Minimum Sector Size)

4096 *

MULTI_PARTITION (Volume partitions feature)

USE_TRIM (Erase feature)

Disabled

FS_NOFSINFO (Force full FAT scan)

0

System Parameters:

FS_TINY (Tiny mode) Enabled *

FS_NORTC (Timestamp feature) Dynamic timestamp

NORTC_YEAR (Year for timestamp) 2015

NORTC_MON (Month for timestamp) 6

NORTC_MDAY (Day for timestamp) 4

WORD_ACCESS (Platform dependent access option) Byte access FS_REENTRANT (Re-Entrancy) Enabled

FS_TIMEOUT (Timeout ticks) 1000

SYNC_t (O/S sync object) osSemaphoreId

FS_LOCK (Number of files opened simultaneously) 1 *

5.7. FREERTOS

mode: Enabled

5.7.1. Config parameters:

Versions:

FreeRTOS version 9.0.0
CMSIS-RTOS version 1.02

Kernel settings:

USE_PREEMPTION Enabled

CPU_CLOCK_HZ SystemCoreClock

 TICK_RATE_HZ
 1000

 MAX_PRIORITIES
 7

 MINIMAL_STACK_SIZE
 128

 MAX_TASK_NAME_LEN
 16

 USE_16_BIT_TICKS
 Disabled

 IDLE_SHOULD_YIELD
 Enabled

 USE_MUTEXES
 Enabled

USE_MUTEXES Enabled
USE_RECURSIVE_MUTEXES Disabled
USE_COUNTING_SEMAPHORES Disabled
QUEUE_REGISTRY_SIZE 8

USE_APPLICATION_TASK_TAG Disabled

ENABLE_BACKWARD_COMPATIBILITY

USE_PORT_OPTIMISED_TASK_SELECTION

USE_TICKLESS_IDLE

USE_TASK_NOTIFICATIONS

Enabled

Enabled

Memory management settings:

Memory Allocation Dynamic
TOTAL_HEAP_SIZE 3500 *
Memory Management scheme heap_4

Hook function related definitions:

USE_IDLE_HOOK Disabled
USE_TICK_HOOK Disabled
USE_MALLOC_FAILED_HOOK Disabled
USE_DAEMON_TASK_STARTUP_HOOK Disabled

CHECK_FOR_STACK_OVERFLOW Option2 *

Run time and task stats gathering related definitions:

GENERATE_RUN_TIME_STATS Disabled
USE_TRACE_FACILITY Disabled
USE_STATS_FORMATTING_FUNCTIONS Disabled

Co-routine related definitions:

USE_CO_ROUTINES Disabled MAX_CO_ROUTINE_PRIORITIES 2

Software timer definitions:

USE_TIMERS Disabled

Interrupt nesting behaviour configuration:

LIBRARY_LOWEST_INTERRUPT_PRIORITY 15
LIBRARY_MAX_SYSCALL_INTERRUPT_PRIORITY 5

5.7.2. Include parameters:

Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled vTaskDelete Enabled Disabled vTaskCleanUpResources vTaskSuspend Enabled vTaskDelayUntil Disabled Enabled vTaskDelay xTaskGetSchedulerState Enabled xTaskResumeFromISR Enabled xQueueGetMutexHolder Disabled xSemaphoreGetMutexHolder Disabled Disabled pcTaskGetTaskName uxTaskGetStackHighWaterMark Disabled xTaskGetCurrentTaskHandle Disabled Disabled eTaskGetState xEventGroupSetBitFromISR Disabled xTimerPendFunctionCall Disabled xTaskAbortDelay Disabled xTaskGetHandle Disabled

5.8. USB DEVICE

1

Class For FS IP: Mass Storage Class

USBD_SUPPORT_USER_STRING (Enable user string descriptor)

5.8.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)

USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)

USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors) 128 *

Enabled * Enabled USBD_SELF_POWERED (Enabled self power)

USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message

Class Parameters:

MSC_MEDIA_PACKET (Media I/O buffer Size) 4096 *

5.8.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English(United States)

MANUFACTURER_STRING (Manufacturer Identifier) Sound Reklama *

Device Descriptor FS:

22314 PID (Product IDentifier)

PRODUCT_STRING (Product Identifier) Sound Reklama FLASH *

SERIALNUMBER_STRING (Serial number) 0000000001A CONFIGURATION_STRING (Configuration Identifier) MSC Config MSC Interface INTERFACE_STRING (Interface Identifier)

^{*} User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PB3	SPI1_SCK	Alternate Function Push Pull	n/a	High *	
	PB4	SPI1_MISO	Input mode	No pull-up and no pull-down	n/a	
	PB5	SPI1_MOSI	Alternate Function Push Pull	n/a	High *	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
Single Mapped Signals	PA0-WKUP	TIM2_CH1	Alternate Function Push Pull	n/a	Low	
GPIO	PC13- TAMPER- RTC	GPIO_Output	Output Push Pull	n/a	Low	LED_RED
	PA4	GPIO_Output	Output Push Pull	n/a	Low	MEM_CS
	PB2	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	JUMPER
	PB15	GPIO_Output	Output Open Drain *	n/a	Low	VOLUME_4K
	PB6	GPIO_Output	Output Push Pull	n/a	Low	MEM_WP
	PB7	GPIO_Output	Output Open Drain *	n/a	Low	VOLUME_2K
	PB8	GPIO_Output	Output Open Drain *	n/a	Low	VOLUME_1K

sound103 Projec
Configuration Repor

6.2. DMA configuration

DMA request	Stream	Direction	Priority
SPI1_RX	DMA1_Channel2	Peripheral To Memory	Low
SPI1_TX	DMA1_Channel3	Memory To Peripheral	Low
I2C2_TX	DMA1_Channel4	Memory To Peripheral	Very High *

SPI1_RX: DMA1_Channel2 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Byte

Memory Data Width:

SPI1_TX: DMA1_Channel3 DMA request Settings:

Byte

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Byte

Memory Data Width: Byte

I2C2_TX: DMA1_Channel4 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable *

Peripheral Data Width: Byte Memory Data Width: Byte

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
DMA1 channel2 global interrupt	true	5	0
DMA1 channel3 global interrupt	true	5	0
DMA1 channel4 global interrupt	true	10	0
USB low priority or CAN RX0 interrupts	true	5	0
TIM3 global interrupt	true	0	0
I2C2 event interrupt	true	6	0
I2C2 error interrupt	true	6	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		
SPI1 global interrupt		unused	

^{*} User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103C8Tx
Datasheet	13587_Rev17

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	sound103
Project Folder	D:\Work\old\Sound\src\sound103
Toolchain / IDE	EWARM
Firmware Package Name and Version	STM32Cube FW_F1 V1.6.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	