

Reg. Na	CM_PER_GPIO2_CLKCTRL / CM_PER_UART5_CLKCTRL																															
Bit Name																																
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
Hex Val	0				0				0				0				0				0				2							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	GPIO2_FALLINGDETECT / GPIO2_IRQSTATUS_SET_0																															
Bit Name																																
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Hex Val	0				0				0				0				0				0				0				2			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	INTC_MIR_CLEAR1																															
Bit Name																																
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Hex Val	0				0				0				0				4				0				0				1			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	conf_lcd_data14 (0x8D8)																										9.3.150					
Bit Name																										RE	PD	MD	MD	MD		
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	0	
Hex Val	0				0				0				0				0				0				2				E			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	conf_lcd_data15 (0x8DC)																										9.3.150					
Bit Name																										RE	PD	MD	MD	MD		
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	
Hex Val	0				0				0				0				0				0				0				E			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	conf_lcd_data9 (0x8C4)																										9.3.150					
Bit Name																										RE	PD	MD	MD	MD		
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	0	
Hex Val	0				0				0				0				0				0				2				C			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Na	conf_lcd_data8 (0x8C0)																										9.3.150					
Bit Name																										RE	PD	MD	MD	MD		
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	
Hex Val	0				0				0				0				0				0				0				C			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

Reg. Name	LCR_UART (0xC) (Switch to MODE A)																												19.5.1.13							
Bit Name																													DLE				NP	NBS	CL	CL
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	1
Hex Val	0				0				0				0				0				0				8				3							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
Reg. Name	DLL_UART (0x00) (MODE A)																												19.5.1.3							
Bit Name																													LSB	LSB	LSB	LSB	LSB	LSB	LSB	LSB
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	0		
Hex Val	0				0				0				0				0				0				4				E							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
Reg. Name	DLH_UART (0x04) (MODE A)																												19.5.1.7							
Bit Name																															MSE	MSE	MSE	MSE	MSB	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Hex Val	0				0				0				0				0				0				0				0							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
Reg. Name	LCR_UART (0xC) (Switch to OP MODE)																												19.5.1.13							
Bit Name																													DLE				NP	NBS	CL	CL
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	
Hex Val	0				0				0				0				0				0				0				3							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
Reg. Name	MDR1_UART (0x20) (ALL MODES)																												19.5.1.26							
Bit Name																																IRS	MS	MS	MS	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Hex Val	0				0				0				0				0				0				0				0							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
Reg. Name	FCR_UART (0x8) (MODE A or OP MODE)																												19.5.1.11							
Bit Name																																DM	TXC	RXC	FEN	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0		
Hex Val	0				0				0				0				0				0				0				6							
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				

Reg. Name	INTC_PENDING_IRQ_1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																											
Bit Name																		UART5																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																										</

Reg. Name	IER_UART (0x4) (OP MODE)																												19.5.1.6				
Bit Name																													MOD	THRIT			
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Hex Val	0				0				0				0				0				0				0								
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Reg. Name	LSR_UART (0x14) (MODE A or OP MODE)																												19.5.1.19				
Bit Name																													THR				
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
Hex Val	0				0				0				0				0				0				2				0				
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Reg. Name	IER_UART (0x4) (OP MODE)																												19.5.1.6				
Bit Name																													MOD	THRIT			
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Hex Val	0				0				0				0				0				0				0								
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Reg. Name	INTC_Control																												6.6.1.6				
Bit Name																																NIRC	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
Hex Val	0				0				0				0				0				0				1								
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	