Reg. Na										СМ	_PE	R_G	PIO2	_CL	KCT	RL/	CM_	PEF	QUA	RT5	_CL	KCTI	RL.									
Bit Name	_																				П					П						
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Hex Val		()))			Ċ	1			()			Ö		一)	寸		2	2	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
																																_
Reg. Na										G	PIO	2_F/	LLIN	GDE	TEC	CT / C	3PIO	<u>2_IF</u>	QST	ATL	JS_S	ET_(<u> </u>									_
Bit Name					\Box			_					\Box	_	_				_	_		_	_	_	_			4	_	_	_	_
Bit Val.	0	0	0	0	0	0	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Hex Val)			(1	- 0								0)	_		2	2	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	-7	6	5	4	3	2	1	_0
Reg. Na	_													<u>I</u>	NTC <u>.</u>	_MIR	LCL!	EAR	1 ,								,					
Bit Name	_		Щ		Щ			Ь.	Щ				Щ	_								_	_	_	_		\sqcup	_		_		
Bit Val.	0	0		0	0			0	0	0	0	0	0	0	0	0	0	1		0	0	0	0	이	0	0		이	0	0	0	_1
Hex Val	-	(-		071	00	-		001	001		-00	401	<u> </u>				4	<u> </u>	40		0		_		_(_		1	·	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	_0
														_					_			_	_	_	_			_	_	_		
																												_				_
Reg. Na						_					_	соп	f_loc	_da	ta14	(0x8	(8D8			_								_		9.3.1		
Bit Name					\Box					_			\rightarrow	_	_		_		\rightarrow	_		\rightarrow	_	_	_		RE	$\overline{}$	$\overline{}$	MD	MD	_
Bit Val.	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	이	0	0	0	0	의	0	0		의	1	1	1	_0
Hex Val	04	(071	00	_	L 0.4	001	001		-00	401	<u> </u>		40	I	74				0		_			2	_	্ব	E		
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
																									-							
Reg. Na												con	f_lod	dal	-15	m _e 8	יםם: יםם:											-		9.3.1	150	-
Bit Name												0011	_,co	_00		(0.0	- -		\neg			\neg	\neg	\neg	\neg		RE	\dashv		MD		MD
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	Ω	0	0	0	0	0	0	0	0	0	0	o	0	0	0		o	1	1	1	0
Hex Val	_		_			(_		۳	- (0		Ĭ		(ď		0		-	- 0)	7	-'	Ë	- '	Ť
Bit Pos	31		29	28	27			24	23		21	20	19	18		16	15	14		12	11	10	9	8	7	- 6		4	3	2	- 1	_
Reg. Na												сог	f_loc	Lda	ta9	(0x8	C4)													9.3.1	1.50	
Bit Name	•																										RE		PD	MD	MD	MD
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	0
Hex Val		(((0				()			0					2			С	,	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
													, .			48.												_				
Reg. Na												cor	ıf_loo	<u>1_da</u>	ta8	(0x8	CO)										'	_		9.3.1		
Bit Name								_		_				_	_			_		_			_	_	_	_	RE	$\overline{}$	$\overline{}$	MD	$\overline{}$	_
Bit Val.	0			0	0	0	_	0	0	이		0	0	이	이	0	0	0		0	0	0	0	의	0	0		이	1	1	0	0
Hex Val		(J			(-		l					0	ı			(J			0				- 1)			C	,	
Bit Pos	0.4	00	29	00	27	00	0.5	0.7	23	001	0.4	20	19	401	471	16	15	44	13	12	11	10	9	- 81	7	- 6	- 5	4	3	2	1	0

Reg. Name											LCR	UAR	T (0x	C) (S	witc	h to	MOD	EA)												19.5.1	.13		
Bit Name																									DLE				NP	NBS	CL C	L	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	
Hex Val	_	0				0				0				0				()			0				8				3		П	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
																																\neg	
Reg. Name												DLL_	UAR	T (0x	00) (1	MOD	EA)													19.5.	1.3		
Bit Name																									LSB	LSB	LSB	LSB	LSB	LSB L	SB LS	SB	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	0	
Hex Val		0)			0	0			0				0	1			()			0				4	ļ			E			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Reg. Name									DLH				_UART (0x04) (N			MOE	EA)												19.5.1.7 MSEMSEMSEMSE				
Bit Name																							_	_				MSE	$\overline{}$	MSEN	NSE N	ISB	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Hex Val	L.,	0	_			0			0					0				(_			0					0			0			
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
																																_	
																														\perp		_	
Reg. Name	Ь.,			_			_			L	.CR_I	JART	T (0xC) (Switch t			to C	P M	ODE)) 										_	19.5.1	_	_	
Bit Name		_				_				_				_	_		_					_	\dashv	\rightarrow	DLE	_	_		$\overline{}$	NBS	_	L	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	
Hex Val	Ь.,	0	_			0				0	_			0				(_			0				0				3		_	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
																														-		_	
D N												ND4		10	۱۵۱ / ۱		1000	·c1												10.5.1	26	-	
Reg. Name									- 1		IVIL	NT_	UART	(UX2	(0)	ALL IV	IODE	:5)											_	19.5.1 MS N	NS N	40	
Bit Name	0		0	_	0	-	_		_	_	_		0	_		_	_	_			0			_	_	_			0	$\overline{}$	_	_	
Bit Val.	U	0		0	U	0	0	0	0	0	0	0	U	0	0	0	0	0	0	0	U	0	0	0	0	0	0	0	U	0	0	0	
Hex Val	31	30	_	28	0.7	26	25	24		0	_	20	19	18	17	16	15		_	12	11	10	_	_	7	0	5	_		2	-	_	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	1/	16	15	14	13	12	11	10	9	8		6	5	4	3		1	0	
																																_	
Reg. Name										E	CD I	IADT	(0x8	\ / N A /)DE	A or	OD 14	IODE	١									_	19.5.1.11				
Bit Name											<u></u>	MNI	(UX8) (IVIC	ושכו	H Of	OP IV		- <i>J</i>											TXC F		ENI	
Bit Val.	0	0	0	0	0	0	0	0	_	_	0	0	0	0	0	0	0	0	0	0	0	0	_	0	0	0	0	0	0	1	1	O	
Hex Val	U	0	-	U				U				-1 -1 -1									U	- - - - -											
Bit Pos	31	30	29	28	28 27 26 25 24			0 24 23 22 21 20 :			0 20 19 18 17 1			0 17 16 15 14 13 12				11	10	9	8	0 8 7 6 5 4					6 4 3 2 1 0						
BIT POS	31	30	29	28	2/	20	25	24	23	22	21	20	19	18	1/	TO	15	14	13	12	11	10	9	8	/	ь	5	4	3		1	0	

Reg. Name						e UART5 GPIOIN																										
Bit Name																		UAR	T5											(GPIO	INT
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Hex Val		0				0)			0				0				4				0				0				0		
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Name												IN	ITC_F	PEND	ING	IRQ	1															
Bit Name																		UAR	T5											(GPIO	INT
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0		0	0	0	0	0	0	0	0	0	0	0	0	1
Hex Val		0				0				0				0				0				0				0				1		
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
																														_		
					IIR UART (0x8) (MODE A or OP MODE)																				_		\perp					
Reg. Name										IIR_U		ART	(0x8)	(MC	DE A	orC	OP M	ODE)										_		19.5.		
Bit Name		_		_						_				_	_								_	_	_	-	_	_	_	IT_T I	_	T_P
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Hex Val		0	_			0	_			0	_			0				0				0	_			0				1		
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
																										_		-		\rightarrow		
Reg. Name												IED	UART	104/	1) /(0)	D MC)DE)											-		19.5.	16	
Bit Name									1			LEK_	UANI	(UX-	,, (r IVIC	100											-	MOD	_	THRIT	_
Bit Val.	0	0	0	0	0	0 0 0		0	0 0		0 0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0
Hex Val	-	0		·	U	0		U	U	0		·	U	0		·	U	0		U	- 0	0	U	٠	· ·	0	U	-	1	A		U
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
DICT 03	51	50	23	20	21	20	2.0	24	20	22	21	20	10	10	1,	10	10	14	10	12	-11	10		Ü	-	-		7	-			Ü
																										-	-	+	-	_	-	-
Dog Name										N 4 6	'D II	A D.T.	/0v16	2) / 0.4	ODE	۸ ۵۳	ODA	400	Ε\									-	щ.	19.5.1	1 21	
Reg. Name Bit Name										IVIS	K_U	AKI	(0x18	o) (IVI	ODE	A OI	OPI	VIODI	=)						$\overline{}$	$\overline{}$		TS	$\overline{}$	19.5.1	1.21	
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Hex Val	U		_	U	U	0		U	U	0	-	U	U	0		U	U	0	_	U	U	0	U	U	U	1	U	-	U	0	_	U
Bit Pos	31	0 31 30 29 28				26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
BIL POS	31	30	29	28	27	20	25	24	23	22	21	20	19	18	1/	10	13	14	13	12	11	10	9	ō	/	0	3	4	3		1	U
																												-		-		
Reg. Name										15	R U	ΔRT	0x14) (M	ODE	Δor	OP N	1ODF	١.										19.5.1.19			
Bit Name									П	٦			- I	, (<u> </u>						П			1	THR	\neg	Ť			
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
Hex Val		0	-			0			,	0				0			,	0	-			0	-		-	2	-1	-	_	0		
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	0.1	-						2.7				20										10	-		•	-		•	_		-	-

Reg. Name																		19.5	.1.6													
Bit Name																													MOI	D	THRI	Т
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Hex Val		0				()			0)			0				()			0				()			0)	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Name										LS	R U	ART (0x14) (M	ODE	A or	OP N	/ODE	Ξ)											19.5.	1.19	
Bit Name											_								İ								THR					
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
Hex Val		0)			()			0				0				()		0					2			0)	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Name												IER_	UART	(0x4	l) (O	P MC	DDE)													19.5	.1.6	
Bit Name																													MOI	D	THRI	Т
Bit Val.	0	0	0	0	0	0	0	0	0 0 0			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Hex Val		0)			()			0)			0	1			()			0				()		<u> </u>	0)	
Bit Pos	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Reg. Name													IN	TC_C	ontr	ol													6.6.1.6			
Bit Name																																NIRC
Bit Val.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0 0 1			1
Hex Val		0				()		0				0			0					0				()		1				
Bit Pos	31	30	29	28	28 27 26 25 2			24	 				19 18 17 16			5 15 14 13 12				11	10	9	8	7 6 5				3	0			