ARTEM KULAKEVICH

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EDUCATION

Master of Science Degree, Electrical Engineering GPA: 4.00 / 4.00 June 2019 - Present Portland, OR

Portland State University

Bachelor of Science Degree, Electrical Engineering GPA: 3.97 / 4.00 **September 2017 – June 2020** Summa cum laude Portland, OR

Portland State University

WORK EXPERIENCE

December 2016 - Present

Lake Oswego, OR

Production Specialist III

Micro Systems Engineering, Inc.

- Certified to run and troubleshoot at least 8 major production processes; more certifications than any other specialists on shift.
- Analyze electrical failure using tools like Eagle and Allegro and troubleshoot mechanical systems using knowledge conveyor systems and mechanical alignment. Repair at least 1 mechanical system or electrical fixture per week.
- Lead a team of 8 production specialists multiple days a week by designating tasks for each person and identifying stalls in the workflow. Deal with communication issues between engineering and production staff.

Module Imaging Cell Internship Project

Micro Systems Engineering, Inc.

September 2019 – January 2020 Lake Oswego, OR

- Updated and validated LabVIEW software used in automation of 3 different 4-axis production imaging robots that reduced chances of collision. Added data logging which is used to process 500 – 1000 medical devices daily.
- Modified an outdated workflow process for defibrillator product by retraining a 6-axis Epson robot. Reduced human handling and process time for 200 – 600 defibrillators per week.
- Improved documentation by updating 3 design documents, creating a new record document, and a new standard operating procedure document.

SKILLS

C++/CGit (Github) Verilog ARM Assembly ADC, SPI, I2C Embedded Rust LabVIEW 12.0 Linux (Ubuntu)

Oscilloscope **OCEAN Scripting** IDE Debugger Motor Control

PROJECTS

High Assurance Self Balancing Robot - Senior Capstone

Project Sponsor: Galois, Inc.

January 2020 - June 2020

Portland, OR

- Built and programmed a self-balancing robot to explore methods of control and verification. Modified a Rust compiler in a verification tool called Kind2 to generate embedded Rust code from a language called Lustre.
- Used Kind2 to generate embedded Rust PID and Fuzzy logic controllers from Lustre and found Rust PID to be within 5% of C++ controller for major characteristics. Sponsor's best performing project out of 3 different groups.

MIPS-lite Simulator April 2020 – June 2020

- Designed a 5-stage MIPS simulator in C++ with timing analysis, hazard mitigation, and forwarding.
- Tested the simulator with a given generic memory image. The simulator was able to perform data manipulation on memory image and produce results to terminal.

ARM Sitara AM335x 32-bit Processor

September 2019 – December 2019

- Utilized ARM assembly and C to program a BeagleBone Black board to communicate with an RC8660 talker board over UART and a NewHaven LCD over I2C.
- Used datasheets and pseudo code to identify and plan modifications for peripherals. Was able to complete match all specifications using interrupts and added supplementary features for extra credit.

RELEVANT COURSEWORK

CPU Architecture, Microprocessors 1 & 2, ASIC: Modeling, Verilog & FPGA Design, Formal Verification