ARTEM KULAKEVICH

Artkulak@gmail.com • **503-750-3225**
linkedin.com/in/artem-kulakevich/

EDUCATION

Master of Science, Electrical Engineering **GPA:** 4.00 **Jun 2019 – Jun 2021**
Portland State University

Bachelor of Science, Electrical Engineering **GPA:** 3.97 **Sep 2017 – Jun 2020**
Portland State University

WORK EXPERIENCE

Design & Dev. Engineer, *Micro Systems Engineering, Inc.* **Dec 2020 – Present**

- Equipment owner for 2 laser PCB depaneling systems that process all circuits in production. (1000+ medical implants per day)
- Led projects to replace $180K UV laser source and $80K automated conveyance on laser system.
- Leading equipment purchasing and integration project for 2 additional laser systems. ($770K investment)

Production Specialist, *Micro Systems Engineering, Inc.* **Dec 2016 – Dec 2020**

- Performed LabVIEW software updates for multiple automated imaging robots used to image the majority of production circuits. (1000+ medical implants per day)
- Rebuilt and rewired multiple automated imaging robots currently used in production.
- Programmed Epson 6-axis robot pick and place positions to introduce new products into the production line.

SKILLS

- C++ / C
- Embedded Rust
- LabVIEW 13.0
- ARM / RISC-V Assembly
- SystemVerilog
- Soldering

RELEVANT PROJECTS

High Assurance Self Balancing Robot **Jan 2020 – Jun 2020**

- Programmed a self-balancing robot to explore complex methods of control and formal verification.
- Altered Kind2 compiler to generate embedded Rust from Lustre; generated working PID and Fuzzy controller.
- Created a website with an SQL database to provide a live feed of robot sensor data.

RISC-V SoC FPGA Project **Oct 2020 – Dec 2020**

- Modified a RISC-V processor written in SystemVerilog to introduce VGA, I/O, and a microcontroller.
- Wrote assembly code used by the modified RISC-V processor.

ARM 32-bit Programming Project **Sep 2018 – Dec 2018**

- Utilized ARM assembly and C to program dev board to communicate with peripherals over UART and I2C.
- Used datasheets and pseudo code to identify and plan modifications for peripheral settings.