

# ARTEM KULAKEVICH

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## EDUCATION

<b>Master of Science, Electrical Engineering</b> <i>Portland State University</i>	GPA: <b>4.00 / 4.00</b>	<b>Jun 2019 – Present (Jun 2021)</b> Portland, OR
<b>Bachelor of Science, Electrical Engineering</b> <i>Portland State University</i>	GPA: <b>3.97 / 4.00</b> <i>Summa cum laude</i>	<b>Sep 2017 – Jun 2020</b> Portland, OR

## WORK EXPERIENCE

<b>Production Specialist III</b> <i>Micro Systems Engineering, Inc.</i>	<b>Dec 2016 – Present</b> Lake Oswego, OR
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- Run and troubleshoot 7 major production processes. Acquired more certifications than most production specialists. Perform scheduled preventative maintenance and calibration on equipment.
- Introduce regular document and workflow changes to two different production processes. Frequently maintain and repair module image cell robot.

## SKILLS

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|-----------------------|----------------|-----------------|
| • C++ / C             | • LabVIEW 12.0 | • SystemVerilog |
| • Embedded Rust       | • ARM Assembly | • Oscilloscopes |
| • ADC, SPI, I2C, JTAG | • Soldering    | • Git (Github)  |

## PROJECTS

<b>High Assurance Self Balancing Robot - Senior Capstone</b> <i>Project Sponsor: Galois, Inc.</i>	<b>Jan 2020 – Jun 2020</b> Portland, OR
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- Programmed a self-balancing robot to explore methods of control and verification. Modified a Rust compiler in a verification tool called Kind2 to generate embedded Rust code from Lustre.
- Used Kind2 to generate embedded Rust PID and Fuzzy logic controllers from Lustre and found Rust PID to be within 5% of C++ controller for major characteristics.

<b>Module Imaging Cell Project</b> <i>Micro Systems Engineering, Inc.</i>	<b>Sep 2019 – Jan 2020</b> Lake Oswego, OR
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- Updated and verified LabVIEW software used in automation of 3 different 4-axis production imaging robots that reduced chances of collision and improved data collection.
- Modified an outdated workflow process for defibrillator product by retraining a 6-axis Epson robot. Reduced human handling and process time for 200 – 600 defibrillators per week.
- Improved documentation by updating 3 documents, creating a new record (GTR) document, and creating a new standard operating procedure (SOP) document.

<b>MIPS-lite Simulator</b>	<b>Apr 2020 – Jun 2020</b>
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- Designed a 5-stage MIPS simulator in C++ with timing analysis, hazard mitigation, and forwarding.
- Evaluated the simulator with a provided memory image. The simulator was able to perform data manipulation on the memory image and produce results to a terminal.

<b>ARM Sitara AM335x 32-bit Processor</b>	<b>Sep 2019 – Dec 2019</b>
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- Utilized ARM bare metal assembly and C to program a BeagleBone Black board to communicate with an RC8660 talker board over UART and a NewHaven LCD over I2C.
- Used datasheets and pseudo code to identify and plan modifications for peripherals. Implemented all necessary features using interrupts.

## RELEVANT COURSEWORK

Computer Architecture • **Microprocessors 1 & 2** • ASIC: Modeling & Synthesis • **Digital Integrated Circuits Design** • Analog Integrated Circuit Design • **Formal Verification of HW / SW**