

Artem Kulakevich

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Education

Sep 2017 | **BS in Electrical Engineering**, *Portland State University*, GPA: 3.97, *Portland, OR*.
Jun 2020
Jun 2019 | **MS in Digital IC Design**, *Portland State University*, GPA: 4.0, .
Jun 2022

Work Experience

Apr 2017 – **Production Specialist III**, *Micro Systems Engineering Inc.*, Lake Oswego, OR.
Present

- Trained on and run the majority of production processes on production floor, progressed towards troubleshooting mechanical and software errors.
- Worked on team with engineers to rebuild improve an automated imaging cell used in production for thousands of modules daily.
- Particed in engineering group to work on config and software updates to Epson cell to improve robot efficiency and to solve factory bottleneck.

Skills

Programming SystemVerilog, Arm Assembly, Labview, C++
Hardware Soldering, Electrical Wiring, Oscilloscope, DMM, Transistor Biasing
General Excel, Word, PowerPoint, LTSpice, Jira, LaTeX, SAP BusinessObjects

Projects

Module Imaging Cell, *LabView, Excel / Word*.

Rebuilt multiple 4-Axis robots based on BOM, retaught robots for production, created documentation for teaching for robot in the future. Continue to maintain robots and make improvements in production.

ASIC Design Projects, *SystemVerilog, Design Compiler, Git, Linux*.

Programmed multiple ASIC projects including FIFO, counters, traffic lights, and then synthesized the projects for comparison with simulation.

ARM Sitara AM335x UART / I2C, *ARM Assembly, C*.

Programmed BeagleBone Black boards to communicated with a RC8660 talker boards and NewHaven LCD using barebone assembly.

Buck Converter, Oscilloscope, Matlab.

Built buck converter design, tested the design, and then improved the design by changing the compensator stage using bode plot analysis.