Artem Kulakevich

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Education

Jun 2019 – Present
Master of Science, Electrical Engineering, *Portland State University*, *Portland, OR.*
GPA: 4.00, *Expected: Jun 2021*

Sep 2017 – Jun 2020
Bachelor of Science, Electrical Engineering, *Portland State University*, *Portland, OR.*
GPA: 3.97

Work Experience

Dec 2016 – Present
Production Specialist III, *Micro Systems Engineering Inc.*, Lake Oswego, OR.
- Certified for all back-end production processes; more certifications than anyone in area.
- Team lead for back-end tasks, designated to assign work, and resolve communication issues.
- Introduce new production processes, workflow changes, and training through various engineering projects.
- Work on LabVIEW software changes for production imaging cell, including documentation and code reviews.

Skills

Languages C++, C, Rust, LabVIEW 12.0, ARM Assembly, Matlab, SystemVerilog

Programs Git, Linux (Ubuntu), Windows, LTspice, Cadence Virtuoso, Visual Studio, MS Office, SAP

Hardware Soldering, Oscilloscope (Tektronix/Rigol), Function Generator (Tektronix), Power Supply

Projects

Jan 2020 – Jun 2020
Senior Capstone | Galois Inc., *Rust, C++, Arduino, Kind2, Lustre, PHP, SQL, Apache2.*
Modified Kind2 Lustre to Rust compiler to generate embedded Rust code from a verifiable language. Streamlined the process of creating verifiable embedded controllers. Found Rust PID controller to have identical real-world performance to controller written in C++. Wrote and tested a Fuzzy control on a real-world system.

Oct 2019 – Jan 2020
Module Imaging Cell | MSEI, *LabVIEW 12.0, log4net.*
Implemented software changes to automated production cell software. Introduced log4net logging to SQL, data collection to a digital factory, and changes to the state machine that reduced chances of collision and product loss.

Sep 2019 – Feb 2020
CMOS Standard Library Design | ECE 526, *Virtuoso 6.1.8, ADE, OCEAN/SKILL.*
Designed standard library components using Cadence Virtuoso layout and ADE tools. Wrote scripts to simulate and measure output values with different temperatures, inputs voltages, and input rise times.

Sep 2019 – Dec 2020
Interactive Cube | ECE 411, *C++, Soldering, MS Project, Git.*
Worked with a team to design a PCB for ATmega328p with necessary bypass, addressable LEDs, and IMU. Personally programmed processor to control LEDs using physical movement.

Sep 2019 – Dec 2020
Class AB Audio Amplifier | ECE 521, *LTspice, Soldering, Oscilloscope.*
Designed a complimentary symmetry audio amplifier using mostly discrete BJTs to drive a 10W speaker. Soldered, designed and tested using homelab equipment.

Jun 2019 – Sep 2019
ASIC Design | ECE 581, *SystemVerilog, Design Compiler, Git, Linux.*
Programmed multiple Verilog designs including FIFO, counters, and traffic lights. Synthesized the projects for comparison with simulation.

Sep 2018 – Feb 2019
ARM Sitara AM335x UART / I2C | ECE 372, *ARM Assembly, C.*
Programmed BeagleBone Black to communicate with a RC8660 talker boards and NewHaven LCD.