RFLAGS register

The RFLAGS register consists of 64 bits. Some of the bits are used in user mode to signify the results of arithmetic, logical and compare instructions:

Name	Symbol	Use
carry	**CF**	**Set if the previous instruction had a carry**
parity	**PF**	**Set if the last byte produced in a previous instruction has an even number of 1's**
adjust	AF	Used in BCD instructions
zero	**ZF**	**Set if the previous instruction resulted in zero**
sign	**SF**	**Set if the previous instruction resulted in 1 in the most significant byte.**
direction	DF	Used to specify direction for some string instructions
overflow	**OF**	**Set if the previous instruction resulted in overflow**