蘇正耀、蔡明忠

NVIDIA Joint-Lab: NCHC Tech Sharing Workshop, July 6, 2021

Homomorphic Encryption

Homomorphic Encryption (HE) is commonly known as the "Holy Grail of Encryption", which is a method that permits users to perform computations on encrypted data without first decrypting it. The result of computation remains in encrypted form, and can be revealed by the owner of the secret key.

Current Schemes

HE in Latticed-based Cryptosystems, adopted by DARPA, IBM, Microsoft,

Given a quantum code [n, k, C], n > k, and a pair of keys

$$\begin{cases} \textbf{PublicKey} = (Q_{en}, \ E) & Q_{en}: \ \text{An n-qubit encoding operator} & \textit{$M:$ The k-qubit arithmetic operation} \\ \textbf{PrivateKey} = \textbf{A}^{\dagger}P^{\dagger} & E: \ \textbf{A correctable error set} & \textit{$P,$ A: n-qubit operators of mix-up} \\ |\textbf{x}\rangle & \xrightarrow{\textbf{Encryption}} |\textbf{c}\rangle = EQ_{en}|\textbf{0}\rangle\otimes|\textbf{x}\rangle & \xrightarrow{\textbf{Computation}} U_{en}|\textbf{c}\rangle & \xrightarrow{\textbf{Decryption}} & \textbf{A}^{\dagger}P^{\dagger}U_{en}|\textbf{c}\rangle \\ |\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle & \xrightarrow{\textbf{Evaluation}} V_{en}|\textbf{c}\rangle & \xrightarrow{\textbf{A}^{\dagger}P^{\dagger}} & =|\textbf{0}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle & \text{A}^{\dagger}P^{\dagger}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle & \text{A}^{\dagger}P^{\dagger}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle & \text{A}^{\dagger}P^{\dagger}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes|\textbf{a}\rangle\otimes$$

Code - based Cryptography (a.k.a. **Post - Quantum**)

 $E \in \mathbf{E}$

Given a linear code [n, k], n > k, and a pair of keys

Given a quantum code [n, k, C], n > k, and a pair of keys

PublicKey =
$$(Q_{en}, E)$$
PrivateKey = $A^{\dagger}P^{\dagger}$

 Q_{en} : An *n*-qubit encoding operator

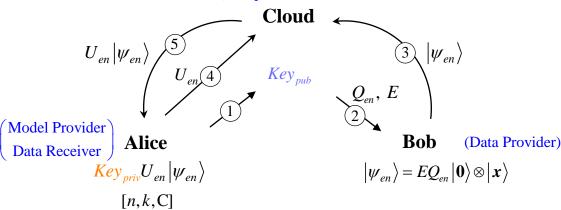
E: A correctable error set

M: The k-qubit arithmetic operation

P, A: n-qubit operators of mix-up

$$\begin{vmatrix} \boldsymbol{x} \rangle & \xrightarrow{\text{Encryption}} & |\boldsymbol{c}\rangle = EQ_{en} |\boldsymbol{0}\rangle \otimes |\boldsymbol{x}\rangle & \xrightarrow{\text{Computation}} & U_{en} |\boldsymbol{c}\rangle & \xrightarrow{\text{Decryption}} & A^{\dagger}P^{\dagger}U_{en} |\boldsymbol{c}\rangle \\ k\text{-qubit plaintext} & n\text{-qubit ciphertext} & M |\boldsymbol{x}\rangle & \\ E \in \mathbf{E} & M |\boldsymbol{x}\rangle & K = \mathbf{E}$$

(Computation Provider)



- 1 Alice generates the public-key $Key_{pub} = (Q_{en}, E)$ and the private key $Key_{priv} = A^{\dagger}P^{\dagger}$
- (2) Bob obtains $|\psi_{en}\rangle = EQ_{en}|0\rangle \otimes |x\rangle$ via Key_{nub}
- 3 Bob sends $|\psi_{en}\rangle$ to the cloud

- (4) Alice sends the computation instruction of $U_{ed} = PAM \ Q^{\dagger} \ (mix-up)$
- (5) Alice acquires the evaluation $U_{en}|\psi_{en}\rangle$ from the cloud, and the recovers $A^{\dagger}P^{\dagger}U_{en}|\psi_{en}\rangle$ via decryption

Given a quantum code [n, k, C], n > k, and a pair of keys

PublicKey =
$$(Q_{en}, E)$$
PrivateKey = $A^{\dagger}P^{\dagger}$

$$|x\rangle \xrightarrow{\text{Encryption}} |c\rangle = EQ_{en} |\mathbf{0}\rangle \otimes |x\rangle \xrightarrow{\text{Computation}} U_{en} |c\rangle \xrightarrow{\text{Decryption}} A^{\dagger}P^{\dagger}U_{en} |c\rangle$$

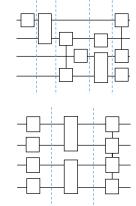
$$|x\rangle \xrightarrow{\text{Evaluation of}} U_{en} |c\rangle \xrightarrow{\text{Evaluation of}} U_{en} |c\rangle \xrightarrow{\text{Evaluation of}} A^{\dagger}P^{\dagger}U_{en} |c\rangle$$

$$U_{en} = PA \quad M \quad B \quad Q_{en}^{\dagger}$$

$$= \begin{array}{c} -S_{a_1}^{\epsilon_1} - \\ \vdots \\ -S_{a_n}^{\epsilon_n} - \end{array}$$

$$M = I \otimes M$$

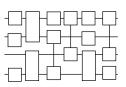
$$= (PV^{\dagger}P^{\dagger})(PV \text{ A } \mathbf{M} V^{\dagger}P^{\dagger})(PV \text{ B } V^{\dagger}P^{\dagger})(PV \text{ } Q_{en}^{\dagger} V^{\dagger}P^{\dagger})(PV)$$

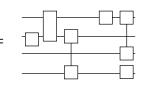


Exact computation

Blind computation

Problem - Dependent Optimizations of Circuits





Hamming Code

[5, 2]

The generator matrix

$$G = \begin{pmatrix} 1 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 \end{pmatrix}.$$

The parity-check matrix

$$H = \begin{pmatrix} 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 \end{pmatrix}.$$

$$GH^{T} = \begin{pmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \end{pmatrix}$$

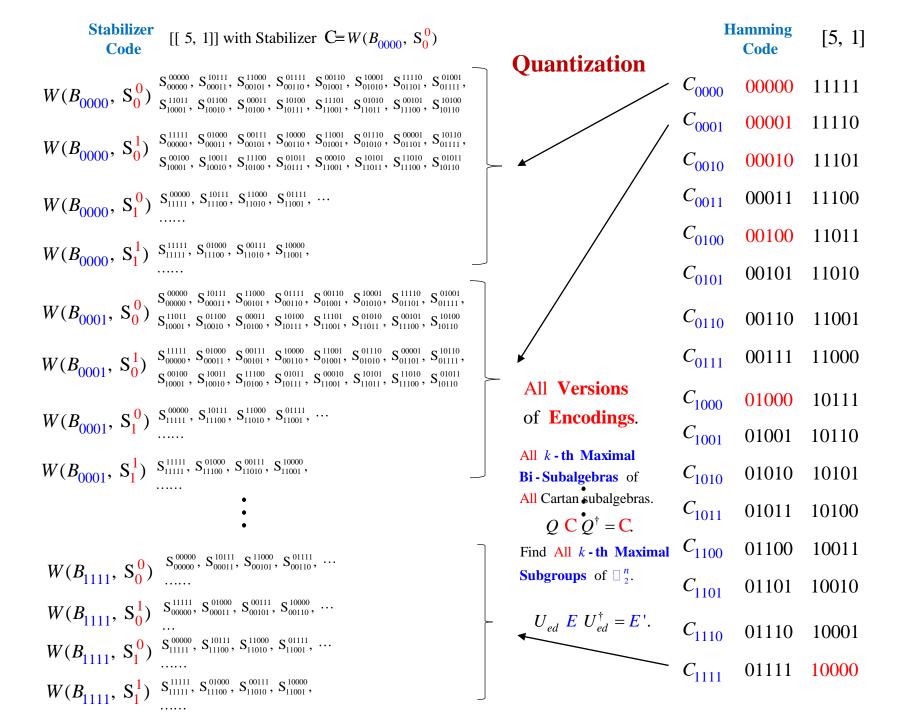
The dual code [5, 3]

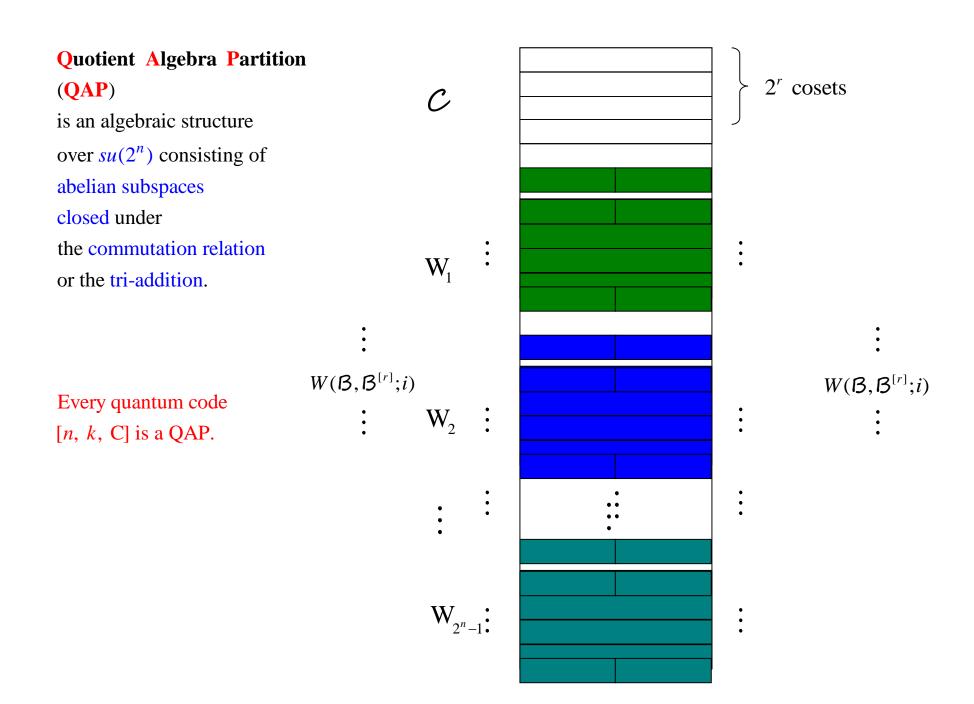
$$\mathbf{C}^{\perp} = \{ 00000, 10100, 11010, 01001, 01110, 11101, 10011, 00111 \}.$$

The relation between \mathbb{C} and \mathbb{C}^{\perp} :

$$\forall u \in \mathbb{C}, \ w \in \mathbb{C}^{\perp}, \ \langle u | w \rangle = 0,$$

$$H|u\rangle = \begin{pmatrix} 0 \\ 0 \\ 0 \end{pmatrix}$$
 and $G|w\rangle = \begin{pmatrix} 0 \\ 0 \\ 0 \end{pmatrix}$.





Quantum Adder

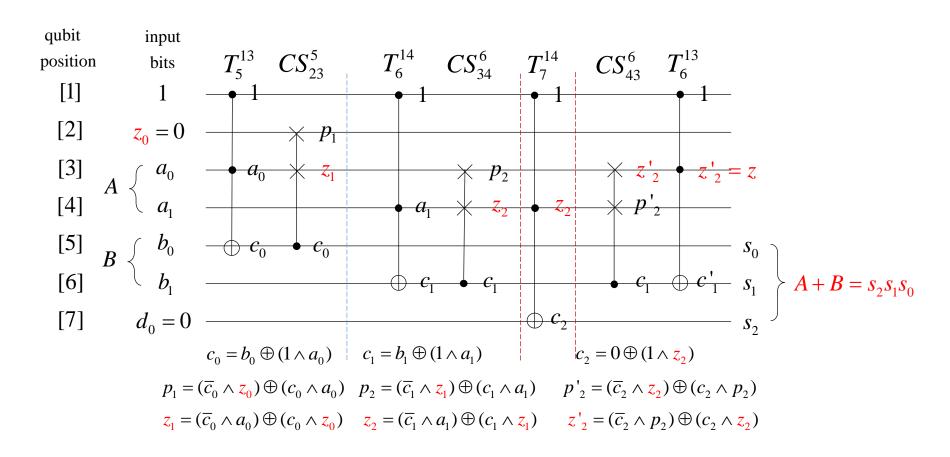
2-bit adder: given $A = a_1 a_0$ and $B = b_1 b_0 \in \mathbb{Z}_2^2 = \{00, 01, 10, 11\},\$

1. Prepare the 7-qubit basis state
$$|1 z_0\rangle |a_0a_1\rangle |b_0b_1\rangle |d_0\rangle$$
, $z_0=0=d_0$

2. Apply the circuit
$$U_{\text{Add}}^{(2)} \begin{vmatrix} 1 & z_0 \end{vmatrix} |a_0 a_1\rangle |b_0 b_1\rangle |d_0\rangle = \begin{vmatrix} 1 & z \end{vmatrix} |a_0 a_1\rangle |s_0 s_1 s_2\rangle$$

$$A + B = s_2 s_1 s_0$$

$$\begin{array}{ccc}
A & - & - & - & A \\
B & - & - & - & A + B
\end{array}$$



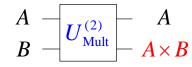
Quantum Multiplier

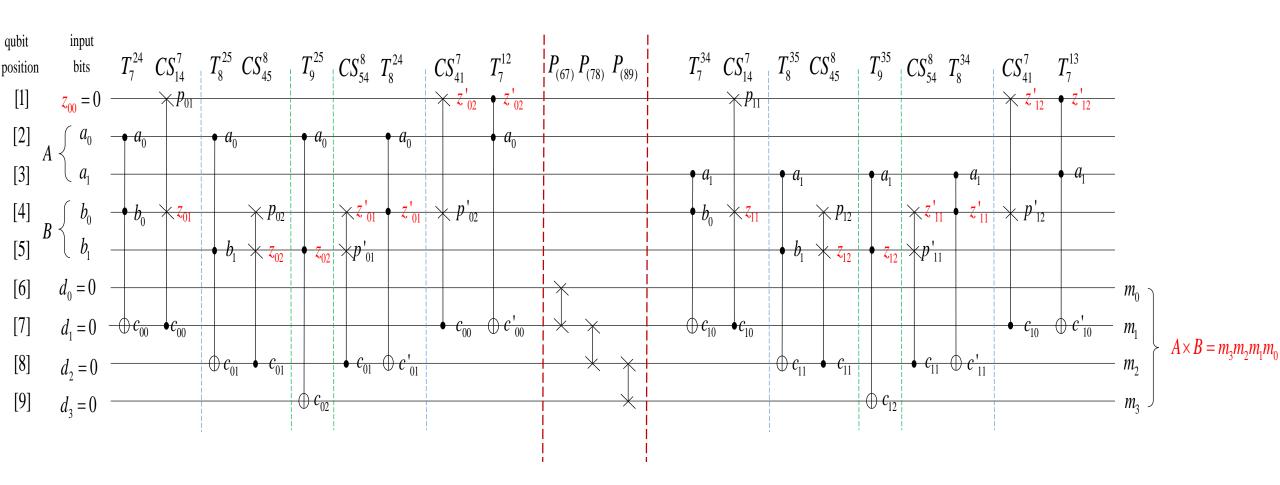
2-bit Multiplier: given $A = a_1 a_0$ and $B = b_1 b_0 \in \mathbb{Z}_2^2 = \{00, 01, 10, 11\},$

1. Prepare the 9-qubit basis state $|\psi\rangle = |0\rangle |a_0 a_1\rangle |b_0 b_1\rangle |0000\rangle$

2. Compute $U_{\text{Mult}}^{(2)} |\psi\rangle = |z_0\rangle |a_1 a_0\rangle |b_1 b_0\rangle |m_0 m_1 m_2 m_3\rangle$

 $A \times B = m_3 m_2 m_1 m_0$





Runtimes of Basic Gates on codewords of length n

	$n = 10^4$	$n=10^5$	$n=10^6$	$n=10^7$	$n=10^8$
$\mathbf{S}_{lpha}^{\;arsigma}$	$2 \times 10^{-4} \text{ s}$	2×10^{-3} s	$3.6 \times 10^{-2} \text{ s}$	2×10^{-1} s	2.1 s
$C_j^{\ i}$	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s
$P_{(i \ j)}$	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s
$T_{\ l}^{i\ j}$	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s
CS_l^{ij}	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s	10^{-8} s
$\Lambda_{n-1}^1(S_\omega^\pi)$	10^{-4} s	$8 \times 10^{-4} \text{ s}$	$8.5 \times 10^{-3} \text{ s}$	$8 \times 10^{-2} \text{ s}$	0.8 s

Hardware: Intel core i7-8565U CPU @ 1.80 GHz

Runtimes of the Adder $U_{\text{Add}}^{(l)}$ and the Multiplier $U_{\text{Mult}}^{(l)}$ given two *l*-bit integers

$$l = 8$$
 $l = 16$ $l = 32$ $l = 64$ $l = 128$

Helib_{Add}
$$2.3 \times 10^{-3}$$
 $4.2 \times 10^{-2} s$

$$U_{\text{Add}}^{(l)}$$
 1.08×10⁻³ s 1.8×10⁻³ s 2.2×10⁻³ s 4×10⁻³ s 5.8×10⁻³ s

$$Helib_{Multi}$$
 4.17×10⁻³ 5.82×10⁻²

$$U_{\text{Mult}}^{(l)}$$
 10⁻³ 2.8×10⁻³ 2.9×10⁻³ 3×10⁻³ 6×10⁻³

Hardware: Intel core i7-8565U CPU @ 1.80 GHz

Hardware: Intel Core i7 4790 @ 3.60 GHz

J.-W. Chen, et. al., Faster Binary Arithmetic Operations on Encrypted Integers, WCSE'17

security level = 128 size of plaintext x = 64 bits $\approx 10^{19}$ digits

HE computation	x^2	$oldsymbol{x}^4$	x^8	
HElib	$0.09 \ s_{30}$	$0.4 \ s_{100}$	$0.9 \ s_{112}$	
SEAL	$0.4 \ s_{133}$	1.5 s ₃₇₅	1.5 s ₁₈₇	
FV-NFLib	$0.08 \ s_{27}$	$0.09 \ s_{23}$	$0.3 \ s_{37}$	
QAPHE	0.003 s	0.004 s	$0.008 \ s$	

Hardware: Intel core i7-8565U CPU @ 1.80 GHz

Hardware: Intel (R) Xeon (R) CPU E5-2695 v3 @ 2.30 GHz

C. A. Melchor, et.al., A Comparison of the Homomorphic Encryption Libraries HElib, SEAL and FV-NFLlib, SecITC 2018, 425–442, 2018.

security level = 128 size of plaintext x = 256 bits $\approx 10^{76}$ digits

HE computation	\boldsymbol{x}^2	$oldsymbol{x}^4$	x^8	
HElib	1.5 s	2 s	7 s 58	
SEAL	2 s 250	9 s ₃₁₀	70 s ₅₈₃	
FV-NFLib	0.6 s	$0.9 \ s$	4 s 33	
QAPHE	$0.008 \ s$	0.029 s	0.12 s	

Hardware: Intel core i7-8565U CPU @ 1.80 GHz

Hardware: Intel (R) Xeon (R) CPU E5-2695 v3 @ 2.30 GHz

C. A. Melchor, et.al., A Comparison of the Homomorphic Encryption Libraries HElib, SEAL and FV-NFLlib, SecITC 2018, 425–442, 2018.

security level = 128 size of plaintext x = 2048 bits $\approx 10^{146}$ digits

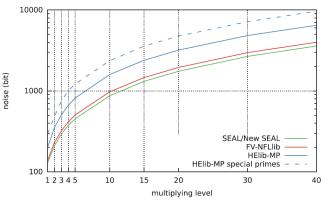
HE computation	x^2	x^4	\boldsymbol{x}^8
HElib	50 s	200 s	500 s
SEAL	N/A	N/A	N/A
FV-NFLib	80 s	550 s ₂₅₀	$800 \ s_{102}$
QAPHE	0.4 s	2.2 s	7.8 s

Hardware: Intel core i7-8565U CPU @ 1.80 GHz

Hardware: Intel (R) Xeon (R) CPU E5-2695 v3 @ 2.30 GHz

C. A. Melchor, et.al., A Comparison of the Homomorphic Encryption Libraries HElib, SEAL and FV-NFLlib, SecITC 2018, 425–442, 2018.

In current schemes, the computation time increases rapidly due to noise reduction.



C. A. Melchor, et.al., SecITC 2018, 425-442, 2018

In QAPHE, the computation time grows linearly with the number of basic gates applied.

size of plaintext $x = 2048$ bits	\boldsymbol{x}^2	\boldsymbol{x}^4	\boldsymbol{x}^8	
Toffli gates	8.3×10^6	2.5×10^{7}	4.2×10^{7}	
Ctrl. SWAPs	8.3×10^6	2.5×10^{7}	4.2×10^{7}	
CNOTs \ SWAPs	4×10^{3}	6.1×10^3	1.4×10^4	

QAPHE shows strong advantages with problems of **High Complexity** and **Large Size**.

Current and Future Work

Parallelism of Gates and Circuits

One-Way Functions of Gates and Circuits

Problem-Dependent Optimization of Circuits

Dynamic and Miniature Modularization in Chips

Special-Purpose Chips, Architectures, Machines for QAPHE