Release Notes for AT32F421 Firmware Library

V2.1.7-2024/08/08

1. Updated core DSP library.

2. Updated I2C eeprom demo and added multi-page write logic.

3. Optimized PWR driver and demo.

4. Updated some notes.

V2.1.7-2024/08/08

1. Updated the "i2c_flag_clear" and "i2c_init" functions.

2. Updated initialization configuration process of some I^{2}C DMA related demos.

3. Updated configuration process of PWC low-power mode related demos.

4. Fixed the AC6 compilation problems of some demos.

5. Updated some notes.

V2.1.6-2024/01/26

1. Added comparator output parameter definition.

2. Updated some notes.

V2.1.5-2024/01/05

1. Updated the counter counting mode in the input_capture demo timer.

2. Updated the systick initialization function in the systick interrupt demo.

3. Updated some pin configuration in ADC demo.

4. Updated the method on how to call the xx_interrupt_flag_get function in all demos.

V2.1.4-2023/10/26

1. Updated some notes.

2. Refer to driver release notes to obtain details on driver update.

V2.1.3-2023/08/04

1. Added AT32IDE project to demons under utilities.

2. Added CRC polynomial support.

3. Improved redirecting compatibility whiles using printf in IAR9.

4. Added macro definitions regarding Flash size and Flash demo.

5. Updated related notes.

V2.1.2-2023/02/16

1. Fixed AC6 compilation problems in freertos demo in utilities.

2. Added LEXT_VALUE macro definition in at32f421_conf.h.

3. Modified the location of crm_reset in system_clock_config function to avoid any error during system clock switch.

4. Updated some notes.

V2.1.1-2022/11/18

1. Deleted the version and date entries from the header of each file.

2. Added *Release Notes_Drivers* to record the updates of drivers separately, which is located under *libraries\drivers*.

3. Added repeat_conversion_loop_transfer demo to ADC to allow DMA to obtain ADC data in a circular way.

V2.1.0-2022/10/28

1. Added AT32F4212C8T7

2. Updated IAP_Programmer.exe.

3. Added interrupt enable determination to the flag bit detection for the interrupt function of USART interrupt demo.

V2.0.9-2022/09/28

1. Updated I2C interrupts and changed DMA demo to non-blocking.

2. Isolated macros for peripherals according to the specific model.

3. Updated ERTC code to optimize the write protection unlock process.

V2.0.8-2022/08/16

1. Updated the I2C eeprom demo with additional 16-bit address support.

2. Added IAR V9.3 project demo under template.

3. Fixed the problem of printf's failure to output in gcc environment.

4. Startup file supports configuration wizard.

V2.0.7-2022/06/28

1. Added a demo on how to enable Flash access protection.

2. Added IO toggle demo.

3. Updated some demos, their drive processes, and some notes.

V2.0.6-2022/05/20

1. Updated some demos, their drive processes, and some notes.

V2.0.5-2022/04/02

1. Added wdt_standby demo.

2. Introduced DSP-related source code, and added cmsis_dsp demo.

3. Updated some demos, their drive processes, and some notes.

V2.0.4-2021/02/11

1. Updated some demos, their drive processes, and some notes

V2.0.3-2021/12/31

1. Revised wrong parameter definitions in the interrupt priority group.

2. Integrated the serial port initialization and redirection functions of printf into xx_board.c, and removed related contents in demo.

3. Updated some demo to support AC6 and –O3 compilation.

V2.0.3-2021/12/17

1. Revised flag clear functions of peripherals in order to avoid inappropriate bit operations.
2. Updated pwc_wakeup_pin_enable function interface.
3. Updated timer frequency configuration process for triggering ADC conversions in ADC demo.

V2.0.1-2021/11/26

1. Updated some notes and readme.
2. Updated demos related to Deepsleep mode, and revised code procedure of entering Deepsleep mode.
3. Added SCFG PA11_PA12 remap demo.
4. Added USART Rx Tx swap demo.
5. Changed the name of GPIO swjtag_remap demo to swd_mux.
6. Updated pwc_wakeup_pinx_enable and pin-related functions to enable pin functions to call the same interface pwc_wakeup_pin_enable.

V2.0.0-2021/11/12

1. Initial release of AT32F421 firmware library.