Release Notes for
AT32F421 Firmware Library Drivers

V2.0.6-2024/12/13

1. Updated some notes.

V2.0.5-2024/08/08

1. Updated the "i2c_flag_clear" and "i2c_init" functions.

2. Updated some notes.

V2.0.4-2024/01/05

1. Fixed the IAR7.4 compilation warning issue of some drivers.

V2.0.3-2023/10/26

1. Added the function interrupt_flag_get to each of the IP sections.

2. Updated system clock switching procedure.

3. Updated ADC drivers.

4. Updated IRTMR drivers.

V2.0.2-2023/08/04

1. Added CRC polynomial support.

2. Updated the process of judging "sector" conditions in the flash_slib_enable.

3. Updated the process of the adc_ordinary_channel_set and adc_preempt_channel_set.

4. Updated relevant notes.

V2.0.1-2023/02/16

1. Updated the parameter type of cmp_scal_brg_config.

V2.0.0-2022/11/18

1. The CRM_HICK48_NODIV is considered the default setting when HICK is

used as PLL clock source.