

Release Notes for AT32F435_437 Firmware Library Drivers

V2.0.6-2024/12/13

1. Updated CAN driver.
2. Updated USB driver.
3. Optimized PWR driver.
4. Updated some notes.

V2.0.5-2024/08/08

1. Updated some notes.

V2.0.4-2024/01/05

1. Fixed IAR7.4 compilation warning issue of some drivers.
2. Optimized EMAC PTP driver.
3. Updated the “dvp_flag_get” and “dvp_interrupt_flag_get” function.

V2.0.3-2023/10/26

1. Added the function interrupt_flag_get to each of the IP sections
2. Optimized system clock switching procedure
3. Updated IRTMR drivers
4. Updated ADC drivers to improve soft trigger process
5. Updated some notes

V2.0.2-2023/08/04

1. Updated qspi_xip_enable
2. Updated the process of judging sector conditions in flash_slb_enable
3. Added CRC polynomial support
4. Updated the process of the adc_ordinary_channel_set and adc_preempt_channel_set
5. Updated relevant notes

V2.0.1-2023/02/16

1. Adjusted the emulation location of CRM emac lowpower to AT32F437

V2.0.0-2022/11/18

1. Updated xmc_ext_timing_config
2. The CRM_HICK48_NODIV is considered the default setting when HICK is used as PLL clock source