

LSV PA1

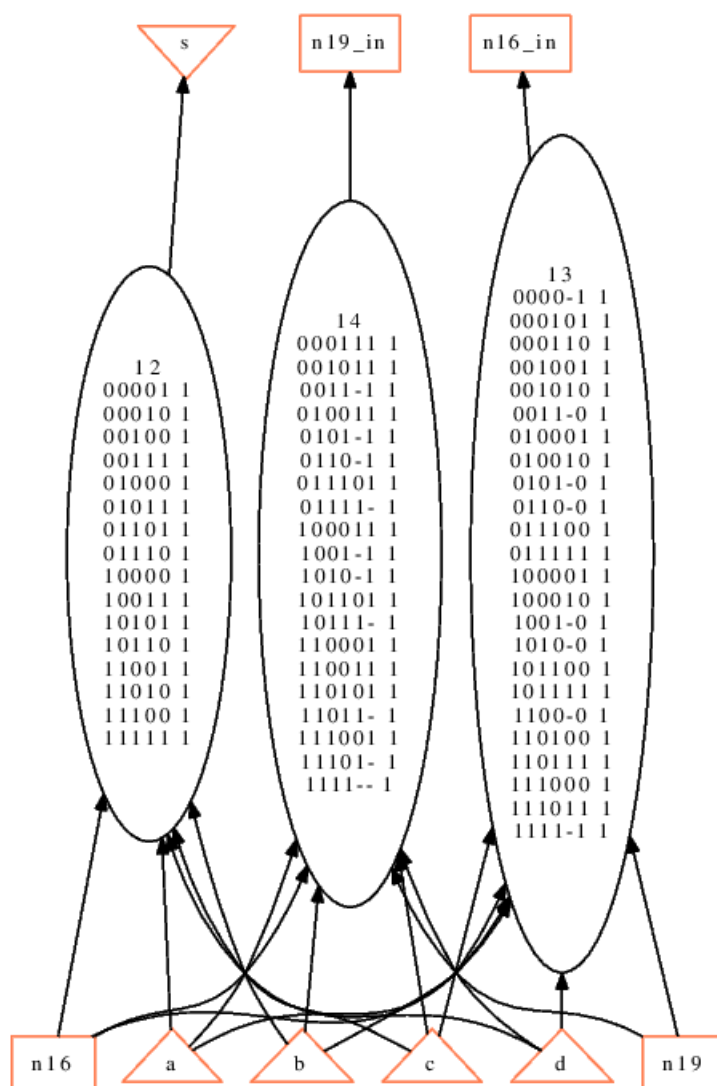
B06901079 張喬善

Results of 'show'

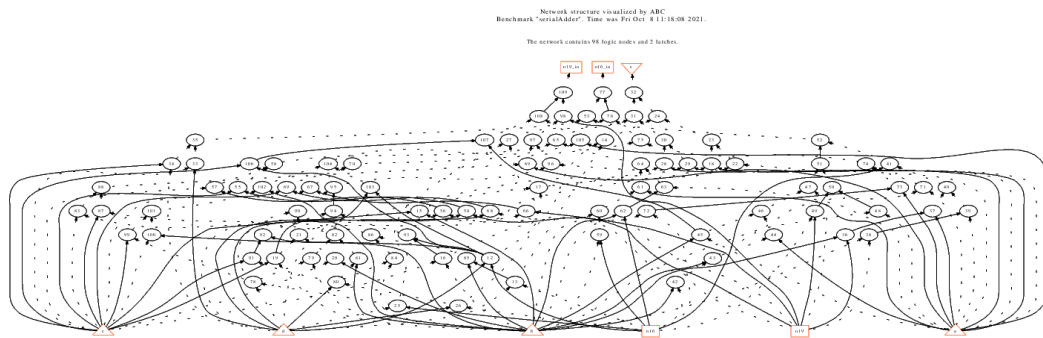
(3)

Network structure visualized by ABC
Benchmark "serialAdder". Time was Fri Oct 8 11:16:16 2021.

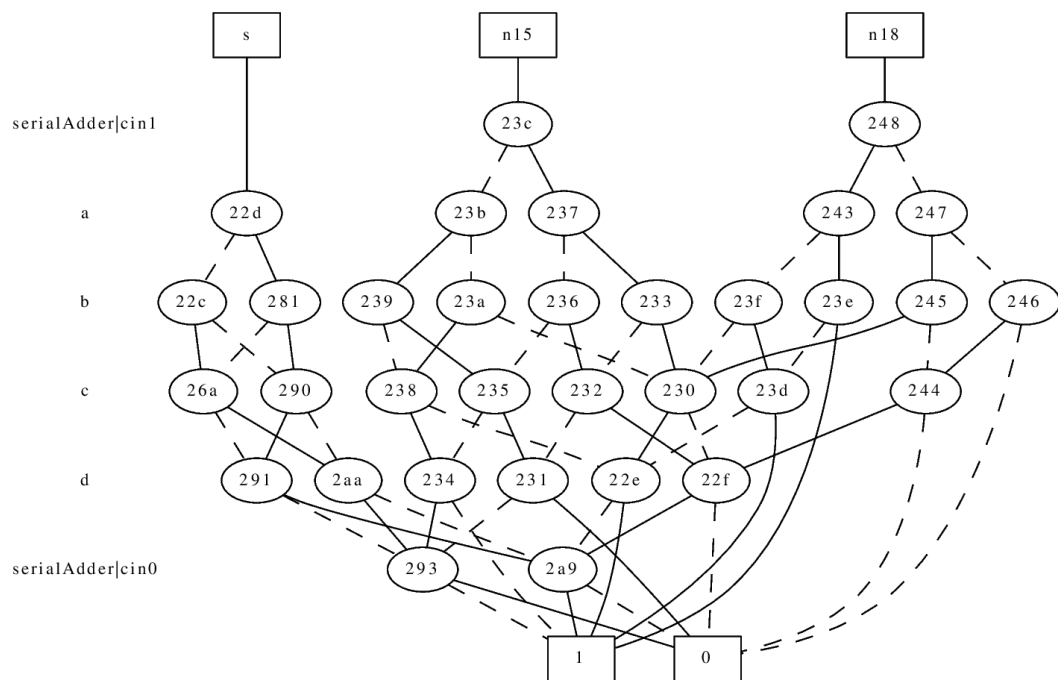
The network contains 3 logic nodes and 2 latches.



(5)



(7)



Answers

(a)

'aig' and 'bdd' will turn the internal circuit into corresponding form.

'strash' and 'collapse' will turn whole of the circuit into corresponding form.

(b)

logic