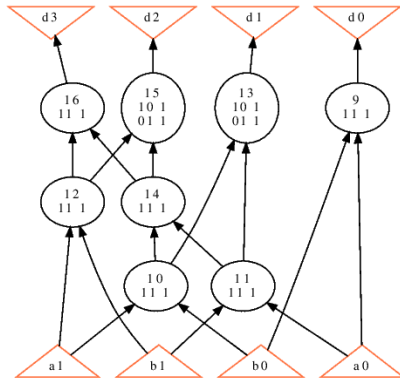


2-b.

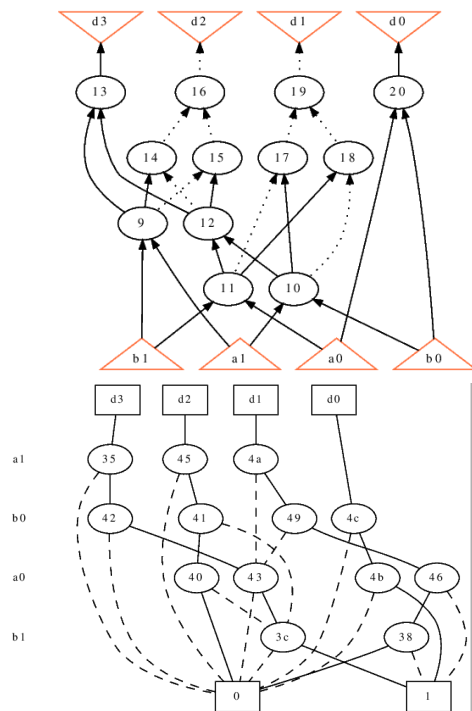
```
abc 01> read mul.blif
Hierarchy reader flattened 2 instances of logic boxes and left 0 black boxes.
```

```
abc 02> print_stats
mul      : i/o = 4/ 4 lat = 0 nd = 8 edge = 16 cube = 10 lev = 3
```

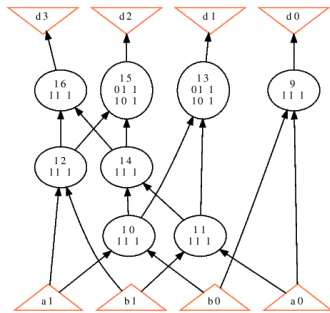


Network structure visualized by ABC  
Benchmark "mul". Time was Thu Sep 14 19:44:40 2023.

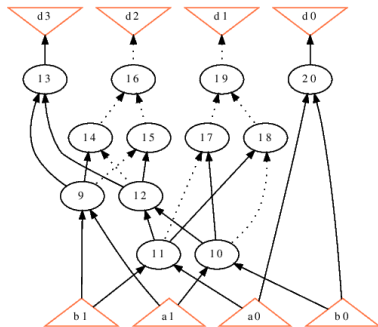
The network contains 12 logic nodes and 0 latches.



3-a-1 .



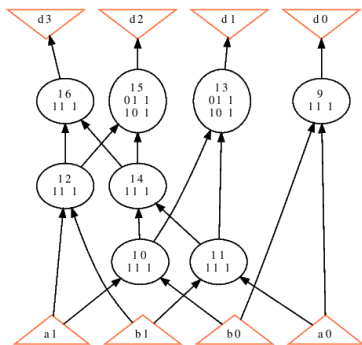
Command “aig”



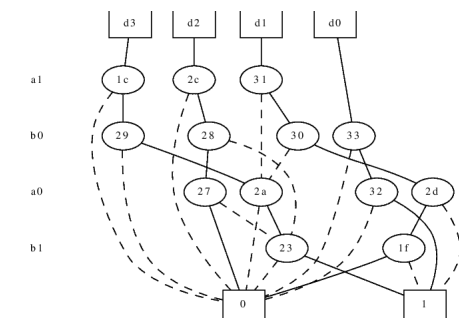
Command “strash”

Command “aig” didn’t change the layout of the graph, because it only modifies the representation inside each node.

3-a-2.



Command “bdd”



Command “collapse”

Similar as 3-a-1.

Command “bdd” didn’t change the layout of the graph, it only modifies the representation inside each node.

3-b.

renode->sop

