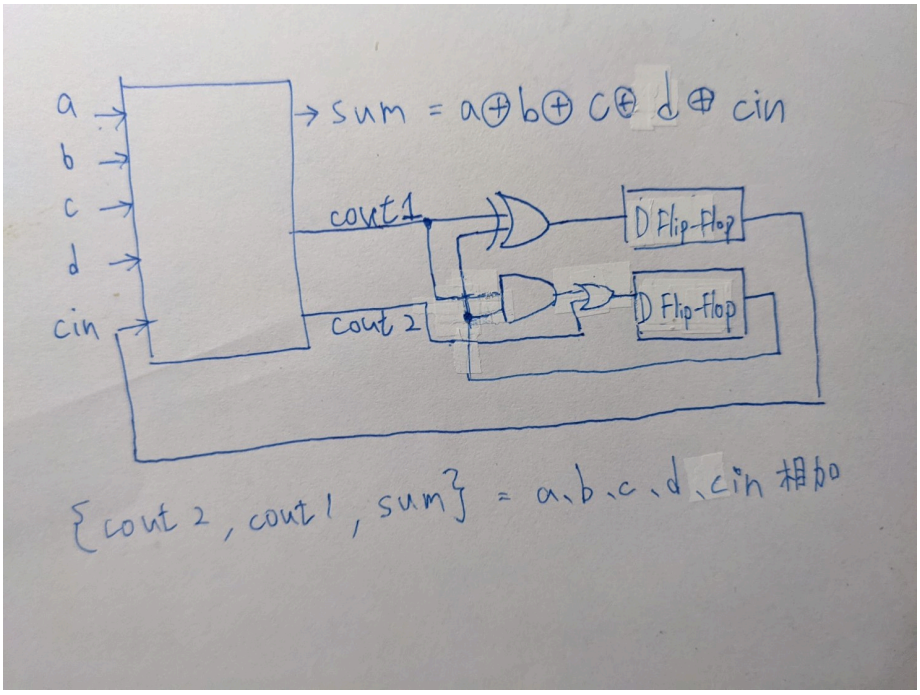
ISV PA1
R10921096許正宗

1(a)

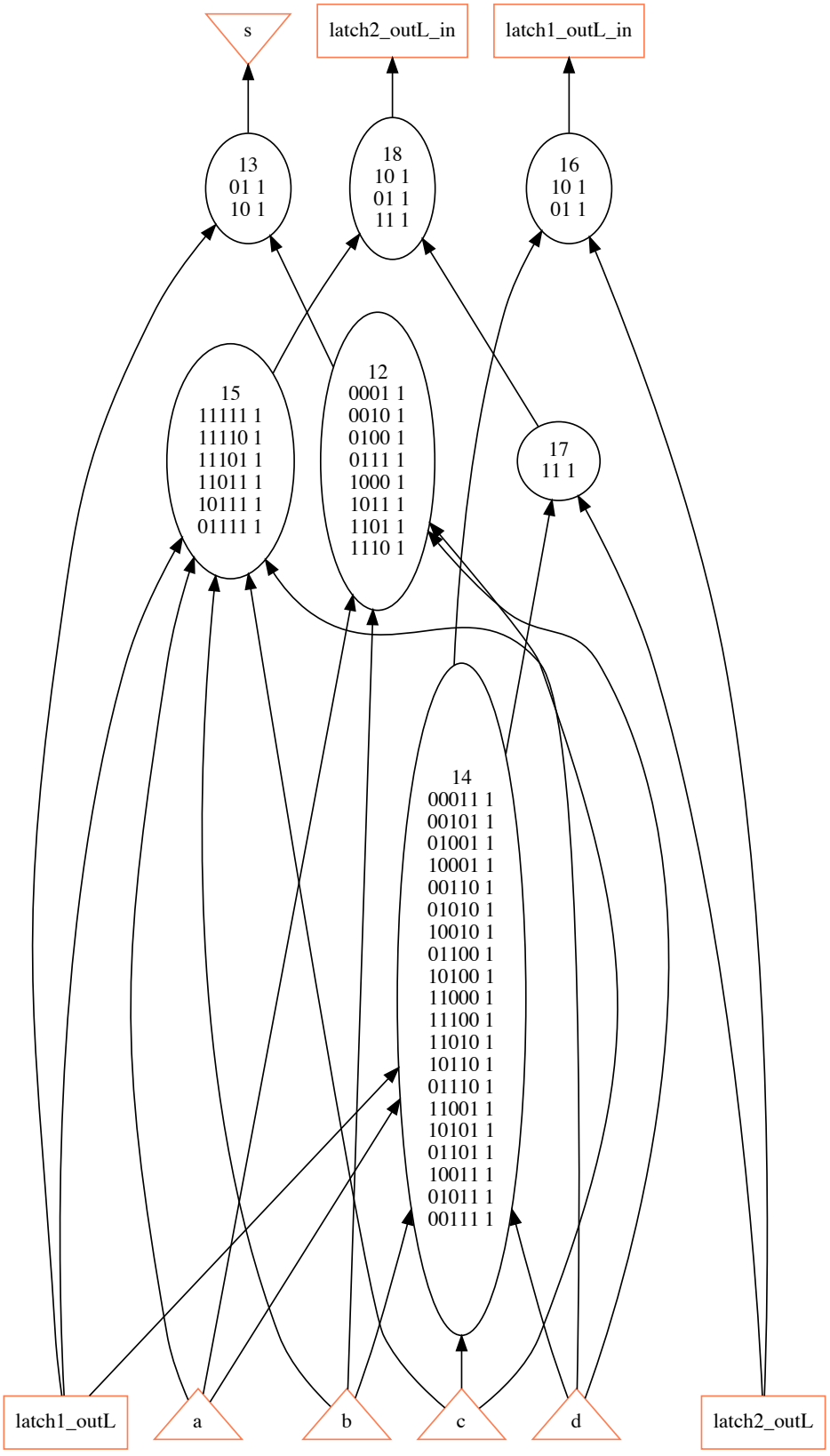
我腦中的4-number serial adder示意圖：



1(b).3.5.7

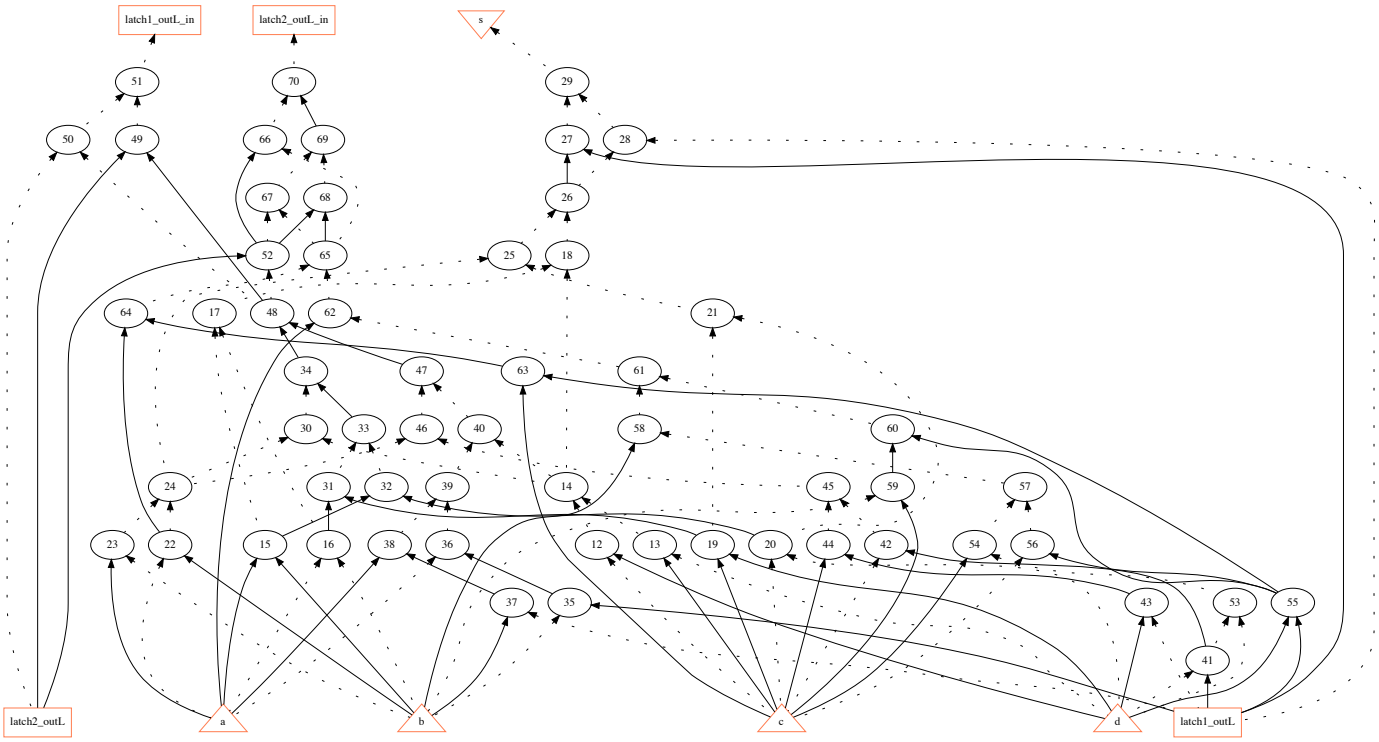
Network structure visualized by ABC
Benchmark "four-numbear_serial_adder". Time was Thu Oct 7 16:18:21 2021.

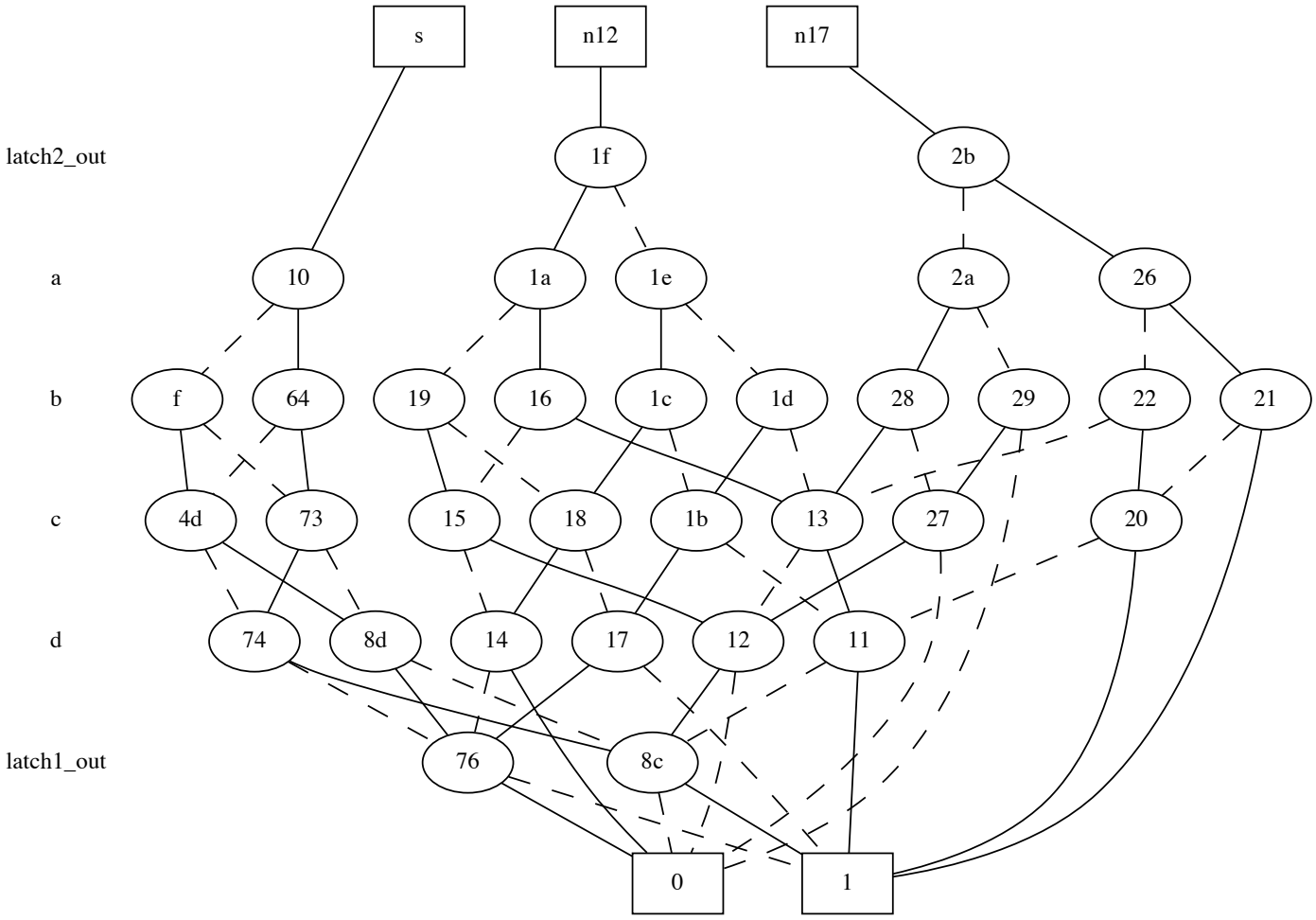
The network contains 7 logic nodes and 2 latches.



Network structure visualized by ABC
Benchmark "four-numbear_serial_adder". Time was Thu Oct 7 19:15:36 2021.

The network contains 59 logic nodes and 2 latches.




s
n12
n17
latch2_out
1f
2b
a
10
1a
1e
2a
26
b
f
64
19
16
1c
1d
28
29
22
21
c
4d
73
15
18
1b
13
27
20
d
74
8d
14
17
12
11
latch1_out
76
8c
0
1

2(a).1
aig - Converts local functions of the nodes to AIGs.
straig - 整張圖的每個node是aig,使用structural hashing
2(a).1
bdd - Converts local functions of the nodes to BDDs.
collapse - 整張圖的每個node是bdd
2(b)
logic