

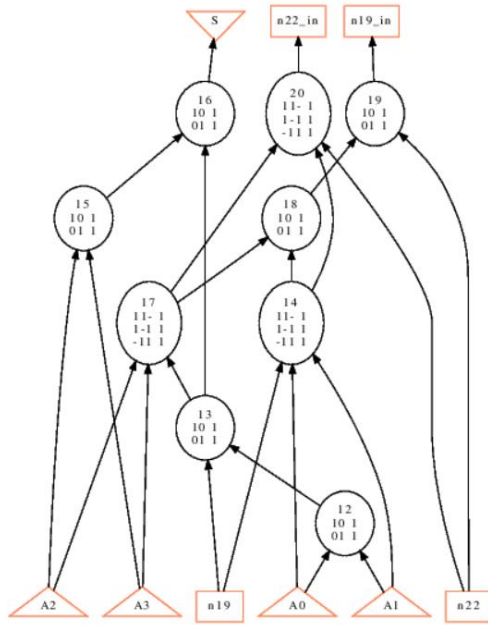
B07901063 EE4 蔡丞威

1.

read

Network structure visualized by ABC
Benchmark "4bitadder". Time was Thu Oct 7 12:37:57 2021.

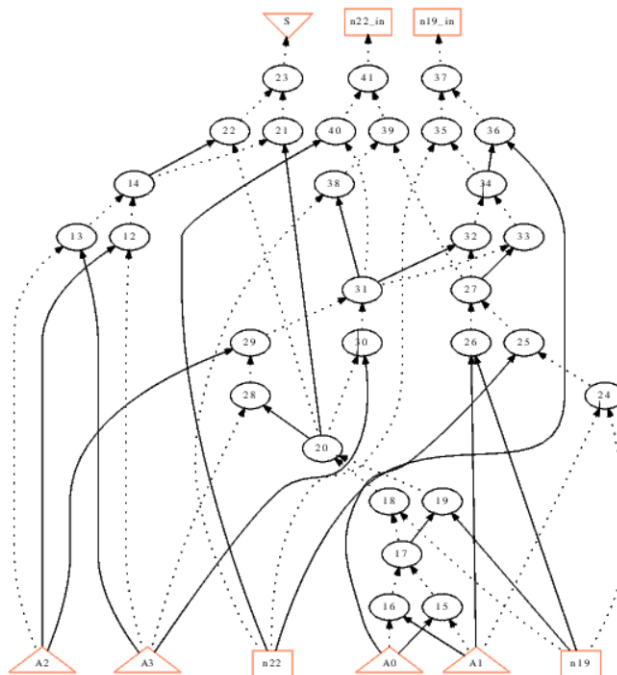
The network contains 9 logic nodes and 2 latches.



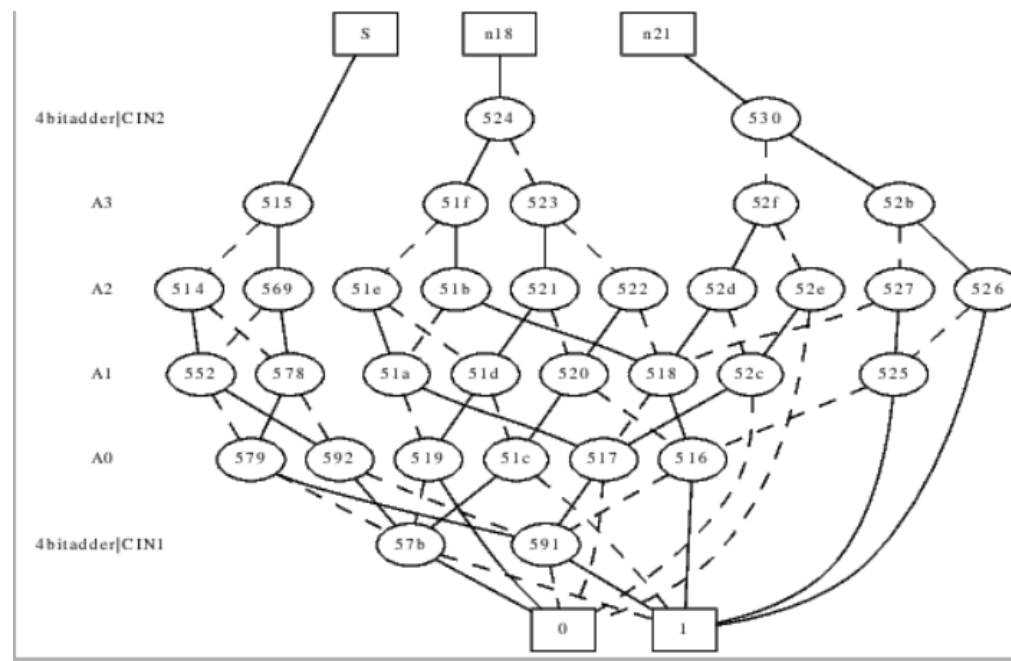
Strash

Network structure visualized by ABC
Benchmark "4bitadder". Time was Thu Oct 7 12:40:56 2021.

The network contains 30 logic nodes and 2 latches.



Collapse



2a1.

Strash command will turn the whole circuit into "And inverter graph"

Aig command, each gate will show the sum of product of the inputs of the gate due to the truth table written inside the gate but the node is constructed in AIG form

2a2.

Bdd command shows the same as aig command but the node is constructed in BDD form

Collapse command will turn the whole circuit into "Binary decision diagram"

2b.

Renode + sop