

# 1.1 spi\_cr1

## Fields List

Bits	Identifier	Access	Reset	Name / Description
[11:9]	SPI_WRITE_MODE	RW	3'h0	spi_single_wr_mode na
[8]	SSM	RW	1'h0	Spi_software_slave_management na
[7]	LSB_FIRST	RW	1'h0	Spi_lsb_first na
[6]	SPE	RW	1'h0	Spi_enable na
[5:3]	BAUD_RATE_CONTROL	RW	1'h0	Spi_baud_rate_control na
[2]	MSTR	RW	1'h0	Spi_Master_selection na
[1]	CPOL	RW	1'h0	Spi_clock_polarity na
[0]	SPI_CPHA	RW	1'h1	Spi_clock_phase 1'h1