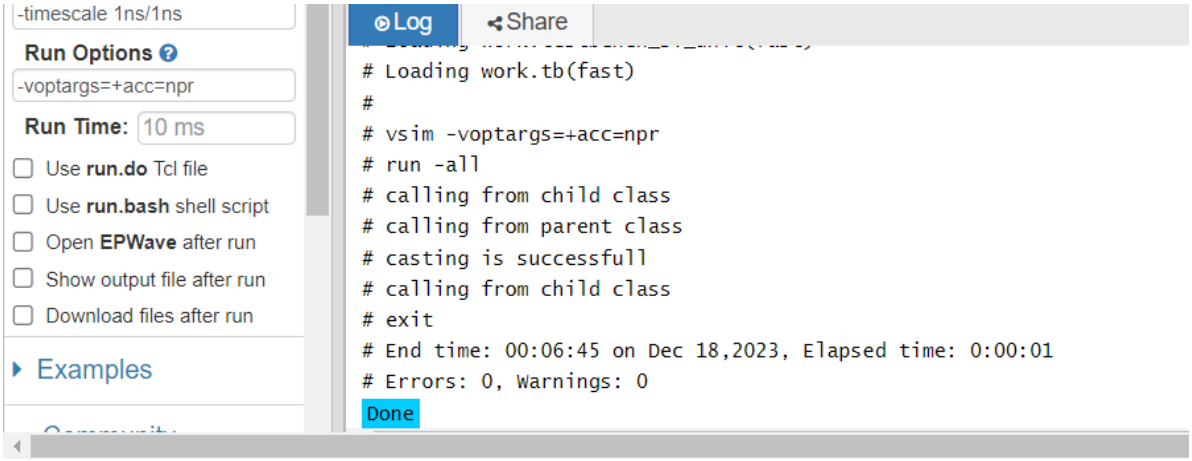
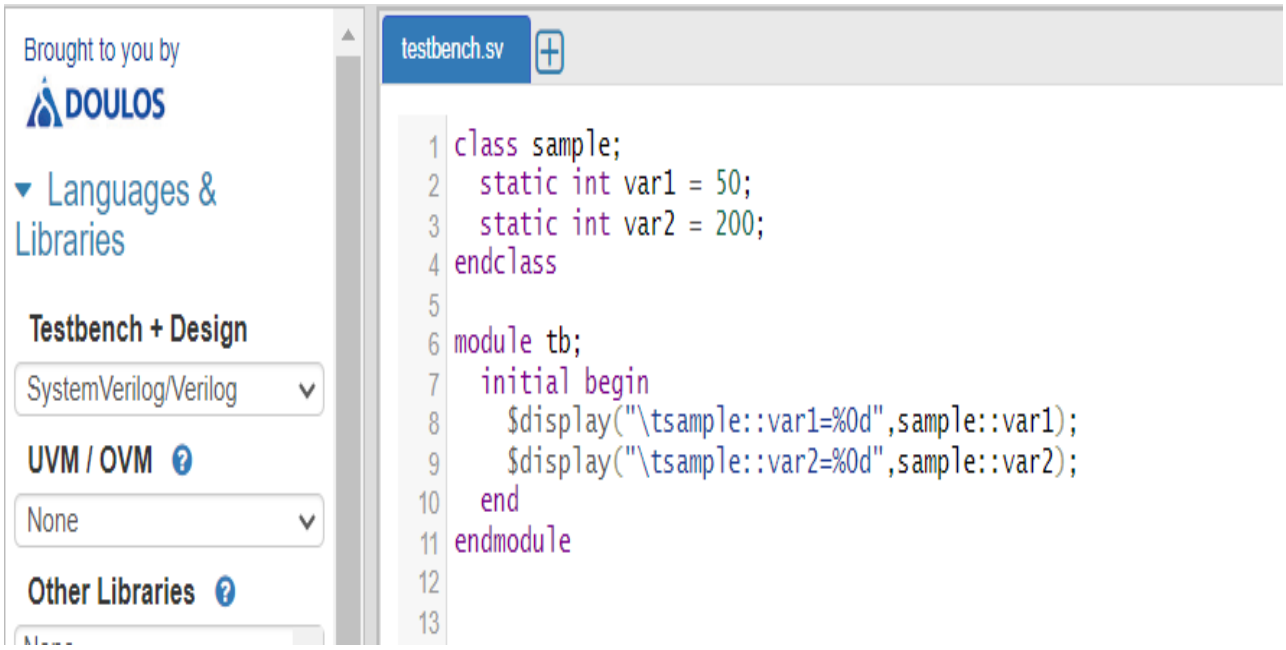
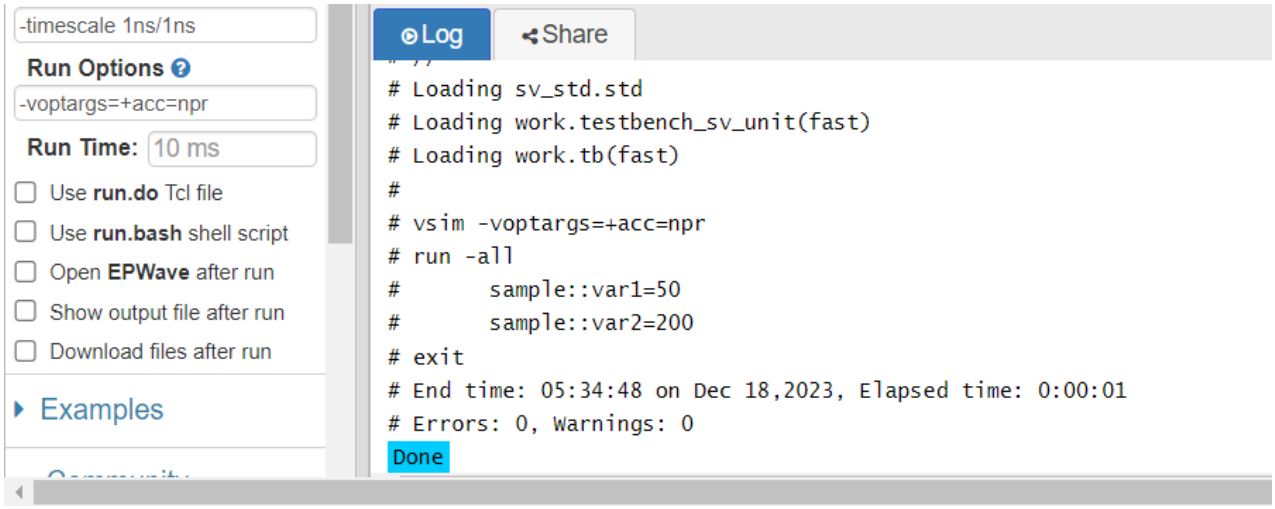
Ques:6 exaple for $cast in system Verilog.

```
1  class base_class;
2    function void print();
3              $display("calling from parent class");
4    endfunction
5  endclass
6
7  class child extends base_class;
8     function void print();
9       $display("calling from child class");
10   endfunction
11 endclass
12
13 module tb;
14   base_class base_o;
15   child ch1,ch2;
16     initial begin
17       ch1=new();
18       ch1.print();
19       base_o=new();
20       base_o = ch1;
21       base_o.print();
22       if($cast(ch2,base_o))begin
23            $display("casting is successfull");
24            ch2.print();
25         end
26      else
27            $error("casting is unsuccessfull");
28     end
29 endmodule
```

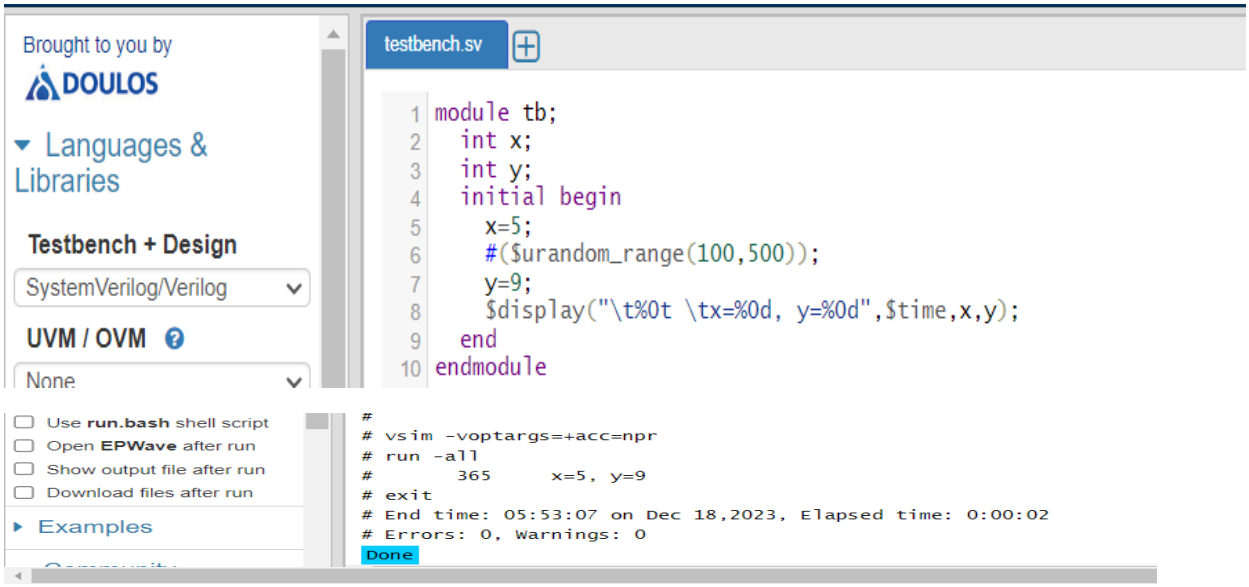


Ques:7 Example for static variable in system Verilog.





Ques:15 Write SV code to wait for a random delay in range 100 to 500ns.



Ques:16 Wrie a code to extract 5 elements at a time from a queue.