

SPEC® CFP2006 Result

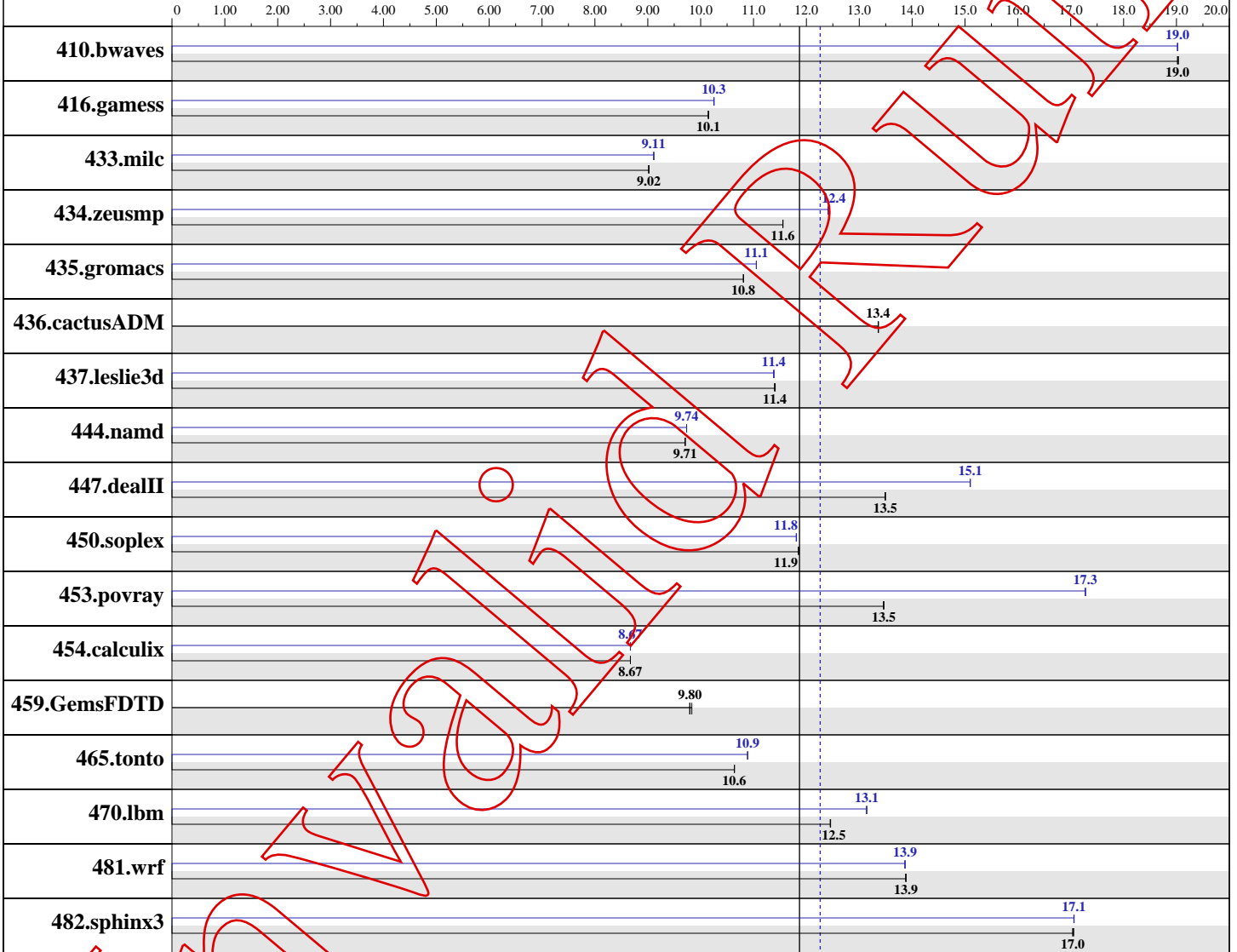
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Intel Corporation
System Model Name

SPECfp®2006 = 12.3

SPECfp_base2006 = 11.9

CPU2006 license #: 13 Test sponsor: Intel Corporation Test date: Dec-9999 Hardware Availability: Dec-9999 Software Availability: Dec-9999



SPECfp_base2006 = 11.9

SPECfp2006 = 12.3

Hardware

CPU Name: Intel Core 2 Duo _____
CPU Characteristics: _____
CPU MHz: _____
FPU: Integrated
CPU(s) enabled: _____ cores, _____ chips, _____ cores/chip
CPU(s) orderable: _____ chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip (shared)
L3 Cache: None
Other Cache: None
Memory: _____ GB

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Software

Operating System: Windows Vista64 Ultimate
Compiler: Intel C++ Compiler for IA32 version 10.0
Build 20070426 Package ID: W_CC_P_10.0.025
Intel Fortran Compiler for IA32 version 10.0
Build 20070426 Package ID: W_FC_P_10.0.025
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit

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Hardware (Continued)

Disk Subsystem: _____ GB SATA, _____ RPM
Other Hardware: None

Software (Continued)

Other Software: None
SmartHeap Library Version 8.0 from
<http://www.microquill.com>

Errors

Your run was marked invalid because it has one or more flags in the "unknown" category. You might be able to resolve this problem without re-running your test; see

<http://www.spec.org/auto/cpu2006/Docs/runspec.html#flagsurl>
for more information.

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	714	19.0	714	19.0	715	19.0	714	19.0	714	19.0	715	19.0
416.gamess	1930	10.1	1930	10.1	1930	10.1	1910	10.3	1910	10.3	1910	10.3
433.milc	1020	9.02	1020	9.02	1020	9.02	1010	9.11	1010	9.11	1010	9.11
434.zeusmp	787	11.6	788	11.6	788	11.6	733	12.4	733	12.4	733	12.4
435.gromacs	661	10.8	661	10.8	661	10.8	646	11.1	646	11.1	646	11.1
436.cactusADM	895	13.4	894	13.4	895	13.4	895	13.4	894	13.4	895	13.4
437.leslie3d	825	11.4	824	11.4	824	11.4	826	11.4	826	11.4	826	11.4
444.namd	826	9.71	826	9.71	826	9.71	824	9.73	824	9.74	824	9.74
447.dealII	848	13.5	848	13.5	848	13.5	758	15.1	758	15.1	758	15.1
450.soplex	704	11.9	704	11.9	704	11.9	706	11.8	706	11.8	706	11.8
453.povray	395	13.5	395	13.5	395	13.5	308	17.3	308	17.3	308	17.3
454.calculix	951	8.67	951	8.67	951	8.67	952	8.67	951	8.67	951	8.67
459.GemsFDTD	1080	9.83	1080	9.80	1080	9.79	1080	9.83	1080	9.80	1080	9.79
465.tonto	925	10.6	925	10.6	925	10.6	904	10.9	904	10.9	904	10.9
470.lbm	1100	12.5	1100	12.5	1100	12.5	1050	13.1	1050	13.1	1050	13.1
481.wrf	805	13.9	805	13.9	805	13.9	806	13.9	805	13.9	806	13.9
482.sphinx3	1140	17.0	1140	17.1	1140	17.0	1140	17.1	1140	17.1	1140	17.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 5/2007:
http://www.intel.com/products/motherboard/_____/index.htm
The system bus runs at _____ MHz
System was configured with _____ discrete graphics card
Binaries were built on Windows XP Professional SP2

Base Unknown Flags

410.bwaves: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

416.gamess: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

433.milc: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

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Base Unknown Flags (Continued)

434.zeusmp: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

435.gromacs: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

436.cactusADM: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD)
"-Qlowercase /assume:underscore" (in FPORTABILITY)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

437.leslie3d: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

444.namd: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-TP" (in CXXPORTABILITY) "-fast" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

447.dealIII: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

450.soplex: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

453.povray: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

454.calculix: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-Qlowercase" (in PORTABILITY)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

459.GemsFDTD: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

465.tonto: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

470.lbm: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

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Base Unknown Flags (Continued)

481.wrf: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

482.sphinx3: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

Peak Unknown Flags

410.bwaves: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

416.gamess: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

433.milc: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

434.zeusmp: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

435.gromacs: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

436.cactusADM: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD)
"-Qlowercase /assume:underscore" (in FPORTABILITY)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

437.leslie3d: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

444.namd: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-TP" (in CXXPORTABILITY) "-fast" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

447.dealII: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)

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Peak Unknown Flags (Continued)

450.soplex: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)

"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

"shlw32m.lib" (in EXTRA_LIBS)

453.povray: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)

"-fast" (in OPTIMIZE) "-Qcxx_features" (in CXXOPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

"shlw32m.lib" (in EXTRA_LIBS)

454.calculix: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)

"ifort" (in LD) "-Qlowercase" (in PORTABILITY)

"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

459.GemsFDTD: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

465.tonto: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

470.lbm: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)

"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

"shlw32m.lib" (in EXTRA_LIBS)

481.wrf: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)

"ifort" (in LD) "-fast" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

482.sphinx3: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)

"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

"shlw32m.lib" (in EXTRA_LIBS)

410.bwaves: "ifort" (in FC) "ifort" (in LD) "-fast" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

416.gamess: "ifort" (in FC) "ifort" (in LD)

"-Qprof_gen" (in PASS1_FFLAGS)

"-fast -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep-" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

"-Qprof_gen" (in PASS1_LDFLAGS)

"-Qprof_use" (in PASS2_FFLAGS)

"-Qprof_use" (in PASS2_LDFLAGS)

433.milc: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)

"-Qprof_gen" (in PASS1_CFLAGS)

"-fast -Qunroll2 -Oa" (in OPTIMIZE)

"/F950000000" (in EXTRA_LDFLAGS)

"-Qprof_gen" (in PASS1_LDFLAGS)

"shlw32m.lib" (in EXTRA_LIBS)

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Peak Unknown Flags (Continued)

433.milc (continued):

"-Qprof_use" (in PASS2_CFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

434.zeusmp: "ifort" (in FC) "ifort" (in LD)

"-Qprof_gen" (in PASS1_FFLAGS)
"-QxT -O2 -Qprec_div- -Qunroll10 -Qscalar-rep-" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"-Qprof_use" (in PASS2_FFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

435.gromacs: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)

"ifort" (in LD) "-Qprof_gen" (in PASS1_FFLAGS)
"-fast -Oa" (in OPTIMIZE)
"-Qprof_gen" (in PASS1_CFLAGS)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"-Qprof_use" (in PASS2_FFLAGS)
"-Qprof_use" (in PASS2_CFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

437.leslie3d: "ifort" (in FC) "ifort" (in LD)

"-Qprof_gen" (in PASS1_FFLAGS) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"-Qprof_use" (in PASS2_FFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

444.namd: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)
"-TP" (in CXXPORTABILITY)
"-Qprof_gen" (in PASS1_CXXFLAGS)
"-fast -Oa" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CXXFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

447.dealII: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)

"-link /FORCE:MULTIPLE" (in LDOUT)
"-Qprof_gen" (in PASS1_CXXFLAGS)
"-fast -Qprefetch" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CXXFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

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Peak Unknown Flags (Continued)

450.soplex: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-Qprof_gen" (in PASS1_CXXFLAGS) "-fast" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CXXFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

453.povray: "icl -Qvc7.1" (in CXX) "icl -Qvc7.1" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-Qprof_gen" (in PASS1_CXXFLAGS)
"-fast -Qansi-alias" (in OPTIMIZE)
"-Qcxx_features" (in CXXOPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CXXFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

454.calculix: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-Qlowercase" (in PORTABILITY)
"-fast" (in OPTIMIZE) "/F950000000" (in EXTRA_LDFLAGS)

465.tonto: "ifort" (in FC) "ifort" (in LD)
"-Qprof_gen" (in PASS1_FFLAGS) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"-Qprof_use" (in PASS2_FFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

470.lbm: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-Qprof_gen" (in PASS1_CFLAGS)
"-fast -Qunroll2 -Qscalar-rep- -Qprefetch" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CFLAGS)
"-Qprof_use" (in PASS2_LDFLAGS)

481.wrf: "ifort" (in FC) "icl -Qvc7.1 -Qc99" (in CC)
"ifort" (in LD) "-fast" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)

482.sphinx3: "icl -Qvc7.1 -Qc99" (in CC) "icl -Qvc7.1 -Qc99" (in LD)
"-link /FORCE:MULTIPLE" (in LDOUT)
"-Qprof_gen" (in PASS1_CFLAGS)
"-fast -Qunroll2" (in OPTIMIZE)
"/F950000000" (in EXTRA_LDFLAGS)
"-Qprof_gen" (in PASS1_LDFLAGS)
"shlw32m.lib" (in EXTRA_LIBS)
"-Qprof_use" (in PASS2_CFLAGS)

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Peak Unknown Flags (Continued)

482.sphinx3 (continued):

"-Qprof_use" (in PASS2_LDFLAGS)

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