

BLOCK DIAGRAM

REVISION RECORD			
LT#	ECO NO.	APPROVED:	DATE

D

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DESIGNER: Vincent Pan		DATE: 2012-02-14	DEPARTMENT: Hardware DEPT.	
EQA: Robin Zhang		DATE: 2012-02-14	COMPANY: Spreadtrum communications, Inc.	
APPROVED: Gang Xu		DATE: 2012-02-14	CONFIDENTIAL AND PROPRIETARY	
			SHEET: 1 OF 20	

TITLE & REV: SP8810-1_SCH_V1.3.0	
DOCUMENT NO.:	

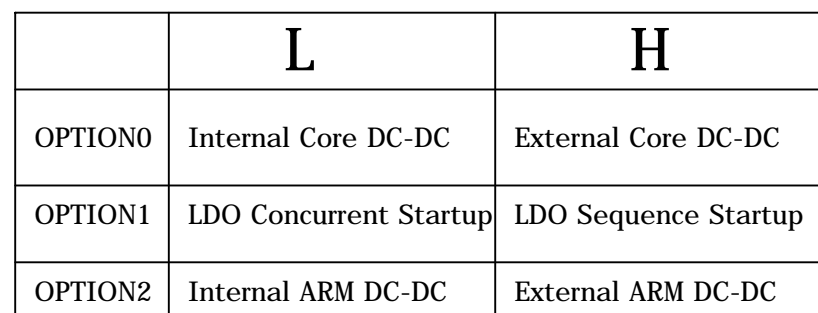
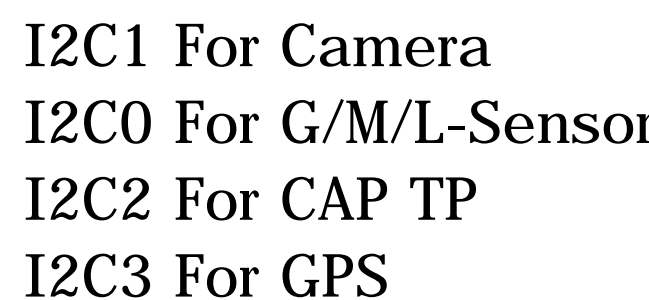
DESIGNER:
EQA:
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spread and

SHEET: 2 OF 20



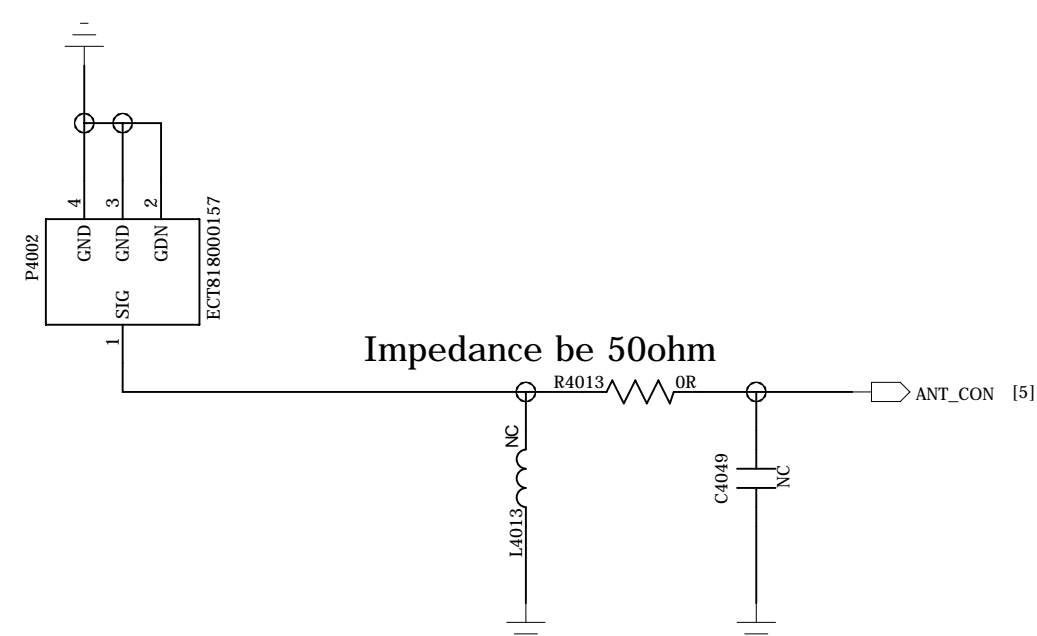




REVISION RECORD			
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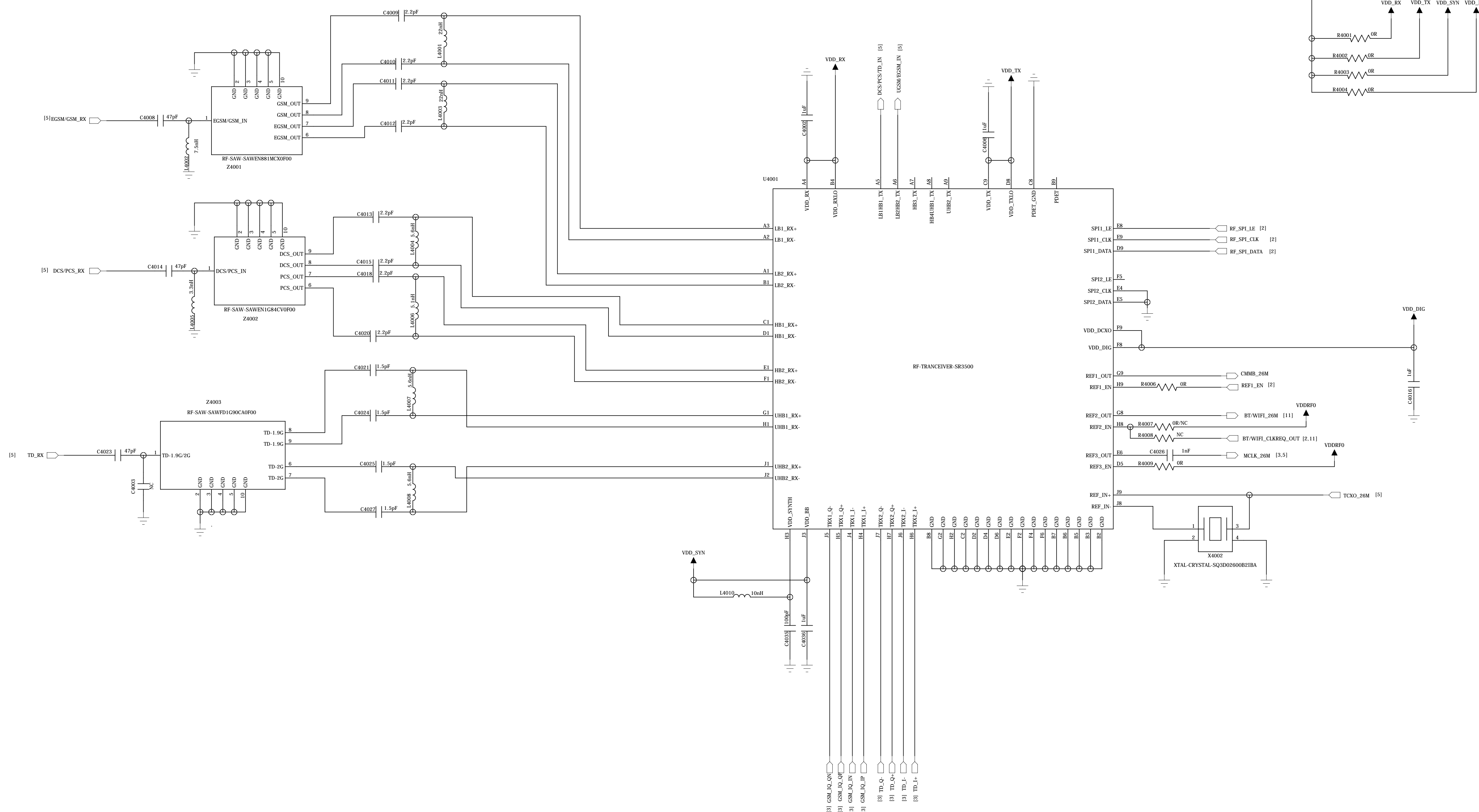
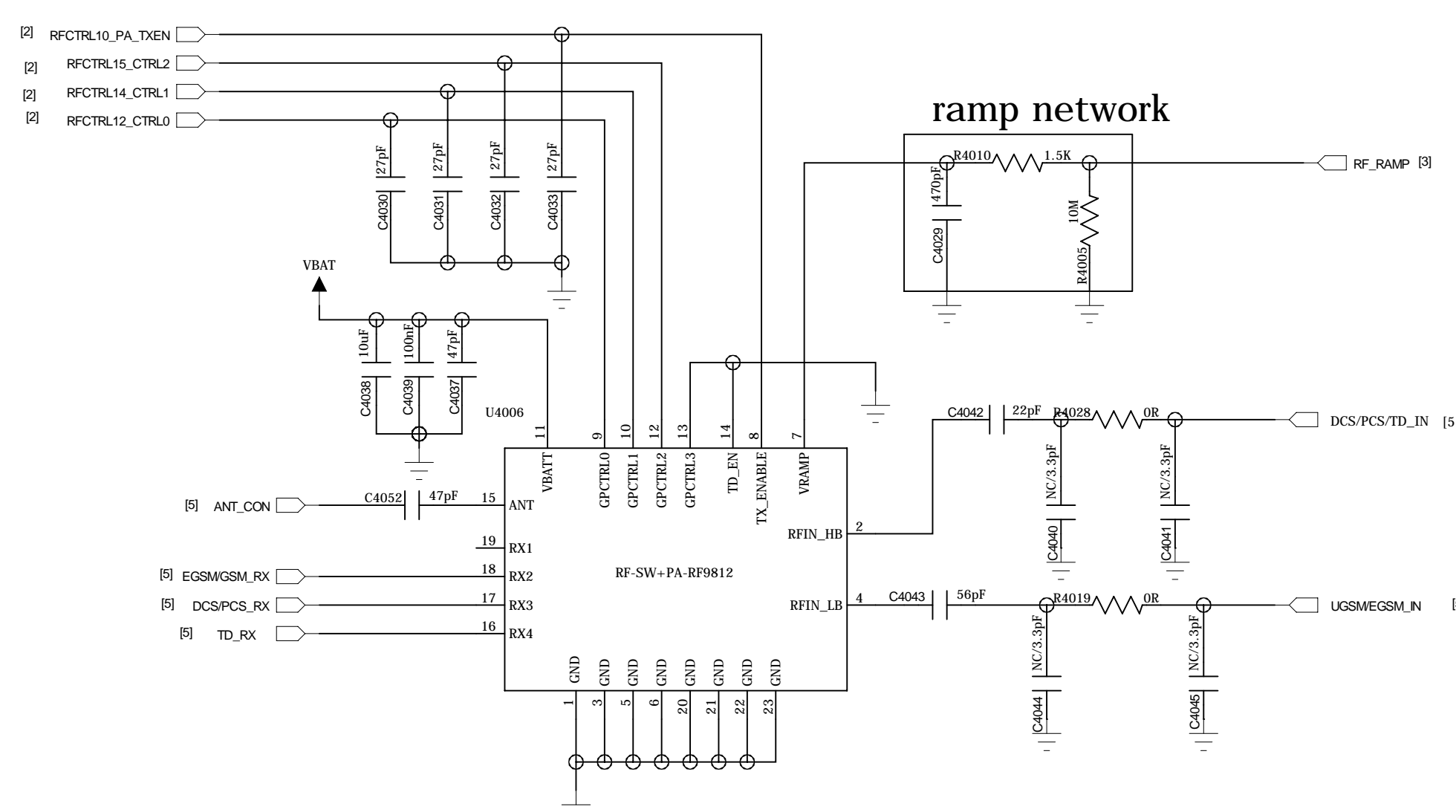
## RF TRANSCEIVER

## RF ANT

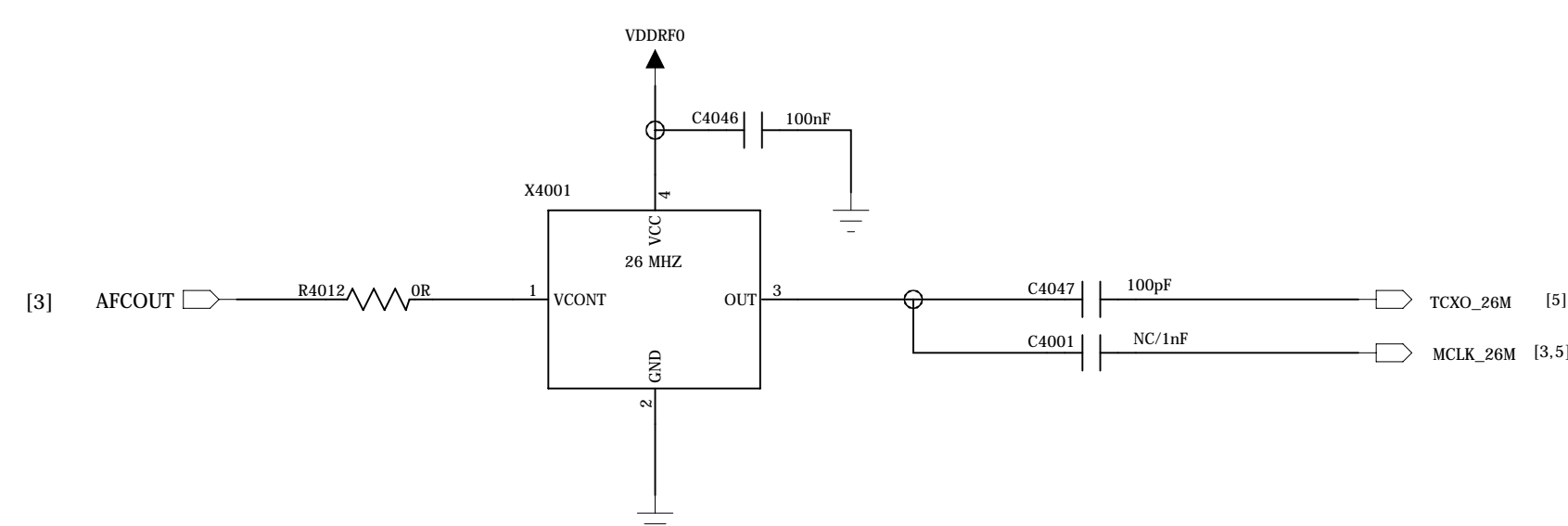


## PA MODULE

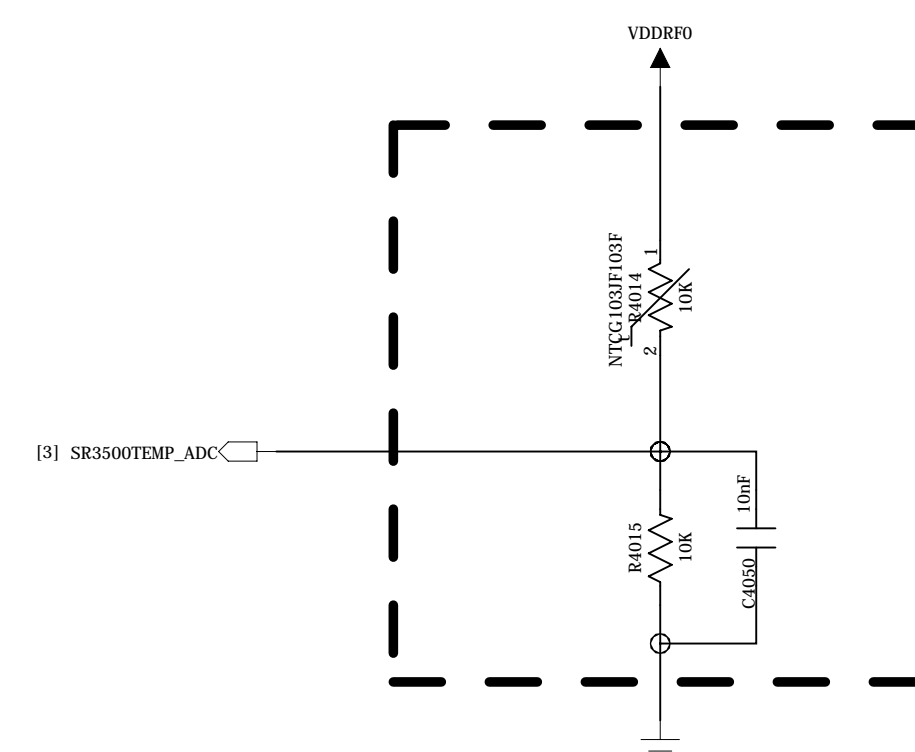
	VRAMP	TXEN	Ctrl_2	Ctrl_1	Ctrl_0
RX1_NC	0	0	1	0	0
RX2_UCSM/EGSM	0	0	0	1	0
RX3_DCS/PCS	0	0	0	1	1
RX4_TD_RX	0	0	0	0	1
TX_LB_GSM	RAMP	1	0	1	0
TX_HB_GSM	RAMP	1	0	1	1
TX_LB_EDGE	1	1	0	0	0
TX_HB_EDGE	1	1	0	0	1
TD-SCDMA_HPM	X	1	1	0	1
TD-SCDMA_LPM	X	1	1	0	0



## 26MHz TCXO Module



TEMP SENSOR

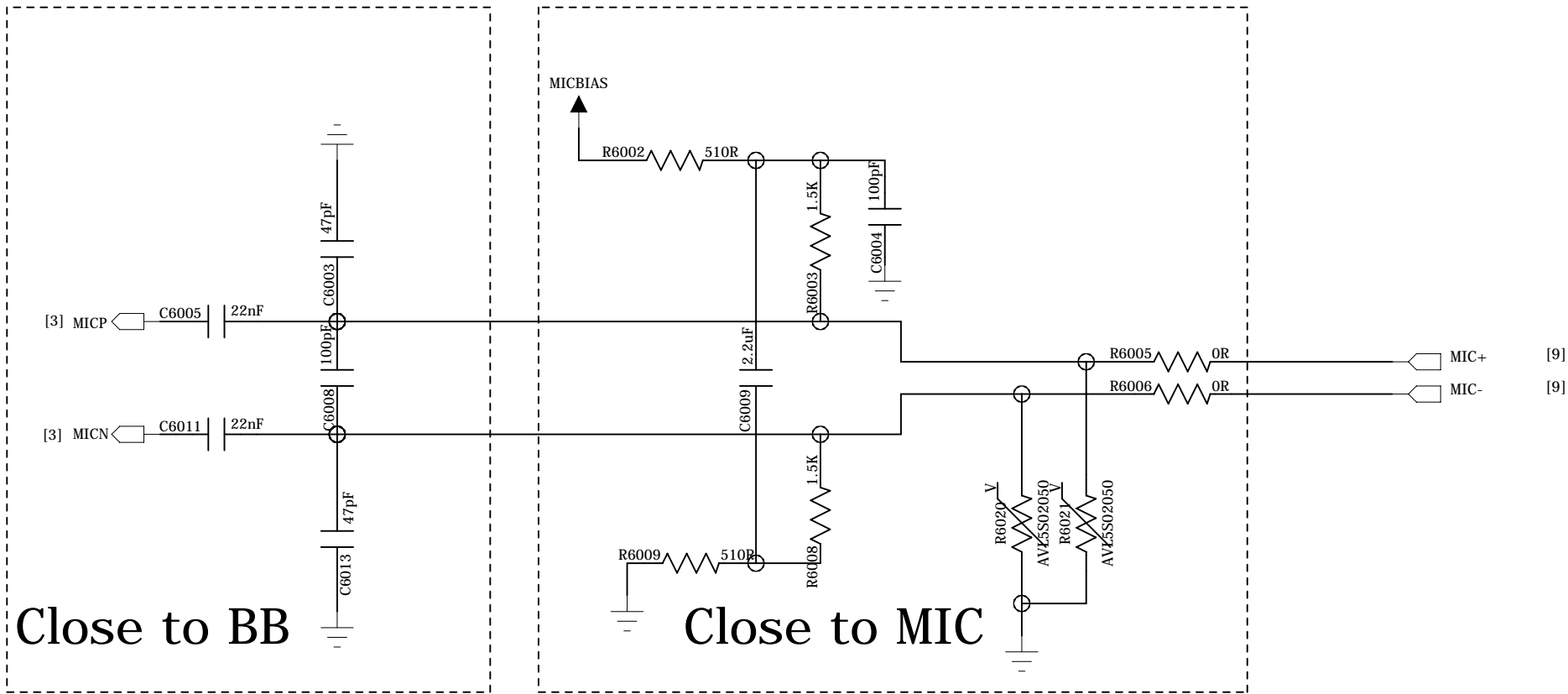


		TITLE & REV:  SP8810-1_SCH_V1.3.0	
		DOCUMENT NO.:	
DESIGNER:	DATED:	DEPARTMENT: Hardware DEPT.	
DRAWN:	DATED:	COMPANY: Spectrum communications, Inc.	
APPROVED:	DATED:	CONFIDENTIAL AND PROPRIETARY	
		SHEET: 5 OF 10	

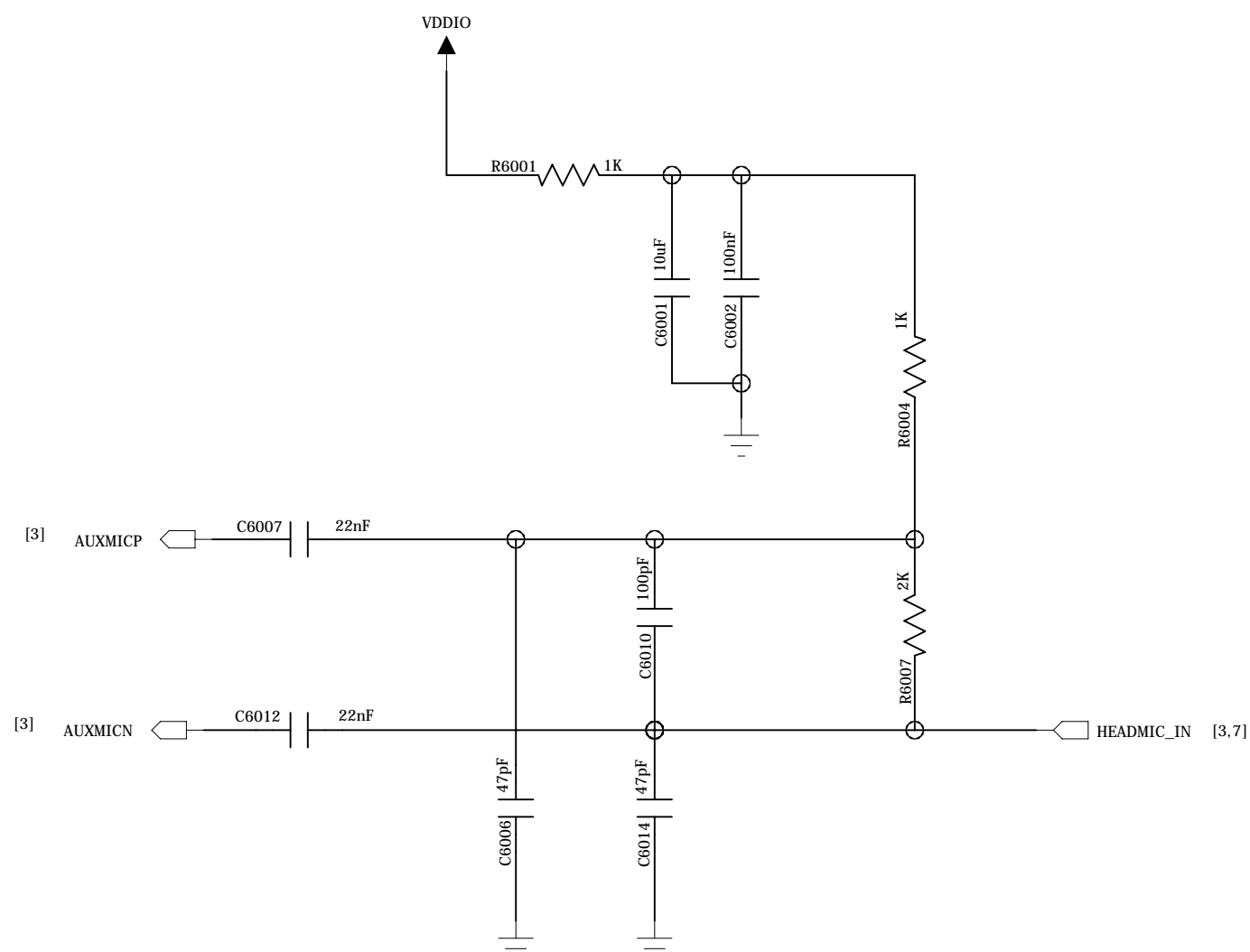


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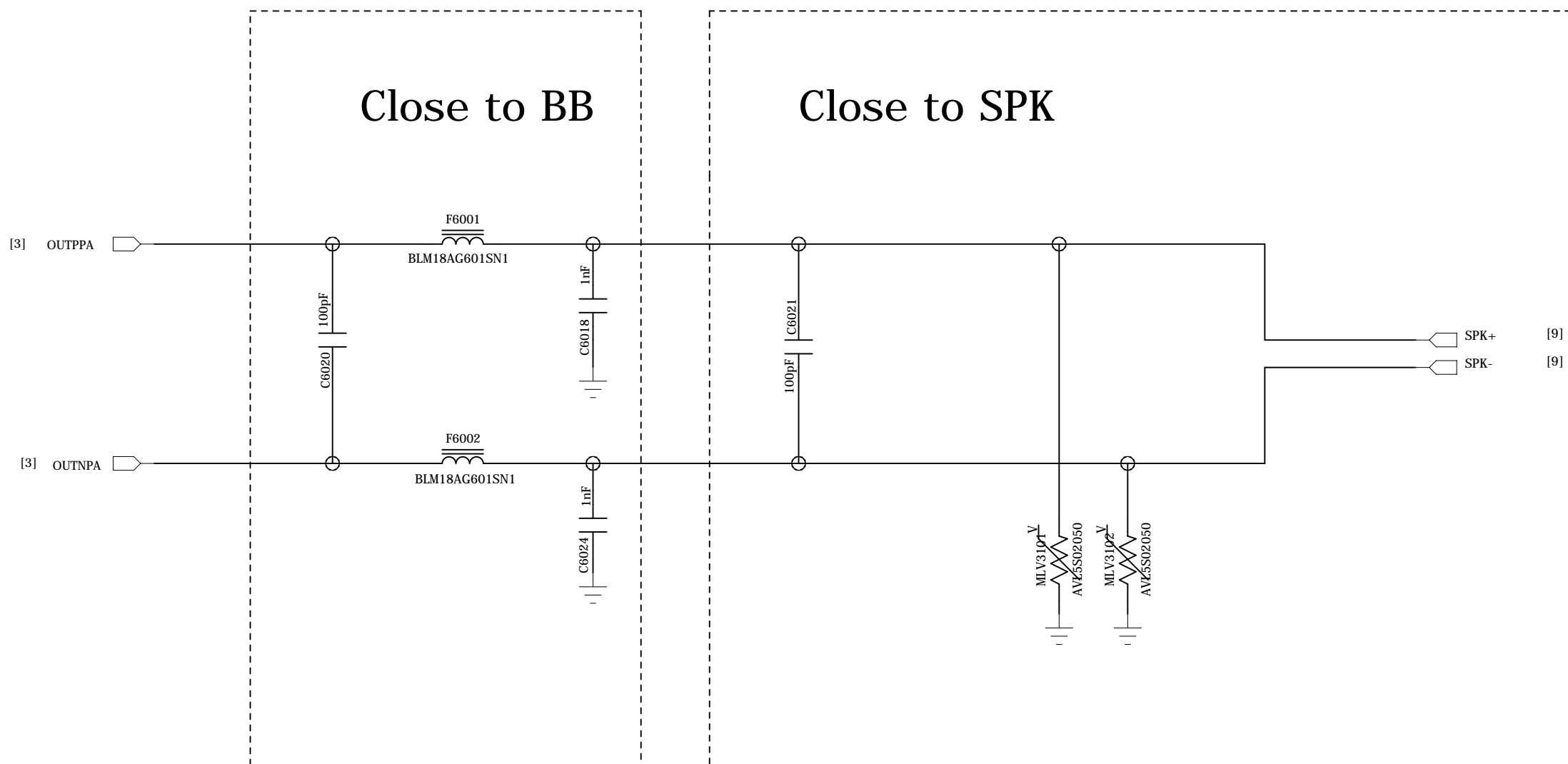
## MIC CIRCUIT



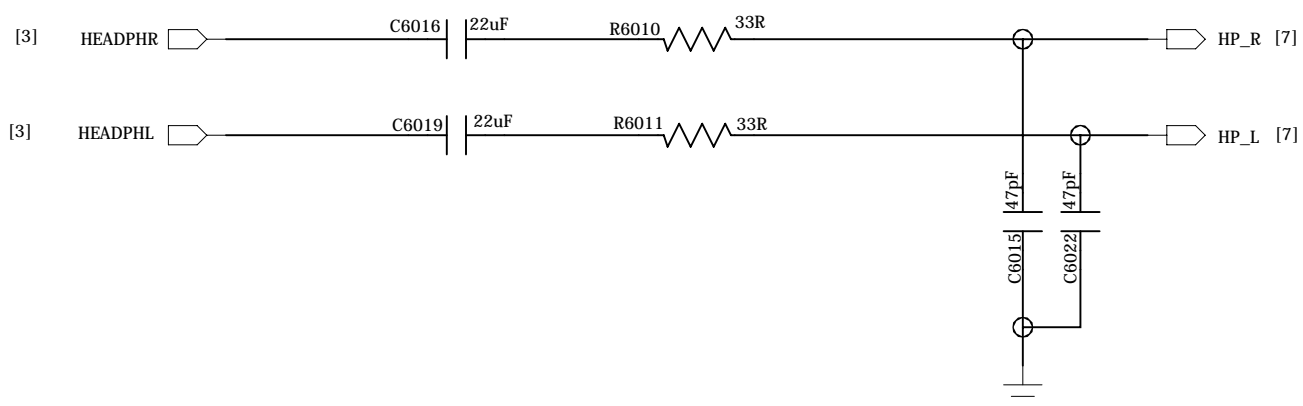
## HEAD MIC



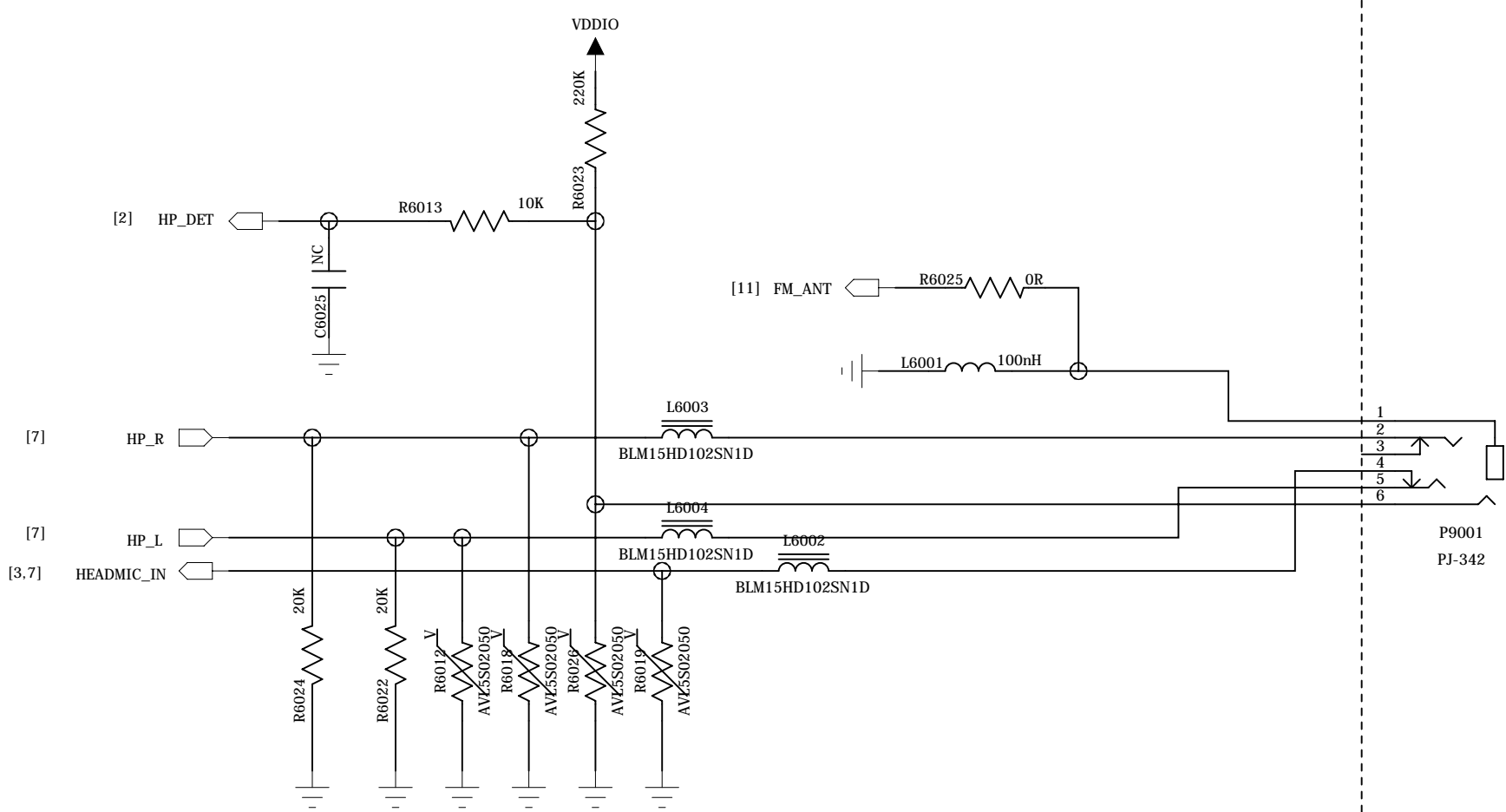
## SPEAKER CIRCUIT



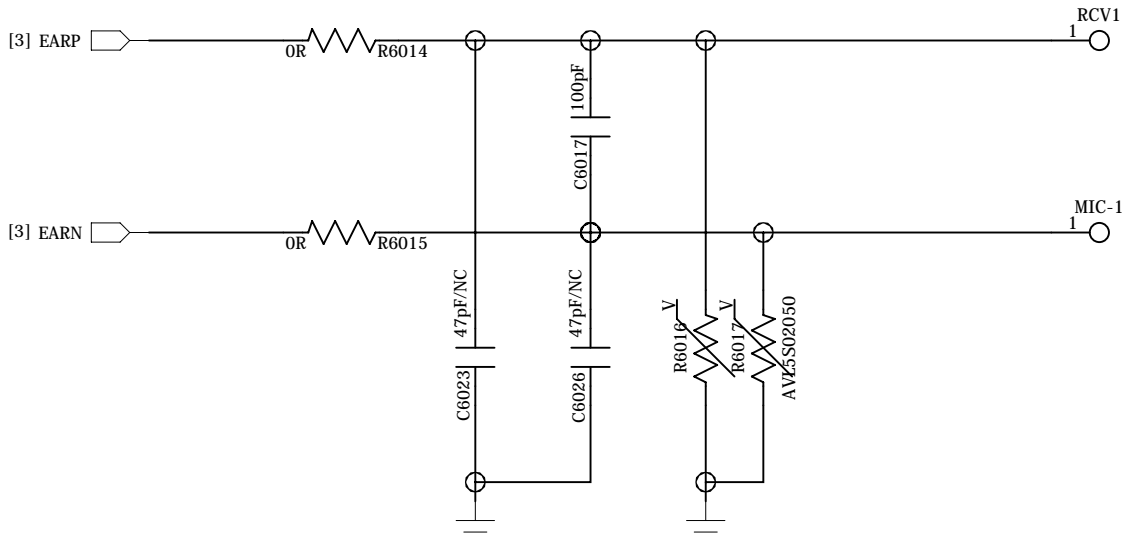
## HEAD EAR



## 3.5mm Headphone Jack



## RECEIVER CIRCUIT



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REVISION RECORD			
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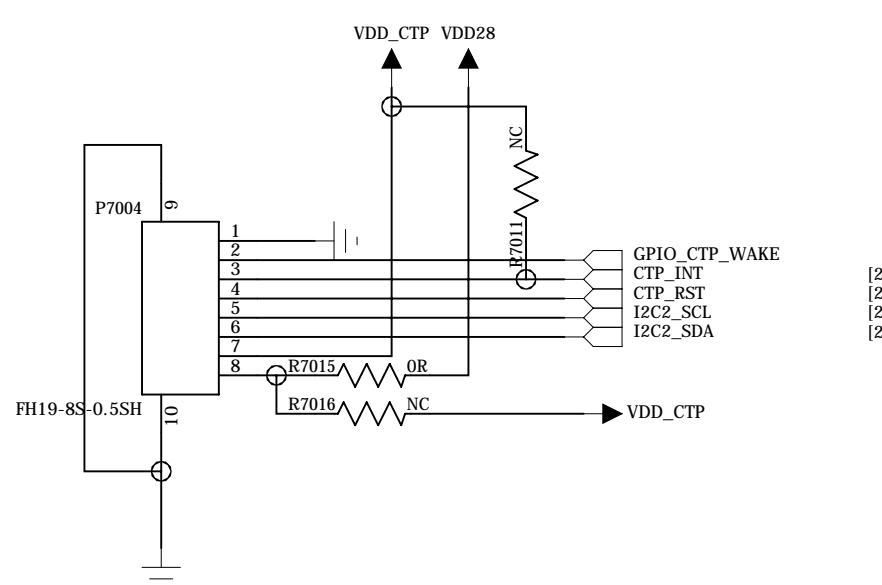
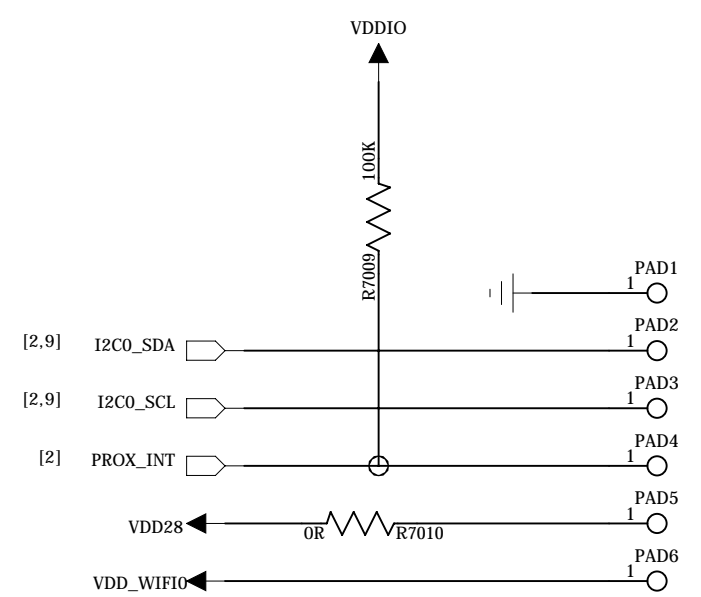
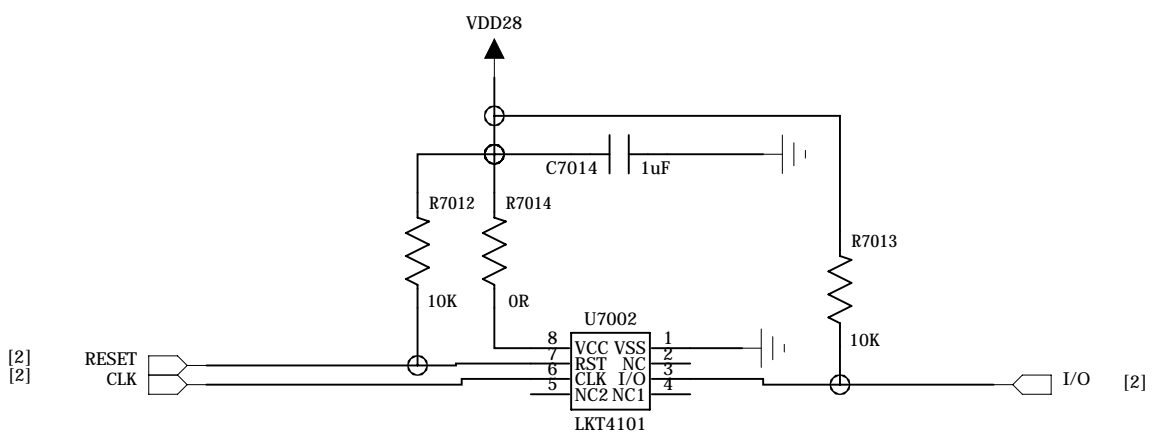
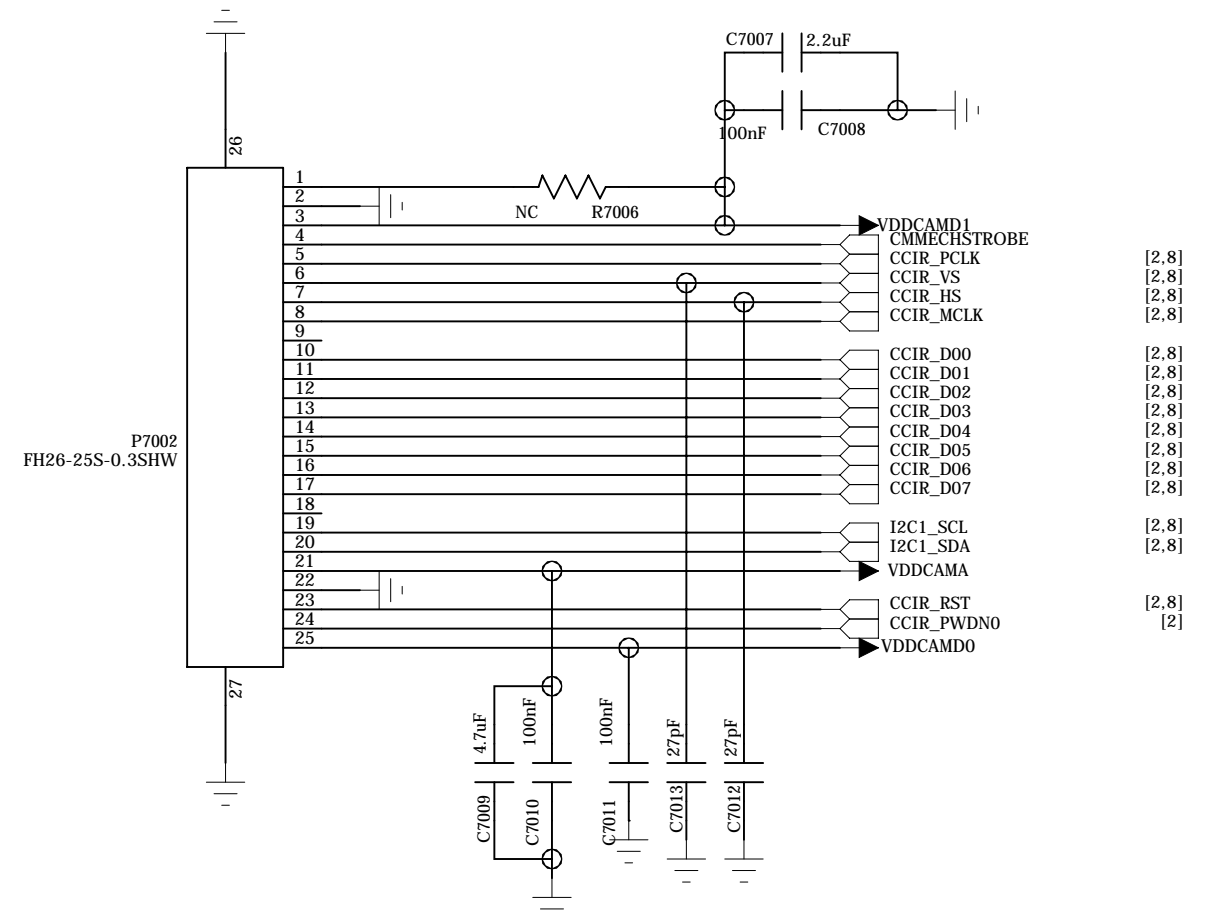
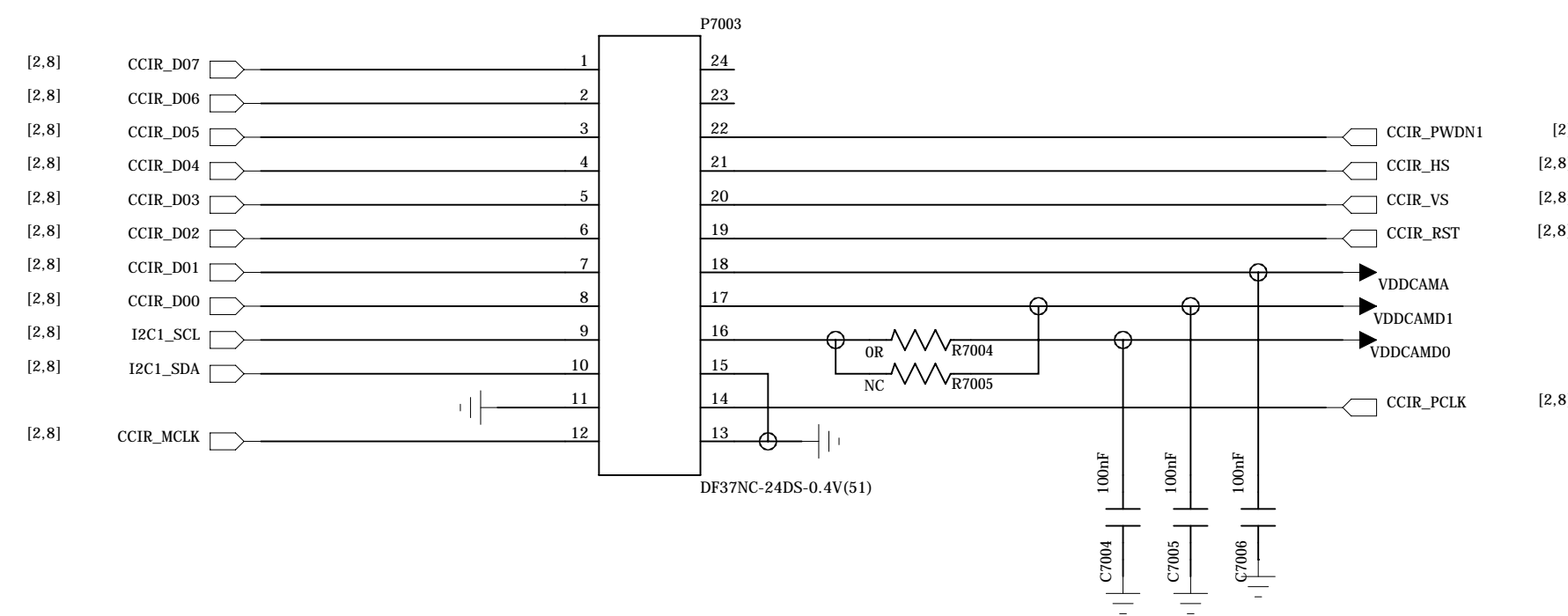
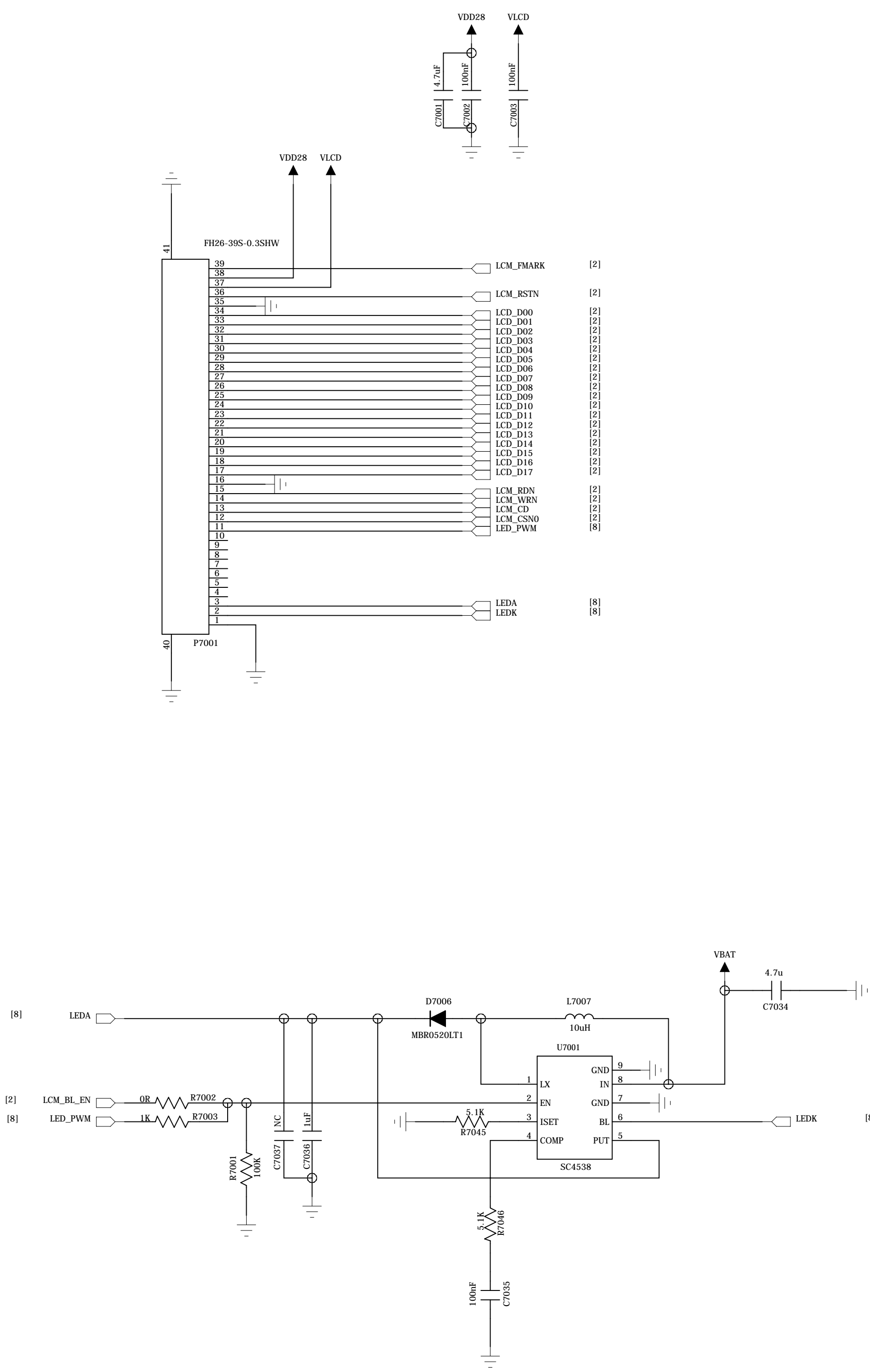
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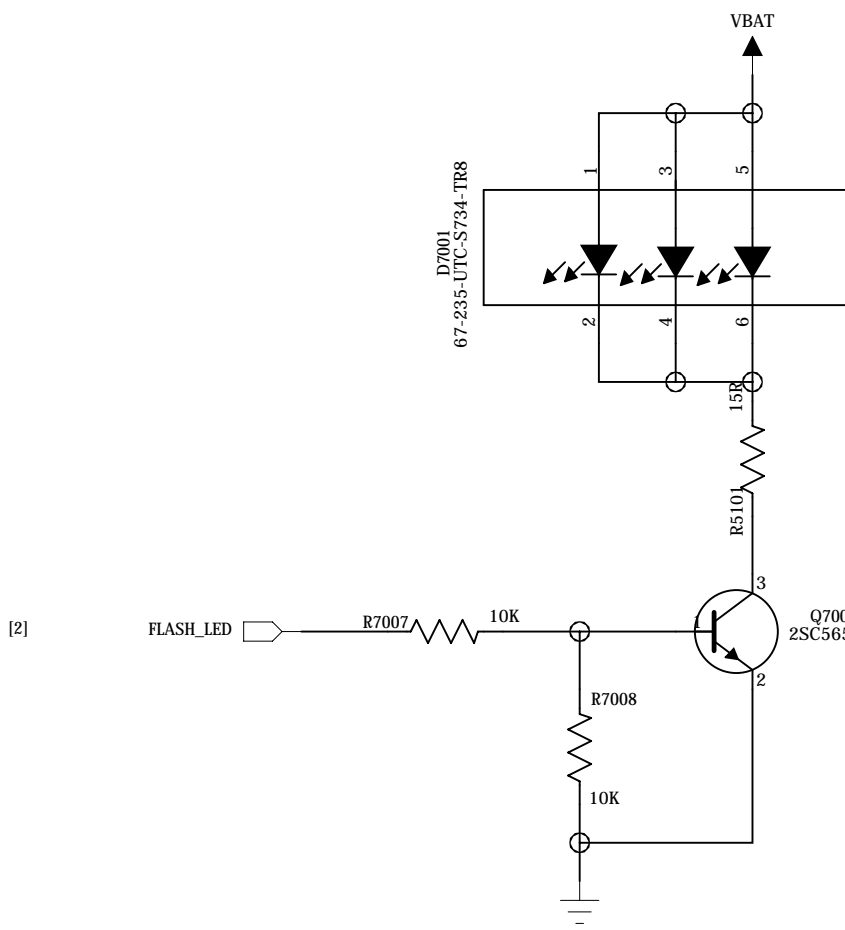
B

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Sensor Module:0.3M+5M



FLASH LED

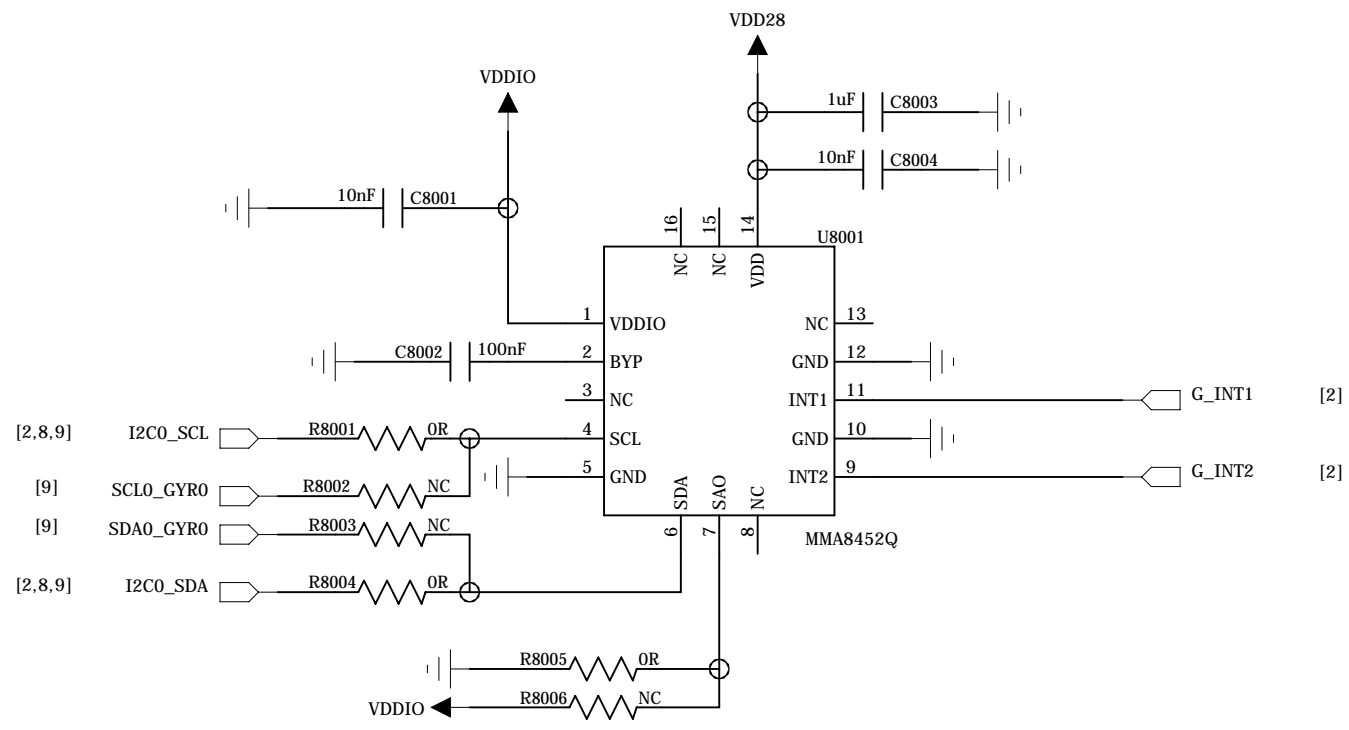


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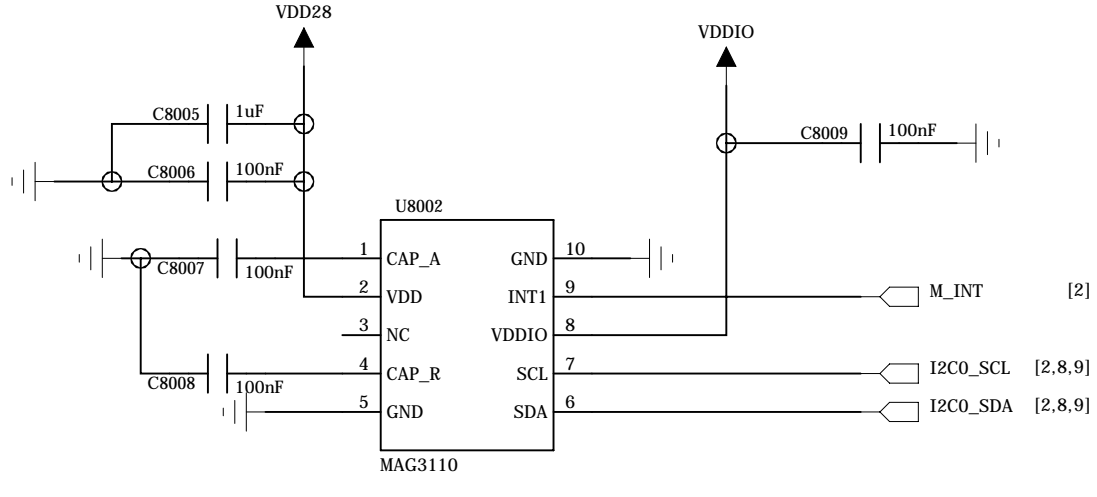


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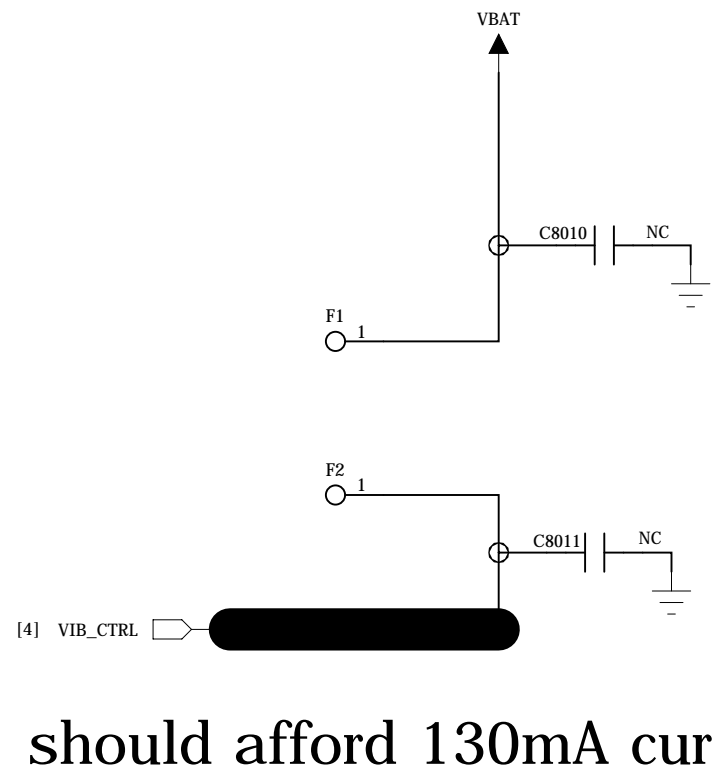
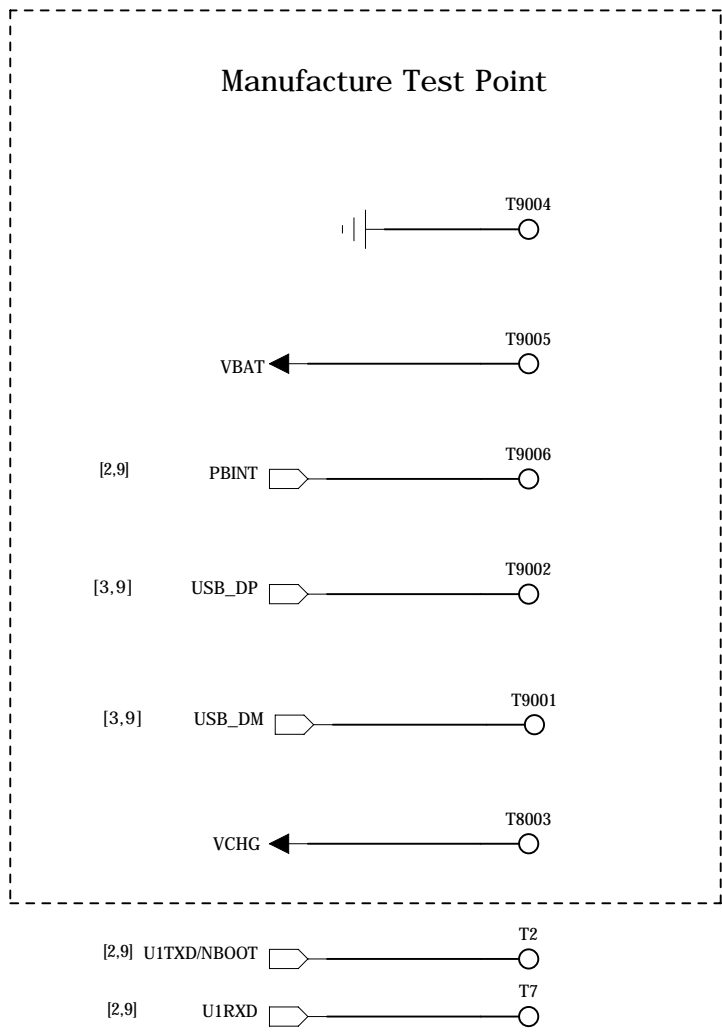
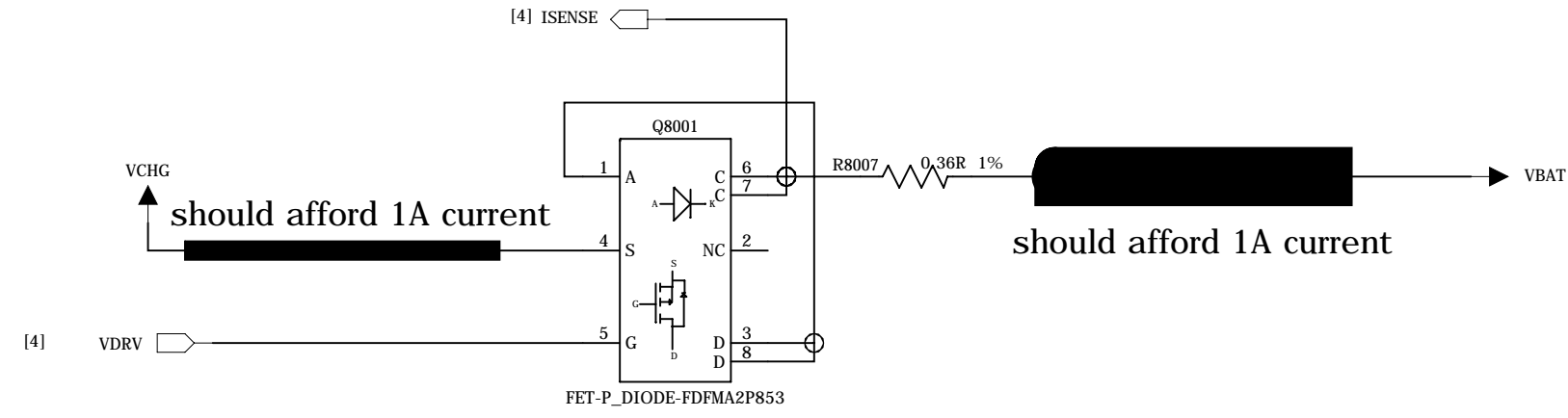
G Sensor



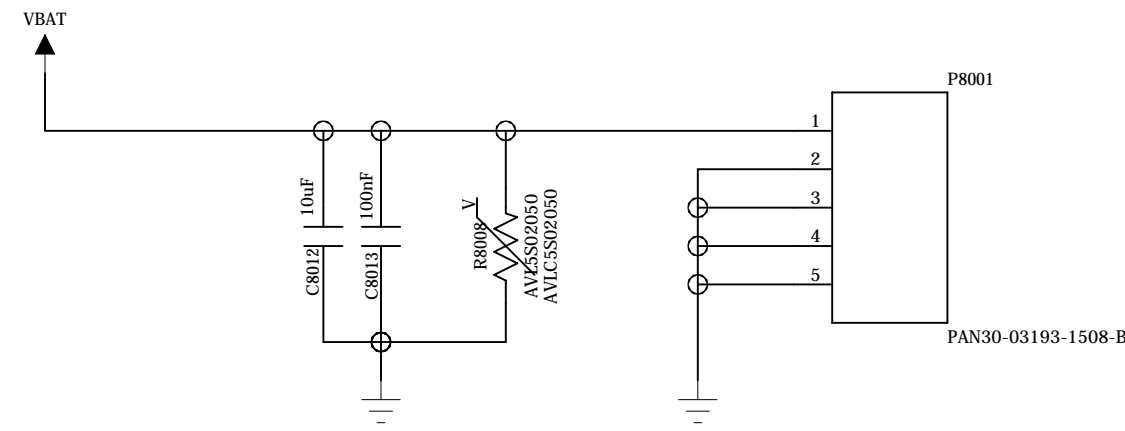
M Sensor



CHARGE

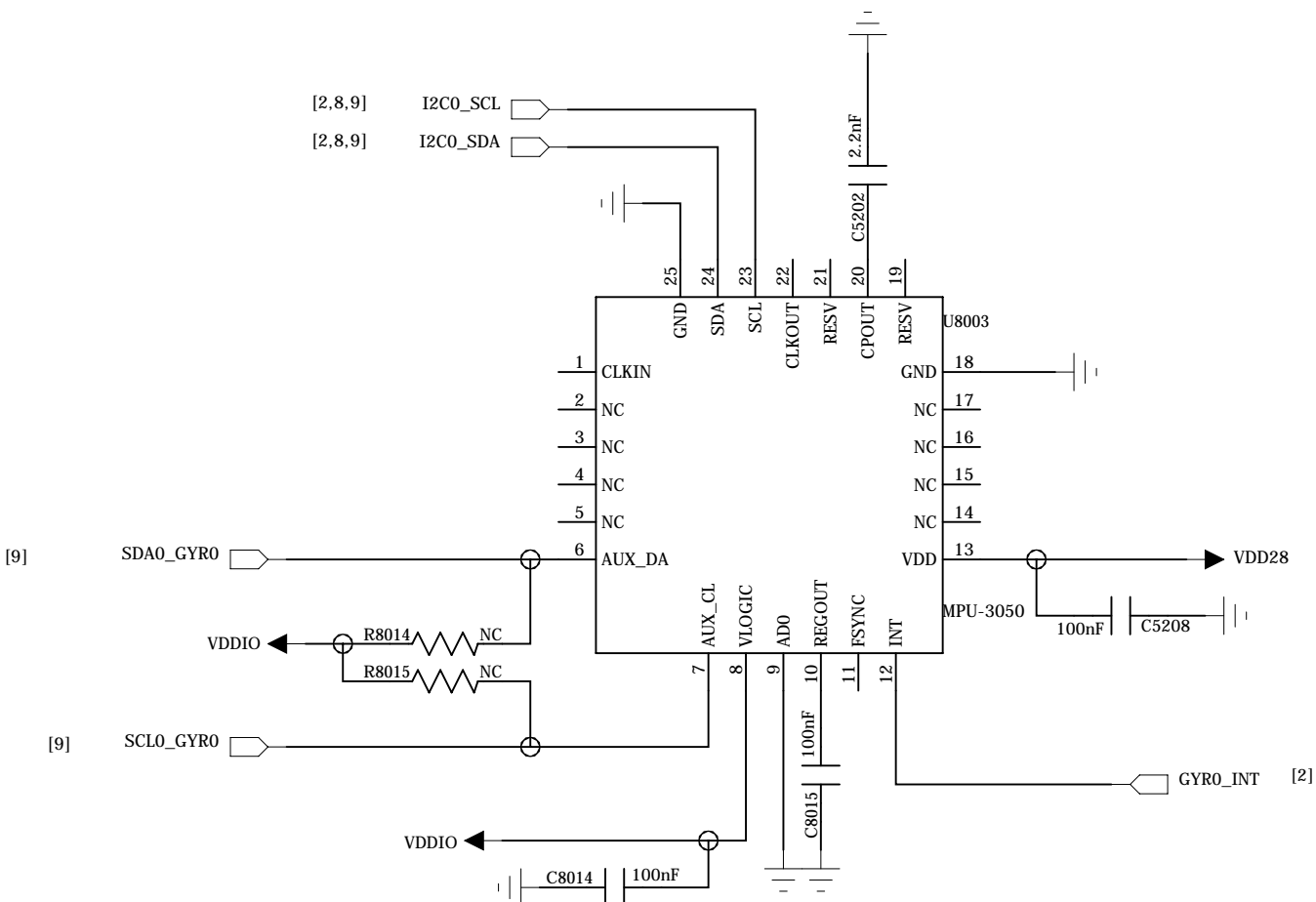


BATTERY

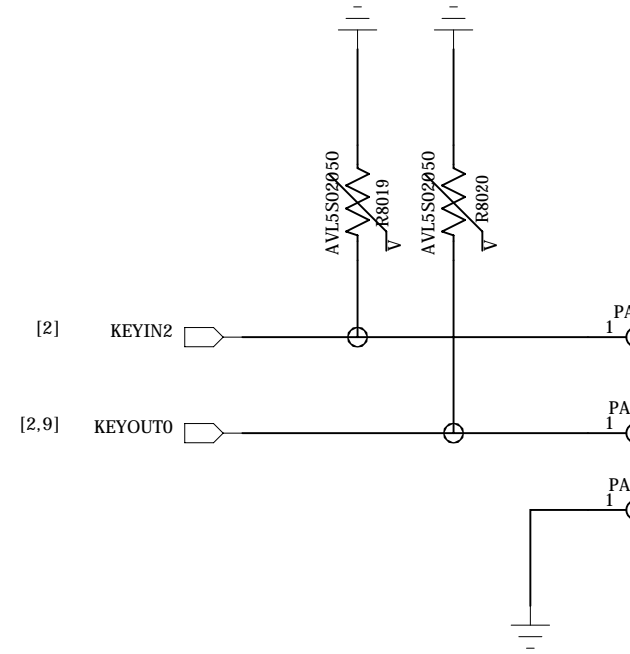
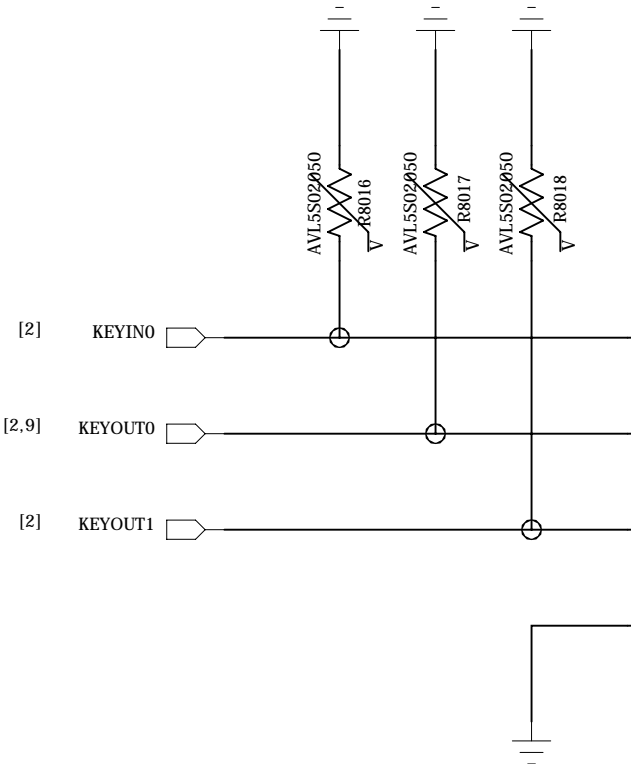


VIBRATOR

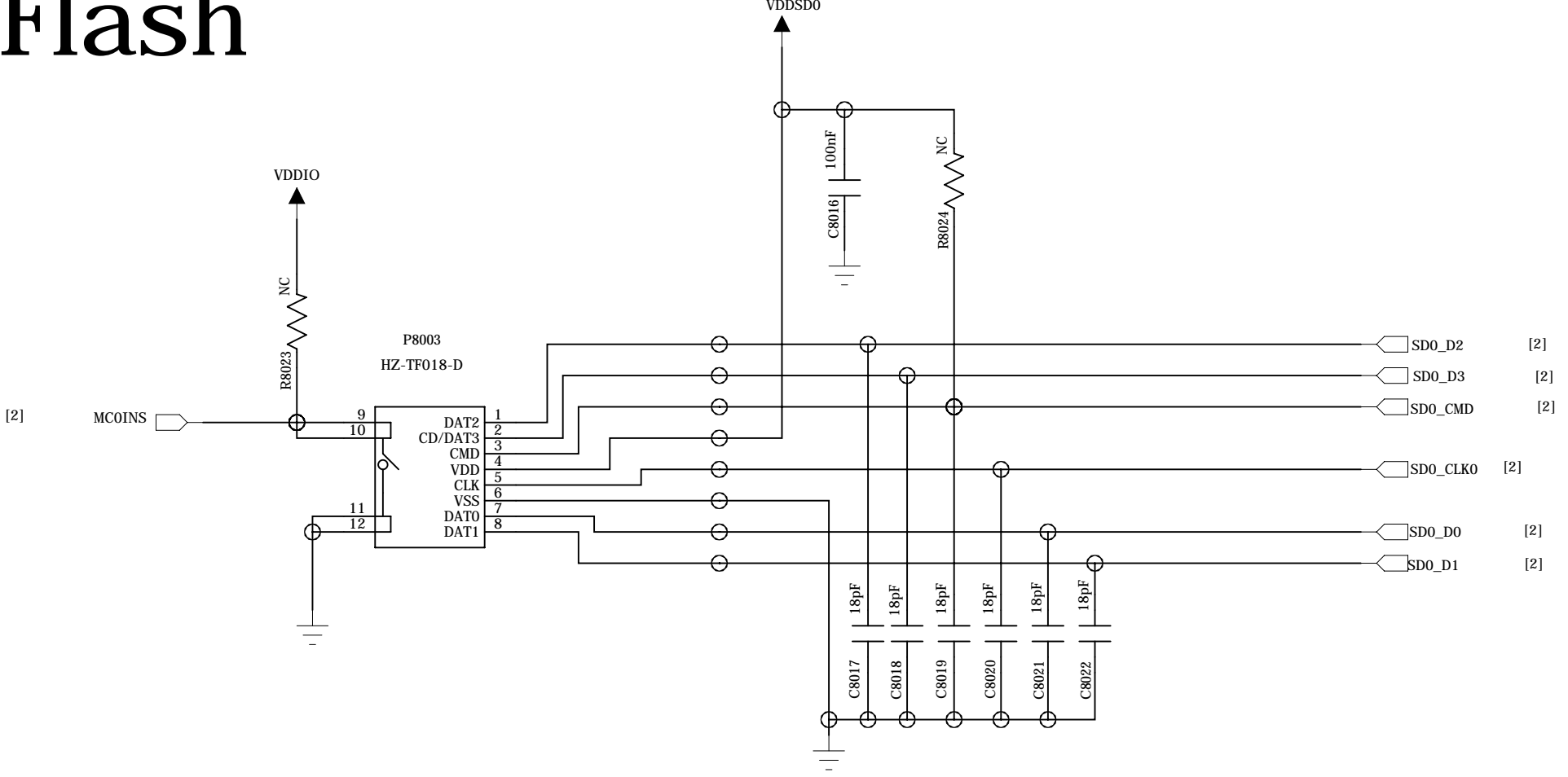
MAG Sensor



CSB= 1: IIC  
IIC ADDRESS is 0x0CH  
power down(communicating), I < 10uA



T\_Flash



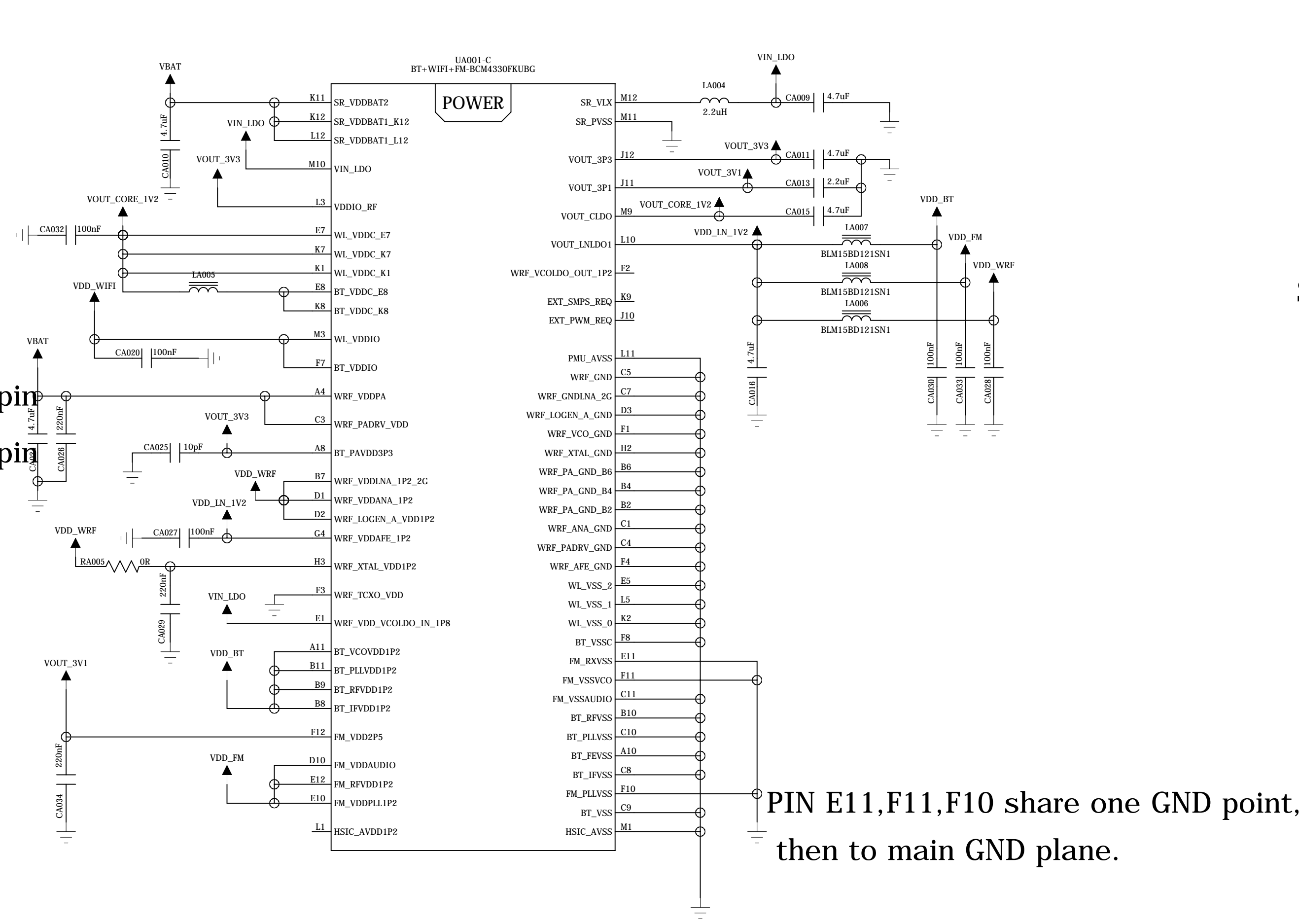
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		Hardware DEPT.	
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		Spreadtrum communications, Inc.	
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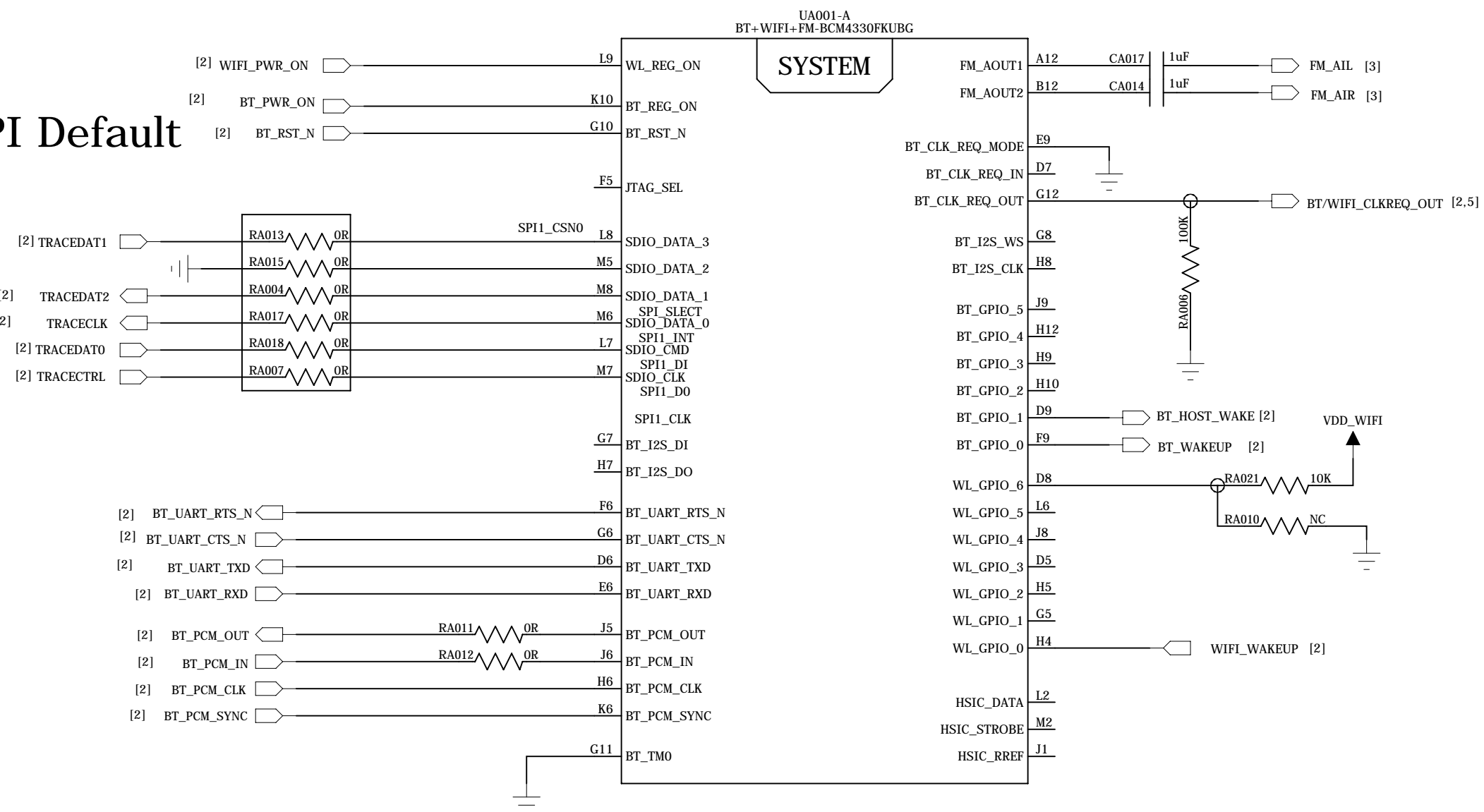
# BT+ WIFI+ FM

C7225 close to A4 pin  
C7226 close to C3 pin

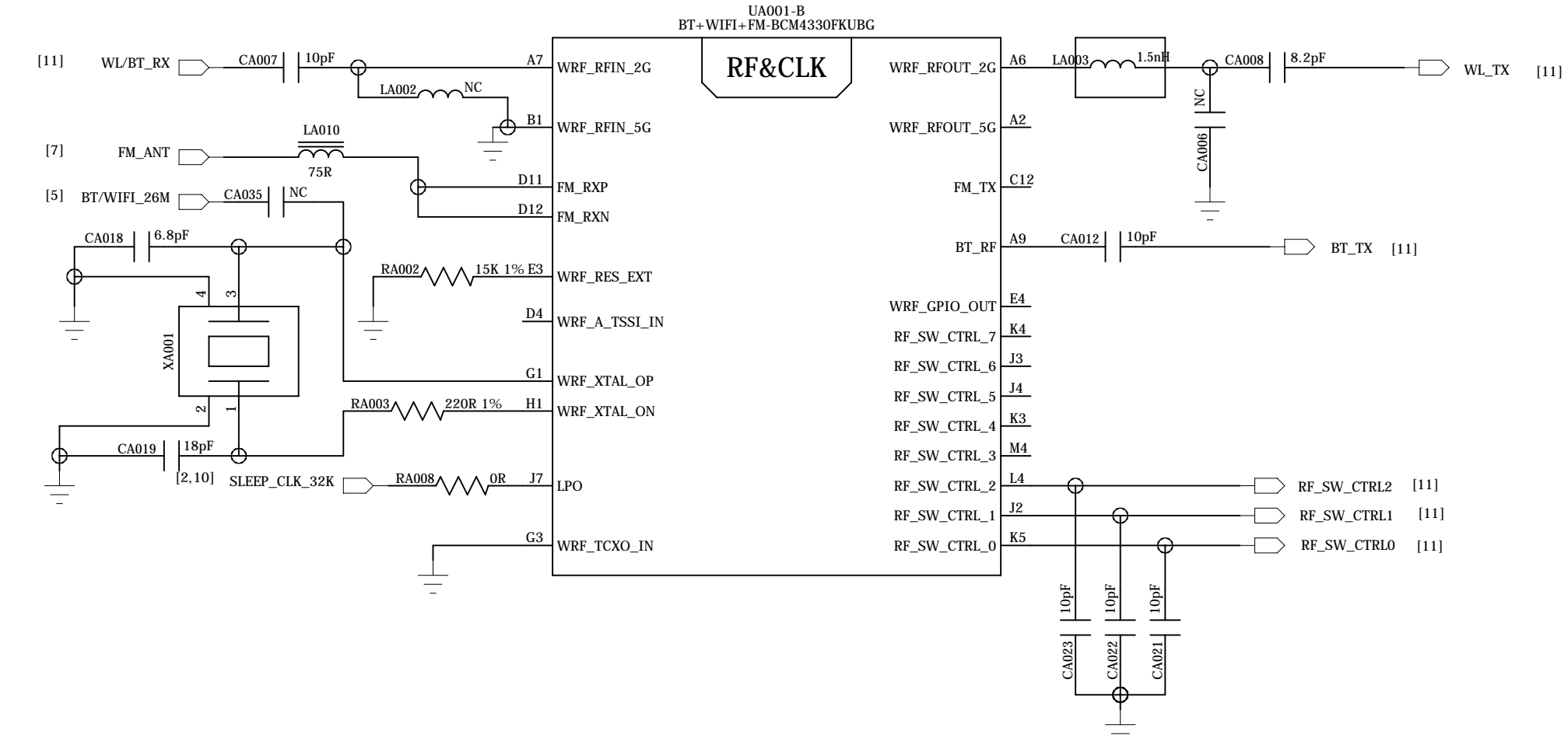


## SPI Default

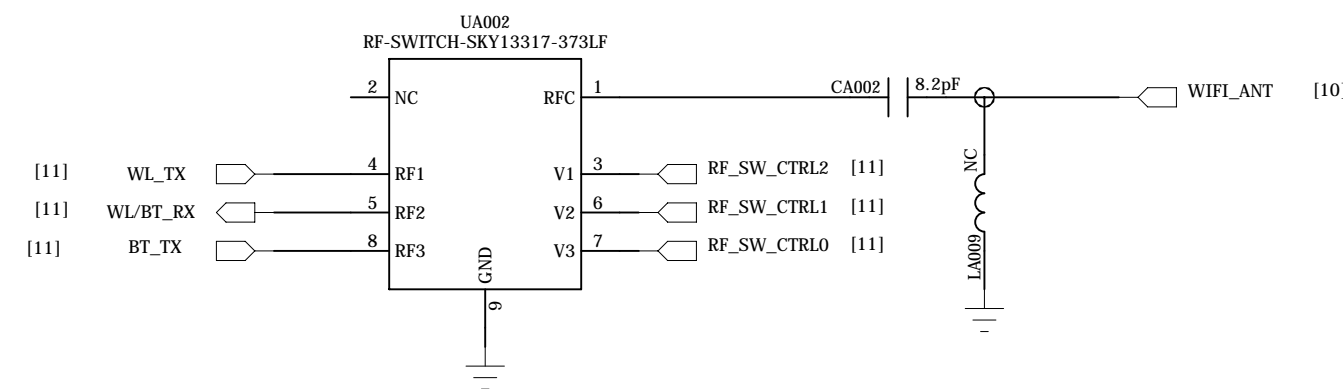
## SDIO NF



WL_GPIO_6	SDIO_DATA_2	Mode
0	X	SDIO
1	0	SPI



	RF_SW_CTRL[0:2]		
BT_TX	H	L	L
WL/BT_RX	L	H	L
WL_TX	L	L	H



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DQA:	DATED:	COMPANY: Spreadtrum communications, Inc.	
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