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TO :

Date: Nov., 20, 2008

# HannStar Product Specification (Preliminary)

Model: HSD089IFW2

-A\*\*

Note: 1.Please contact HannStar Display Corp. before designing your product based on this module specification.

- 2.The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.
- 3. The mark " \*\* " of Model means sub-model code.

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Rev. Date Sub-Model Description of change  1.0 Nov,20 2008 A** Preliminary Product Specification was first released
1.0 Nov,20 2008 A** - Preliminary Product Specification was first released



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# 1.0 GENERAL DESCRIPTION

# 1.1 Introduction

HannStar Display model HSD089IFW2-A is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 8.9 (17:10) inch diagonally measured active display area with WSVGA (1024 horizontal by 600 vertical pixel) resolution.

### 1.2 Features

- 8.9 (17:10 diagonal) inch configuration
- One channel LVDS interface
- 256K color by 6 bit R.G.B signal input
- RoHS Compliance
- Halogen Free

# 1.3 Applications

- Mobile NB
- Digital Photo frame
- Display terminal for AV application

### 1.4 General information

Item	Specification	Unit
Outline Dimension	213.36x 129.55 x 4.6 (Typ.)	mm
Display area	195.072(H) x 113.4(V)	mm
Number of Pixel	1024 RGB(H) x600(V)	pixels
Pixel pitch	0.1905(H) x 0.189(V)	mm
Pixel arrangement	RGB Vertical stripe	
Display mode	Normally white	
Surface treatment	Antiglare, Hard-Coating (3H) with EWV film	
Weight	160 (max.)	g
Back-light	Single LED (Side-Light type)	
Power Consumption	(2.5)(Max.)	W

### 1.5 Mechanical Information

	Item	Min.	Тур.	Max.	Unit
Modulo	Horizontal (H)	213.06	213.36	213.66	mm
Module Size	Vertical (V)	129.25	129.55	129.85	mm
Size	Depth (D)	_	_	4.9	mm
Weight		_		160	g



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# 2.0 ABSOLUTE MAXIMUM RATINGS

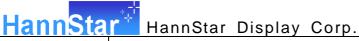
# 2.1 Electrical Absolute Rating

# 2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
LED Power Supply voltage	$V_{LED}$	-0.3	6.0	V	GND=0
Logic Supply voltage	$V_{DD}$	-0.3	6.0	V	

# 2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	$T_{opa}$	0	50	$^{\circ}\mathbb{C}$	
Storage Temperature	$T_{stg}$	-20	60	$^{\circ}\mathbb{C}$	



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# 3.0 OPTICAL CHARACTERISTICS

# 3.1 Optical specification

5.1 Optical S	-							
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		_	500	—		(1)(2)
Response tim	ne	RT		_	10	20	msec	(1)(3)
White lumina (5 points)	nce	Y <sub>L</sub>		160	200	_	cd/m <sup>2</sup>	(1)(4)(5)
	Dad	R <sub>x</sub>	⊖=0		(TBD)	_		
	Red	R <sub>Y</sub>	Normal	_	(TBD)	_		
	0	G <sub>x</sub>	viewing	_	(TBD)	_		
Color chromaticity	Green	G <sub>Y</sub>	angle	_	(TBD)	_		
(CIE1931)	Blue	B <sub>x</sub>		_	(TBD)	_		
(CIL 1931)		B <sub>Y</sub>			(TBD)	_		
	White	W <sub>x</sub>		0.260	0.310	0.360		
		$W_y$		0.280	0.330	0.380		
		$\Theta_{L}$		_	80	_		
Viewing	Hor.	$\Theta_{R}$	CD: 10	_	80	_		(1)(4)
angle	1/0"	θυ	CR>10	_	65	_		
	Ver.	θр		_	75	_		
Brightness uniformity		B <sub>UNI</sub>	⊖=0 (5point)	(80)	_	_	%	(6)
Brightness uniformity		B <sub>UNI</sub>	⊖=0 (13 points	(70)		_	%	(6)

# 3.2 Measuring Condition

■ Measuring surrounding : dark room■ Ambient temperature : 25±2°C

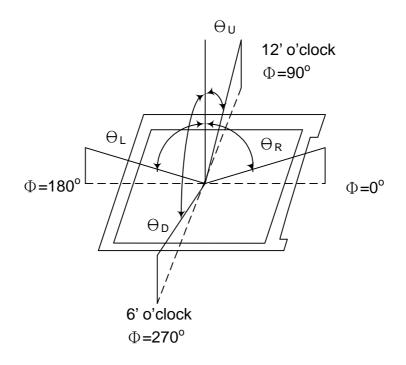
■ 15min. warm-up time.



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# 3.3 Measuring Equipment

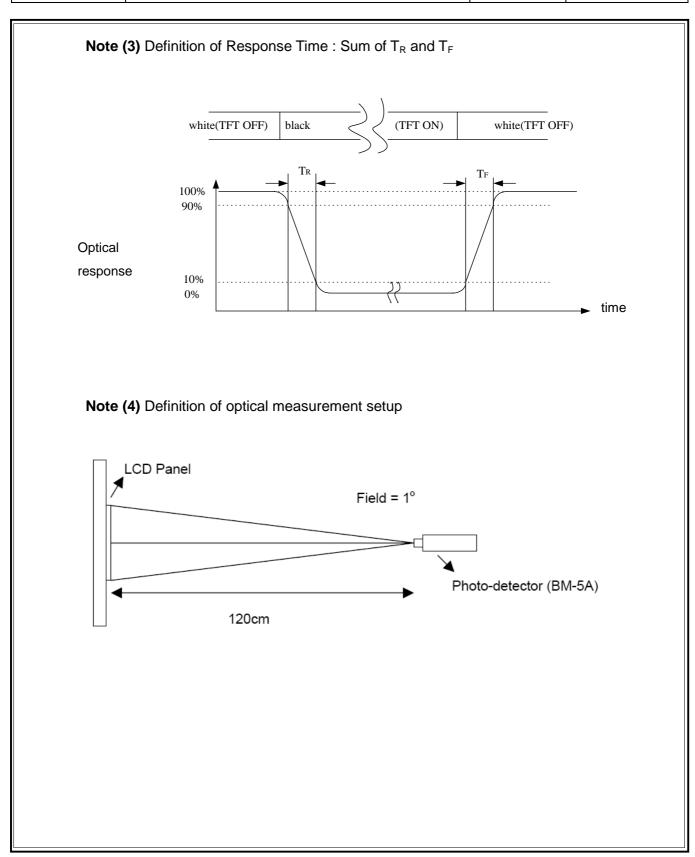
- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20 ~ 21 mm Note (1) Definition of Viewing Angle:



**Note (2)** Definition of Contrast Ratio (CR) : measured at the center point of panel

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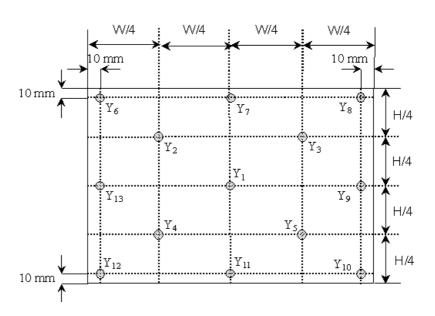




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# Note (5) Definition of Average Luminance Uniformity of White (5 Point)

Average Luminance Uniformity = 
$$\frac{Y_1+Y_2+Y_3+Y_4+Y_5}{5}$$

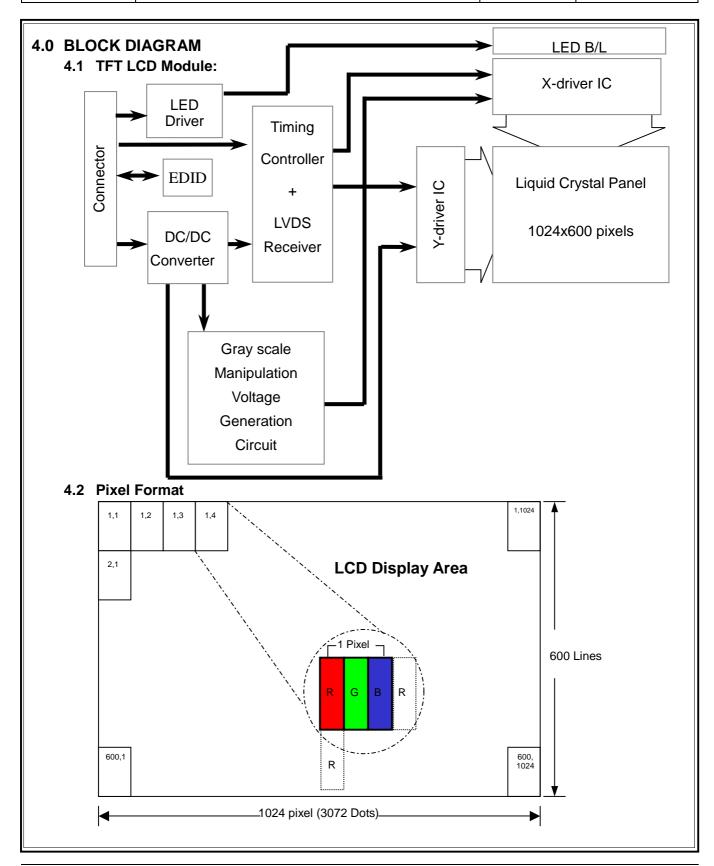


Note (6) Definition of brightness uniformity

Luminance uniformity = 
$$\frac{\text{(Min Luminance of 13 points)}}{\text{(Max Luminance of 13 points)}} \times 100\%$$

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### 4.3 Relationship Between Displayed Color and Input MSB LSB MSB LSB MSB LSB Gray scale R1 R0 G5 G4 R5 R4 R3 R2 G3 G2G1 G0B5 B4 B2 В1 Display В3 B0 level Black LIL Blue L LH Н Η Н Н Η Green LH Н Η Н Н HL L **Basic** Light Blue LH Н Н Н Η HH Η Н Η Н Н color Red Н Н Н HL LL L Purple Н Н Η HL LH Н Н Н Н Н Yellow Н Н Н Η Η L L L L L White Н Н Н Н Н H|HН Н Н Н HH Н Н Н Н Н Black L L L L LL L L L L LL L L L L L L0 L L ΗļΙ L L L LL L L L1 Dark Gray scale L3...L60 of Red Light Н Н Н Н L ΗL L L L LL L L L L L61 L L LL Н Н Н Н L L L L L L L L62 Red Н I Red L63 Η Black L L LL L0 H|IL1 LL LL L2 Dark Gray scale of L3...L60 Green Light LH Н Η H|IL61 Н LH Н Η Н LL L L L62 LH Н L Green L63 Green L L L Н Н Н H|IL L L L Black L L L L0 L L L L L L Н L L1 Dark Gray scale of L3...L60 Blue Light L L LH Н Н Н L Н L61 LH L Н Н Н Н L62 Blue LΗ Τ L L Н Η Н Η Blue L63 Black L L LIL L L L LO L Н L L HL L L Н L1 Н L2 Gray Dark scale of L3...L60 White & Black Light Η Н HH Н Н Н НН L61 Н Н Н LH Н Н Н LH Н Н Н L62 White Н Н Η Н H|HН Н Н Н Н White L63 Η H|HΗ Η Η

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# 5.0 INTERFACE PIN CONNECTION

# 5.1 TFT LCD Module:

CN1 (Input signal): MSAK24024P40 (STM or equivalent)

		·	
Pin No.	Signal	Description	
1	GND	Ground	
2	VDD	3.3V Power	
3 4	VDD	3.3V Power	
	V_EDID	3.3V Power for EDID	
5	NC	No Connection	
6	CLK_EDID	EDID Clock Input	
7	DATA_EDID	EDID Data Input	
8	RXIN0-	LVDS Signal - channel0-	
9	RXIN0+	LVDS Signal+ channel0+	
10	GND	Ground	
11	RXIN1-	Data Input channel1-	
12	RXIN1+	Data Input channel1+	
13	GND	Ground	
14	RXIN2-	Data Input channel2-	
15	RXIN2+	Data Input channel2+	
16	GND	Ground	
17	RXCLKIN-	Data Input CLK-	
18	RXCLKIN+	Data Input CLK+	
19	GND	Ground	
20	NC	NC for RXIN3- channel3-	
21	NC	NC for RXIN3+ channel3+	
22	GND	Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	VLED	LED Power Supply +5V	
30	VLED	LED Power Supply +5V	
31	VLED_GND		
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	NC	No Connection	
35	NC	No Connection	
36	NC	No Connection	
37	NC	No Connection	
38	ADJ	Adjust for LED brightness Note	
		-	



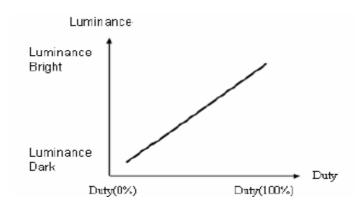
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39	NC	No Connection
40	NC	No Connection

Note: The brightness of LCD panel could be changed by adjusting ADJ

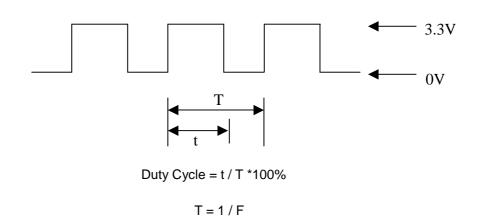
# [Note]

# (1) ADJ can adjust brightness to control Pin. Pulse duty the bigger the brighter.



# (2) ADJ Signal=0~3.3V · Operation Frequency :

Dimming Range				
PWM Frequency (F)	Duty Cycle (Min.)	Duty Cycle (Max.)		
100Hz < F < 500Hz	5%	100%		
500Hz < F < 20KHz	10%	100%		





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# **6.0 ELECTRICAL CHARACTERISTICS**

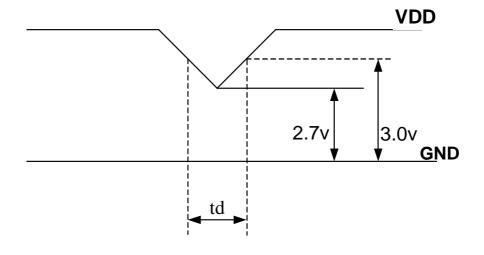
### 6.1 TFT LCD Module

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply Voltage	$V_{DD}$	3.0	3.3	3.6	V	Note (1)
Supply Voltage	$V_{LED}$	4.7	5.0	5.3	V	
Current of power supply	IDD	-	(0.18)	-	А	V <sub>DD</sub> =3.3V ⋅ L0 pattern

# Note (1) V<sub>DD</sub>-dip condition:

When VDD operating within 2.7V  $\leq$  VDD<3.0V  $^{,}$  td  $\leq$  10ms , the display may momentarily become abnormal .

VDD<2.7V, VDD dip condition should also follow the Power On/Off conditions for supply voltage.



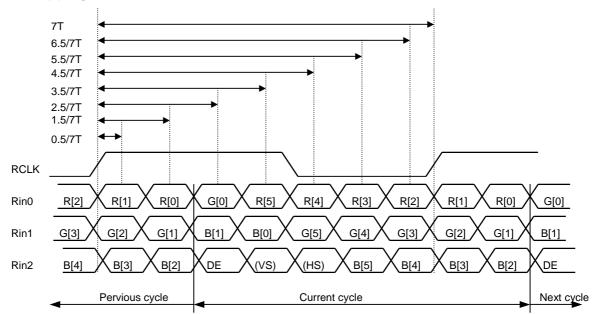


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# 6.2 Switching Characteristics for LVDS Receiver

Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
Differential Input High Threshold	Vth	_	_	100	mV	V 4.2V
Differential Input Low Threshold	VtI	-100	_	_	mV	V <sub>CM</sub> =1.2V
Input Current	I <sub>IN</sub>	-10	_	+10	uA	
Differential input Voltage	V <sub>ID</sub>	0.1	_	0.6	V	
Common Mode Voltage Offset	V <sub>CM</sub>	( V <sub>ID</sub>  /2)	1.25	1.8-0.4-( V <sub>ID</sub>  /2)	V	

# 6.3 Bit Mapping & Interface Definition



LVDS Receiver Input Timing Definition for 6bits LVDS input

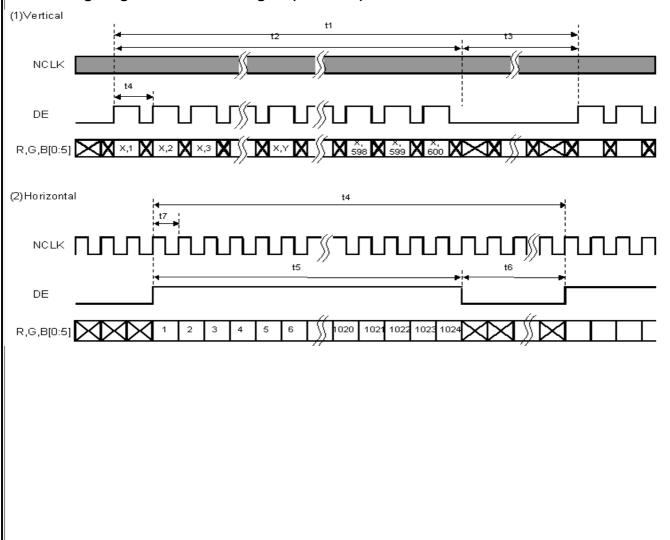
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6.4 In	terface Timing (DE mode	e)				
	Item	Symbol	Min.	Тур.	Max.	Unit
	Frame Rate		55	60	65	Hz
	Frame Period	t1	612	625	638	line
	Vertical Display Time	t2	600	600	600	line
	Vertical Blanking Time	t3	12	25	38	line

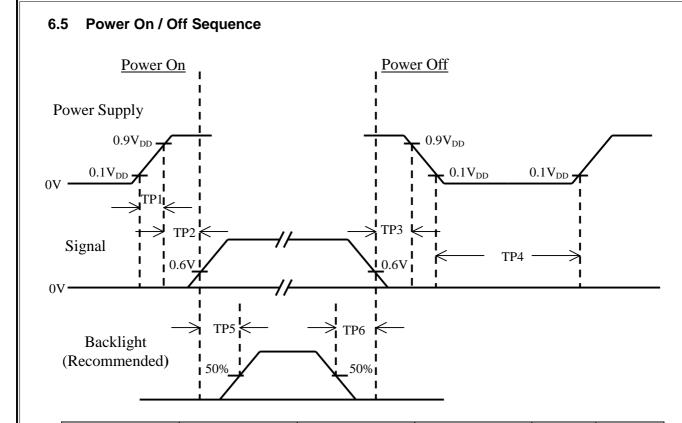
vertical bialiking fille	2	12	23	30	IIIIE
1 Line Scanning Time	t4	1160	1200	1240	clock
Horizontal Display Time	t5	1024	1024	1024	clock
Horizontal Blanking Time	t6	136	176	216	clock
Clock Rate	t7	39	45	51.42	MHz

# **Timing Diagram of Interface Signal (DE mode)**





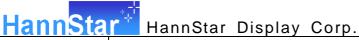
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Item	Min.	Тур.	Max.	Unit	Remark
TP1	0.5		10	msec	
TP2	0		50	msec	
TP3	0		50	msec	
TP4	500			msec	
TP5	200			msec	
TP6	200			msec	

Note: (1) The supply voltage of the external system for the module input should be the same as the definition of V<sub>DD</sub>.

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

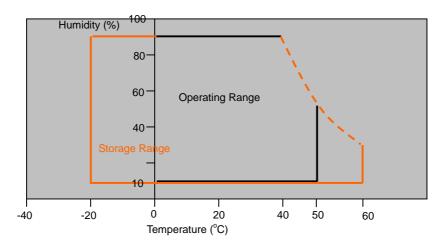


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# 7.0 Reliability test items

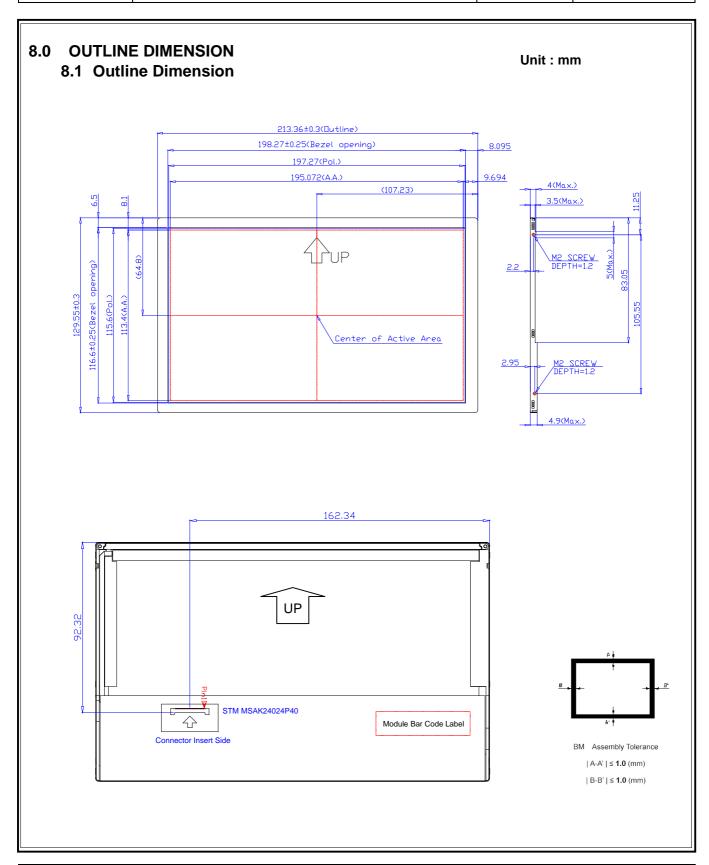
No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+60°C, 240hrs	
2	Low Temperature Storage	Ta=-20°C, 240hrs	
3	High Temperature Operation	Ta=+50°C, 500hrs	
4	Low Temperature Operation	Ta=0°C, 500hrs	
5	Thermal Cycling Test (non operation)	-20°C(30min)→+60°C(30min),100 cycles	
	Vibration	Sine Wave	
6		1.5G, 5~500Hz, XYZ	
		30min/each direction	
7	Shock	Half-Sine, 200G, 2ms, ±XYZ, 1time	

# **Storage / Operating temperature**



Note .Max wet bulb temp.=39°C

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# 9.0 LOT MARK 9.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	--

Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location.

Code 8: production year. Code 9: production month.

Code 10,11,12,13,14,15: serial number.

# Note (1) Production Year

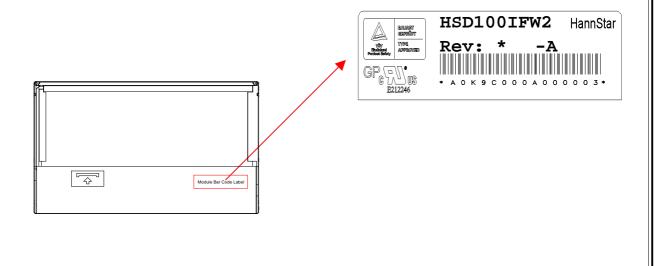
Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

# Note (2) Production Month

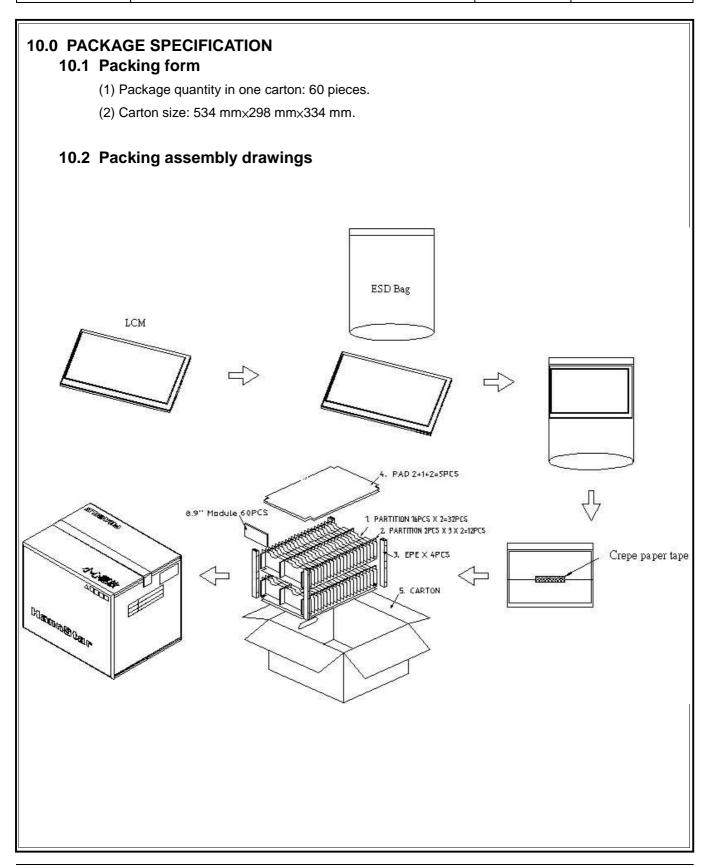
\ /												
Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

### 9.2 Location of Lot Mark

- (1) The label is attached to the backside of the LCD module.
- (2) This is subject to change without prior notice.



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### 11.0 GENERAL PRECAUTION

# 11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

# 11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

# 11.3 Breakage of LCD Panel

- 11.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

### 11.4 Electric Shock

- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

### 11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

# 11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.



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- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

### 11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

# 11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

# 11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

# 11.10 Disposal

When disposing LCD module, obey the local environmental regulations.