

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

COMMENT:			
TITLE:			
DESIGN:	DATED:	DRAWING NO:	
CHECKED:	DATED:	CODE:	REV:
QUALITY CONTROL:	DATED:	D	V4.1
RELEASED:	DATED:	SCALE:	
SHEET: 11		11	

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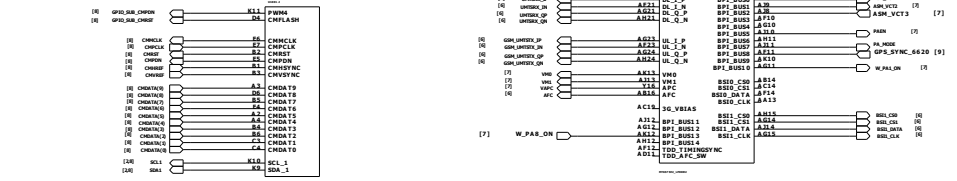
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REVISION RECORD			
LTN	ECN NO.	APPROVED	DATE

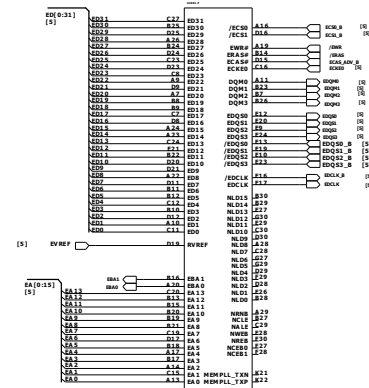
Note: APC > 3G_TX_VGA > APC > 3G_VBIAS



* Camera *

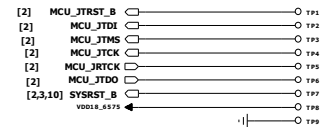
* RF *

* LCD INTERFACE *

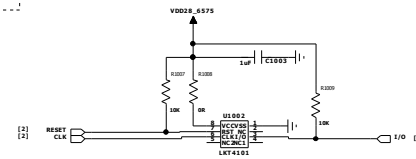


* MT6575 EMI *

Trace32

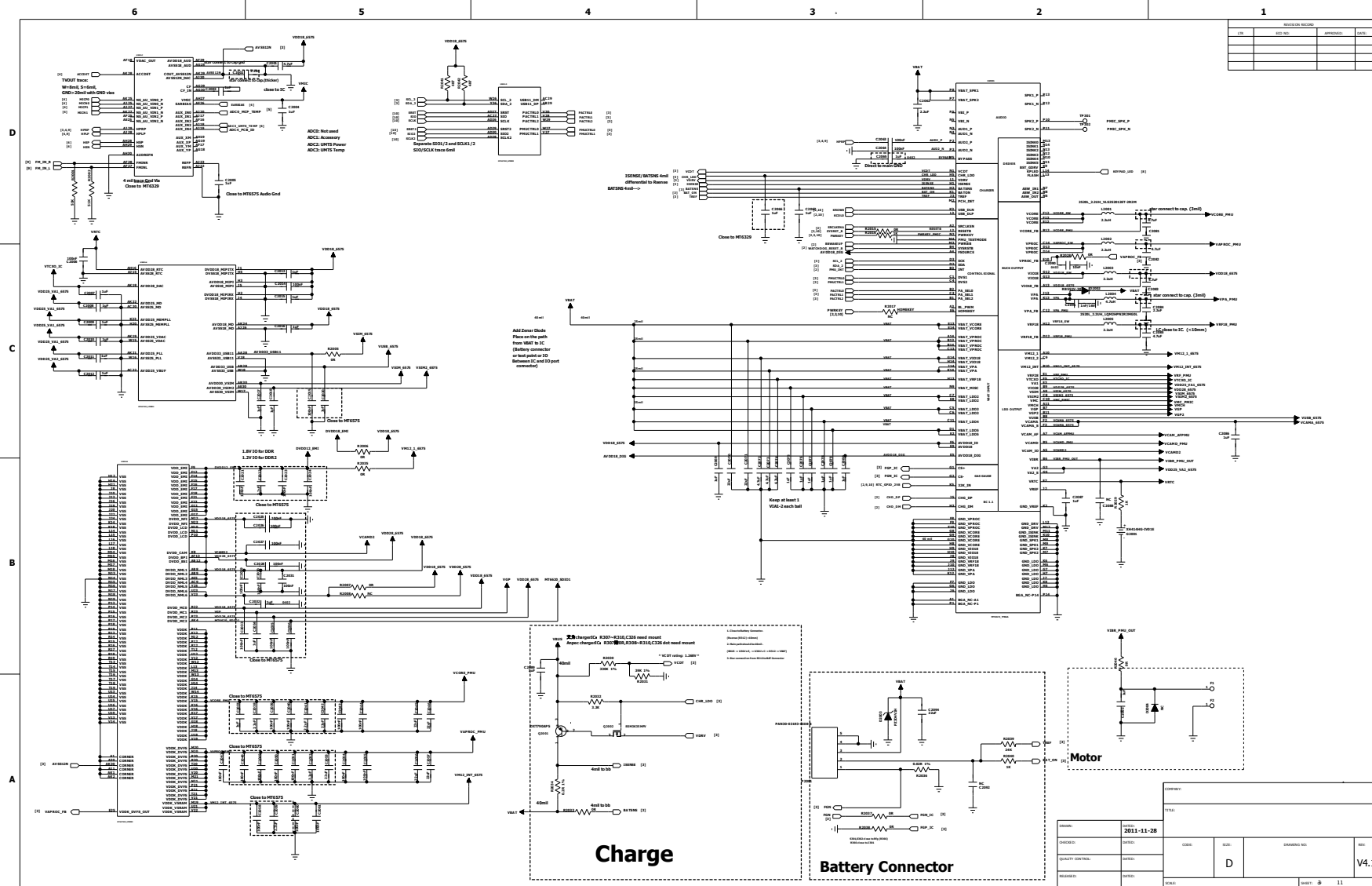


* TEST POINT *



COPYRIGHT			
TITLE			
DATE:	DATED:	REV:	
2011-11-28	2011-11-28	D	
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QUALITY CONTROL:	DATED:	DRAWING NO:	REV:
RELEASED:	DATED:	SCALE:	SHEET: 11

V4.1



	Symbol	Unit	Test (min)	External HW Config
Buck	VR7	0.7V / 2.3V (250000 msp)	300	PACEMAN
	VR6K	0.7V / 2.3V (250000 msp)	300	EXTERNA VDDC 2.1
	VR18	1.8	250	SEACURA
	VR5E	0.5V / 2.3V (250000 msp)	300	SEACURA
	VR18	1.8	10000	NT-A
Asynchronous LDO	VR7	2.50	200	SEACURA
	VR5D	2.50	40	SEACURA
	VR6M	1.5V / 2.5V / 2.8	200	SEACURA
	VR1	1.5V / 2.5V / 2.5	200	SEACURA
	VR2	2.5V / 2.8	100	NT-A
	VR13	1.2	200	
	VR13_C1	1.2	50	
	VR13_C2	1.20	20	
	VR18	2.8	100	
	VR18	2.8	100	
Digital LDO	VR18	1.8V / 2.8	100	
	VR5E	0.5V	300	
	VR5D	0.5V / 2.3V / 2.8 / 2.5	100	
	VR5M	0.5V / 2.3V / 2.8 / 2.5	100	
	VR5C	0.5V	100	
	VR5C	0.5V / 2.3V / 2.8 / 2.5 / 2.5	100	
	VR5C	0.5V / 2.3V / 2.8 / 2.5 / 2.5	100	
	VR5C	0.5V / 2.3V / 2.8 / 2.5 / 2.5	100	
	VR5D	0.5V	100	
	VR5D	0.5V	100	
Vibrator	VR18	1.8V / 2.3V / 2.5V / 2.8V / 2.5	200	
	VR18	1.8V / 2.3V / 2.5V / 2.8V / 2.5	200	
	VR18	1.8V	200	

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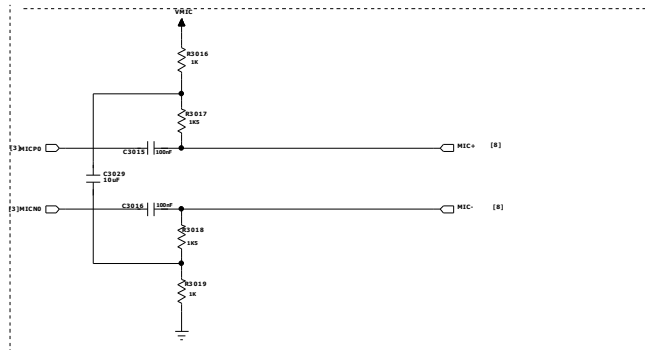
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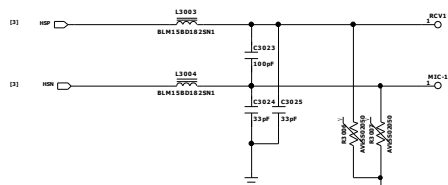
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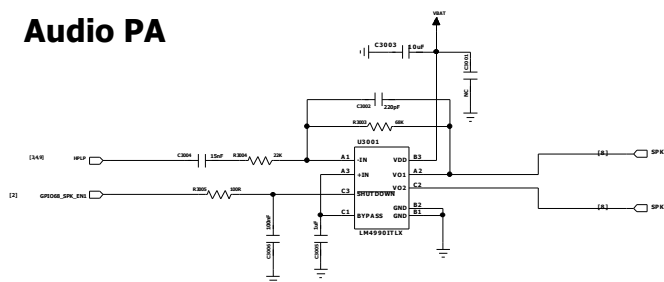
RECEIVER



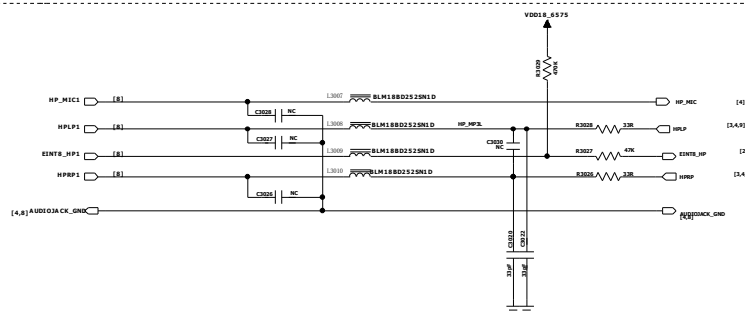
IO CONNECTOR

default on : UART1

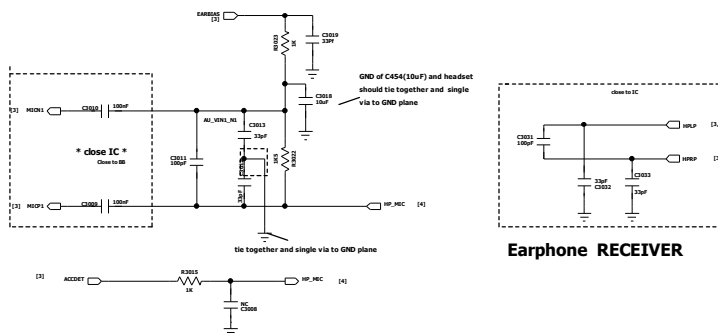
Audio PA



SPEAKER



VIBRATOR(on sub board)

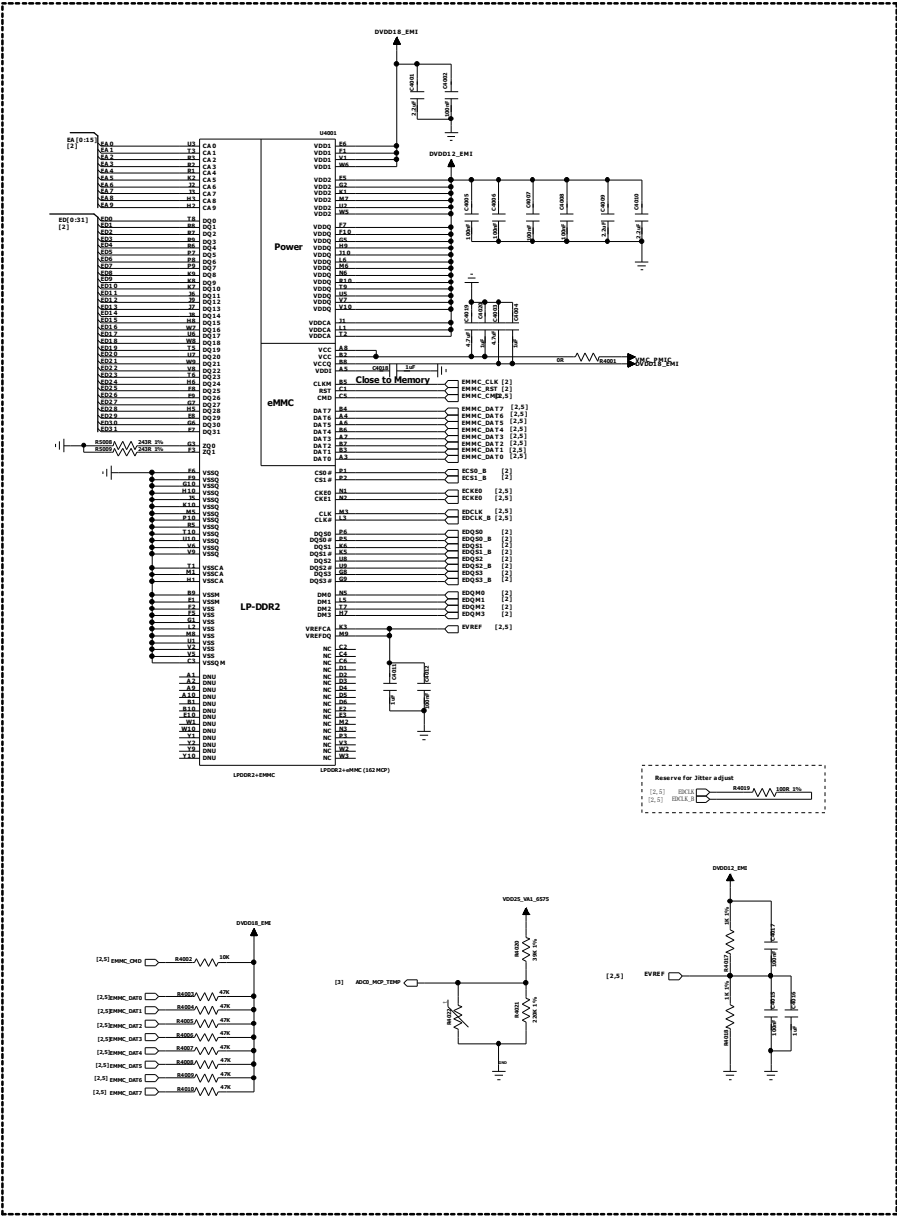
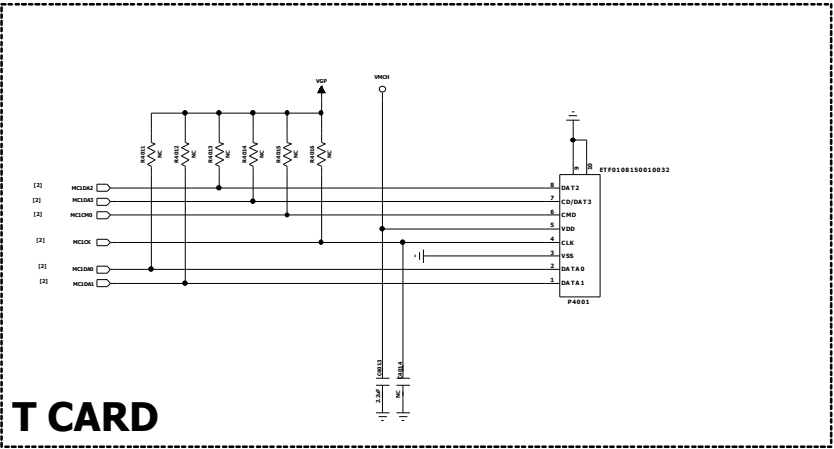


F3.5 Earphone

REVISION RECORD			
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DRAWN:			
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QUALITY CONTROL:			
RELEASED:			
DATE:			
CODE:			
SIZE:			
DRAWING NO:			
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D 03_AUDIO_I/O_USB/MOTOV4.1			
SCALE:			
SHEET: 4 11			

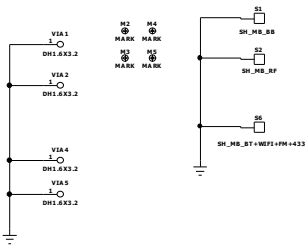
REVISION RECORD			
LTB	ECO NO.	APPROVED	DATE



DRAWN:		DATED:		2011-11-28	
CHECKED:		DATED:			
QUALITY CONTROL:		DATED:			
RELEASED:		DATED:			
CODE:		SIZE:		DRAWING NO:	
D		04_MEMORY/T_CARD		REV:	
SCALE:		SHEET:		5 11	

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COMPANY:			
TITLE:			
CODE:	SIZE:	DRAWING NO:	REV:
	D		V4.1
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DRAWN:	DATED: 2011-11-28
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QUALITY CONTROL:	DATED:
RELEASED:	DATED:

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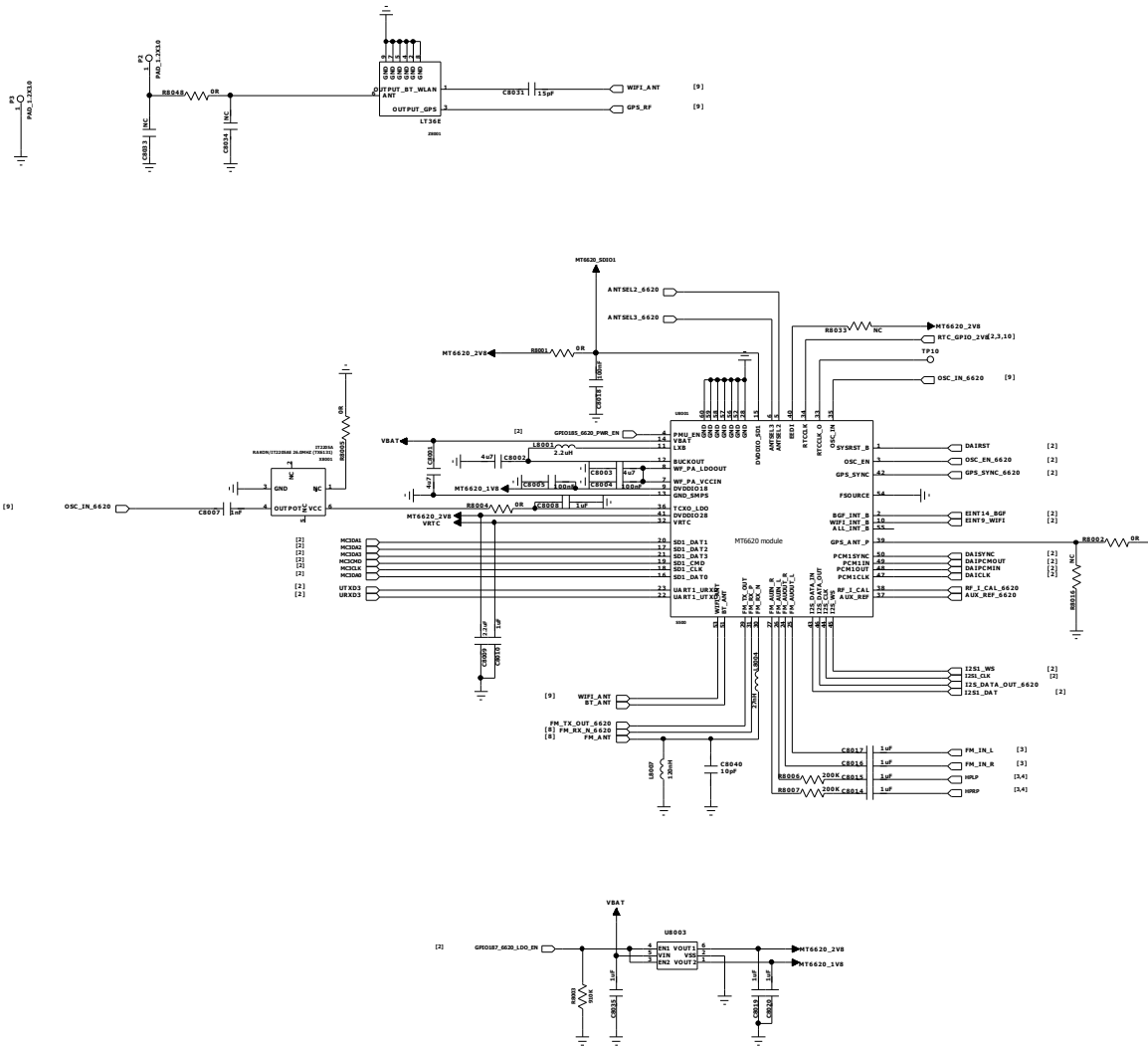
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REVISION RECORD			
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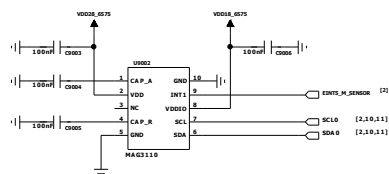
clock setting	XTTEST	EE04	ANTSEL_3	
	0	0	0	Default
	1	1	1	
	0	0	1	
WiFi host interface	XTTEST	ANTSEL_2	ANTSEL_1	
	0	0	0	Default
	0	1	1	
	0	1	1	
BT/common host interface	XTTEST	ANTSEL_U		
	0	0		Default
	0	1		
	0	1		

COMPANY:			
TITLE:			
CODE	SIZE	DRAWING NO.	REV
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SCALE:			

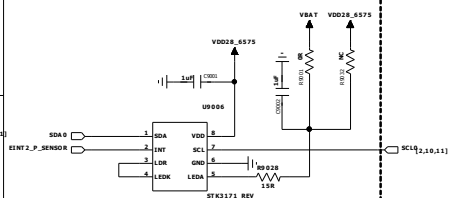
DRAWN:	DATED:
	2011-11-28
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

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M Sensor

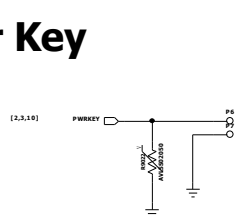


M-Sensor

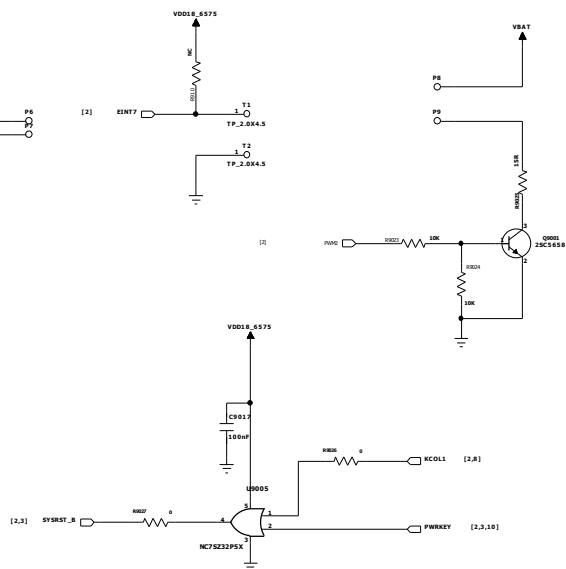
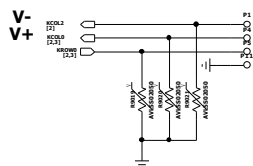


ALS & PS

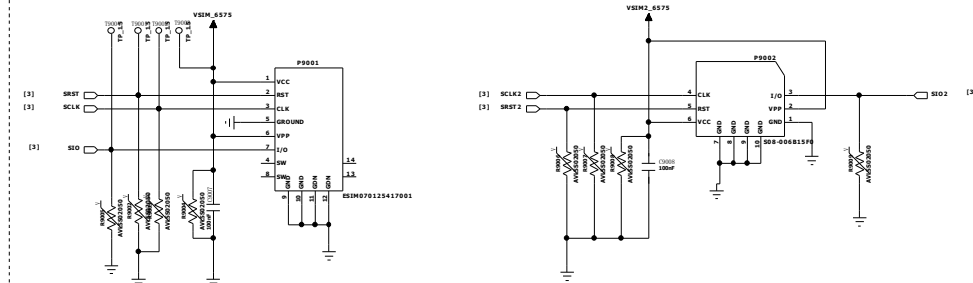
Power Key



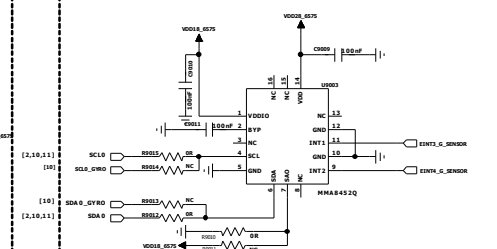
SIDE KEY



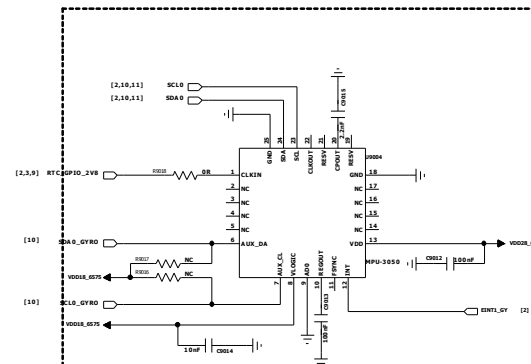
SIM & T-card connector



G Sensor



G-Sensor



Gyro Sensor(Delete)

DESIGN:	DATE:	<div style="text-align: center;"> <h1 style="margin: 0;">D <Drawing Number> V4.1</h1> </div>			
CHECKED:	DATE:				
QUALITY CONTROL:	DATE:				
RELEASED:	DATE:				
2011-11-28		CODE:	SIZE:	DRAWING NO.	REV:
		SCALE: SHEET: 40 11			

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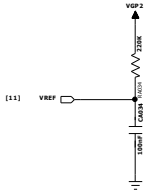
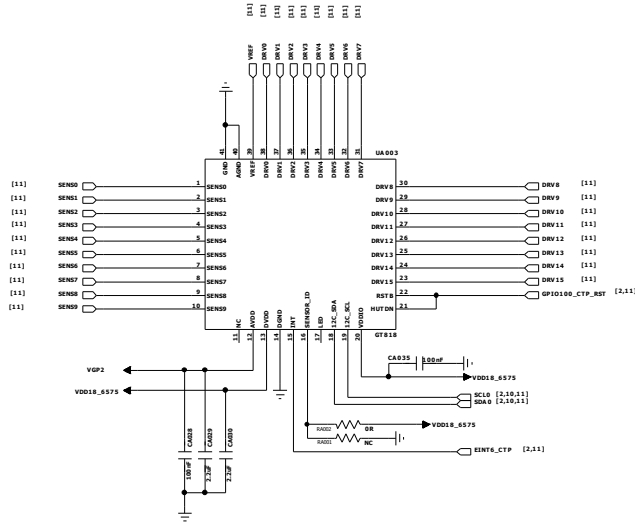
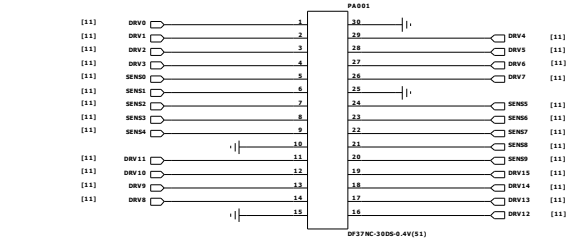
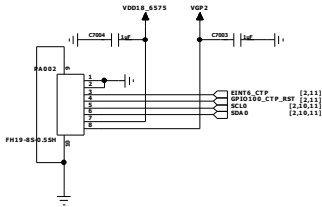
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COMPANY:			
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CODE:	SIZE:	DRAWING NO:	REV:
D <Drawing Number> V4.1			11
SCALE:			