

6

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4

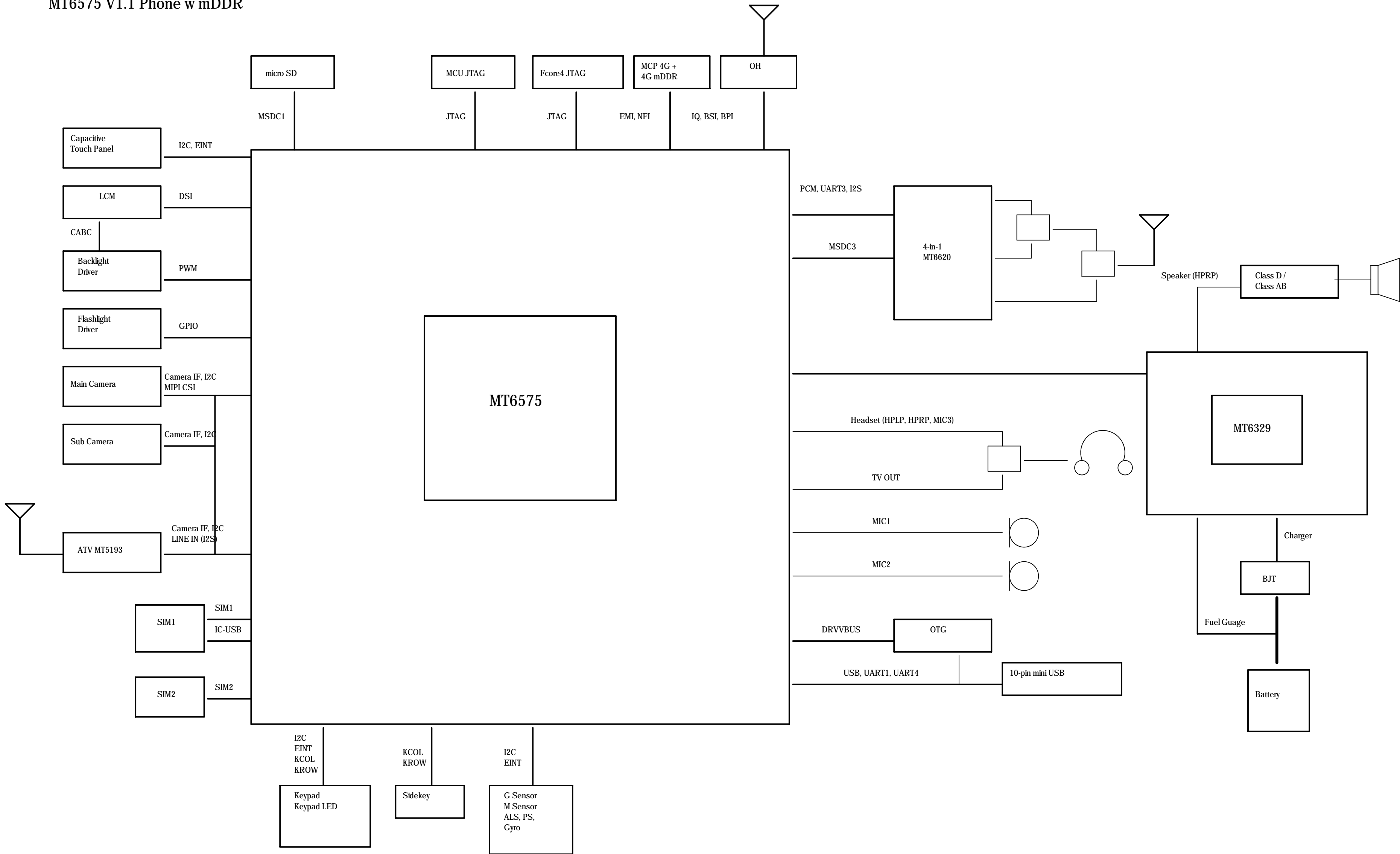
3

2

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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

MT6575 V1.1 Phone w mDDR



Check

B

A

D

C

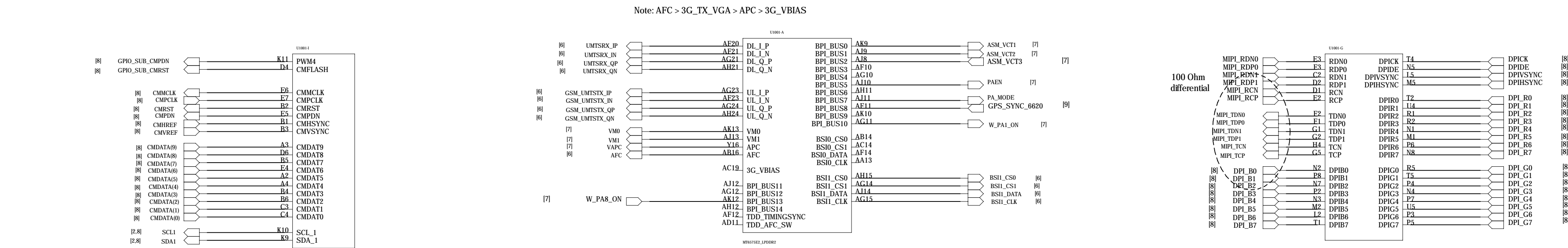
B

A

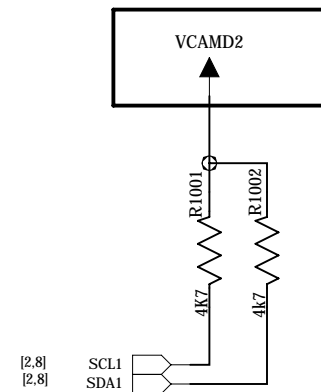
DRAWN:	DATED:
<Drawn By>	2011-11-28
CHECKED:	DATED:
<Checked By>	<Checked Date>
QUALITY CONTROL:	DATED:
<QC By>	<QC Date>
RELEASED:	DATED:
<Released By>	<Release Date>

COMPANY:			
TITLE:			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	D	00_BLOCK_DIAGRAM	V4.1
SCALE: <Scale>			SHEET 11 of 11

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



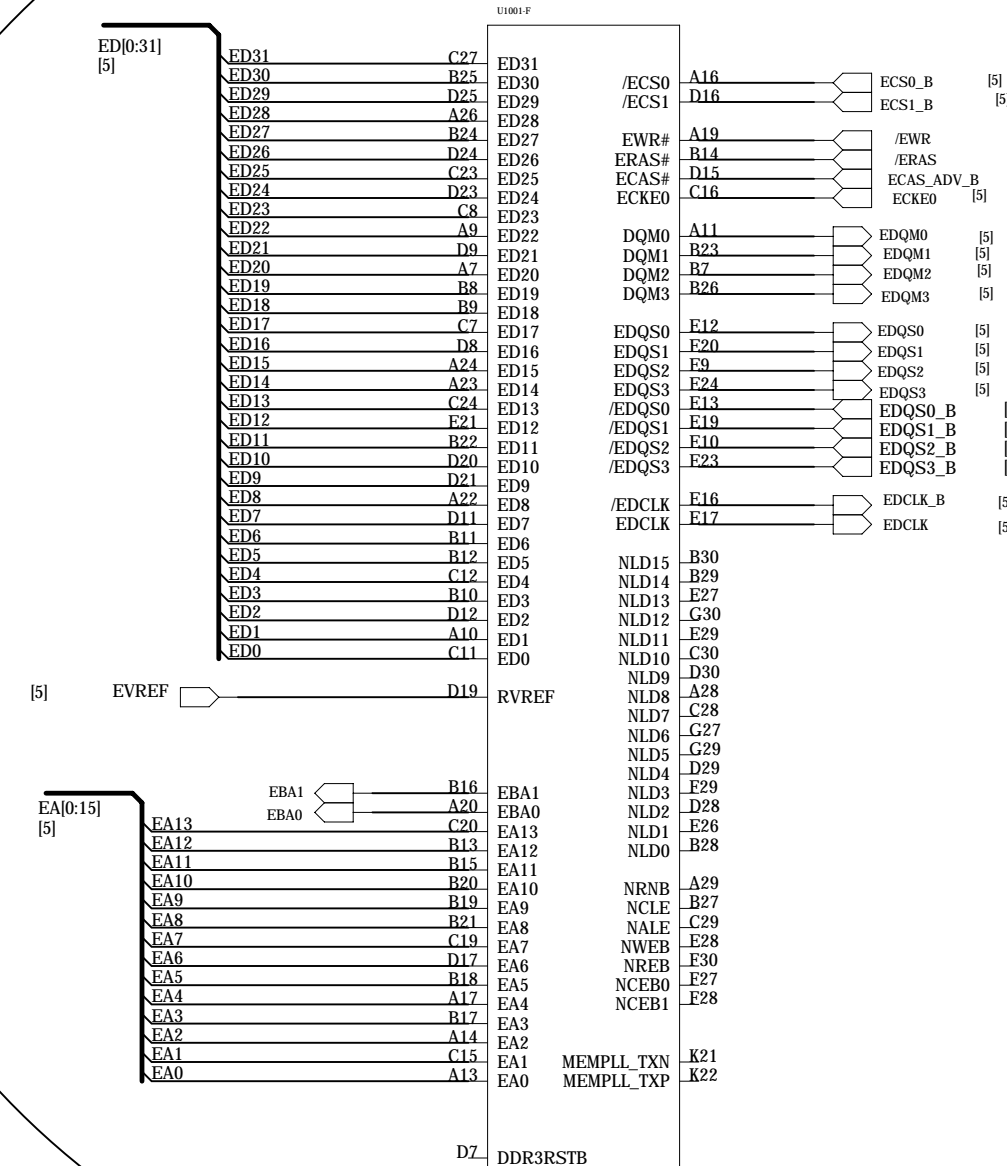
Need to turn off if I2C1 devices not operated



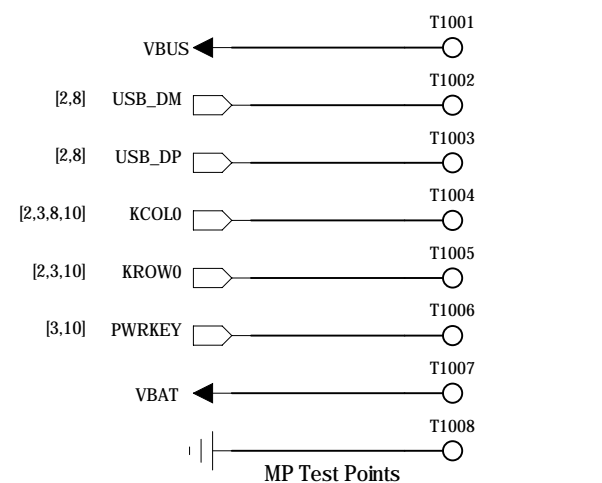
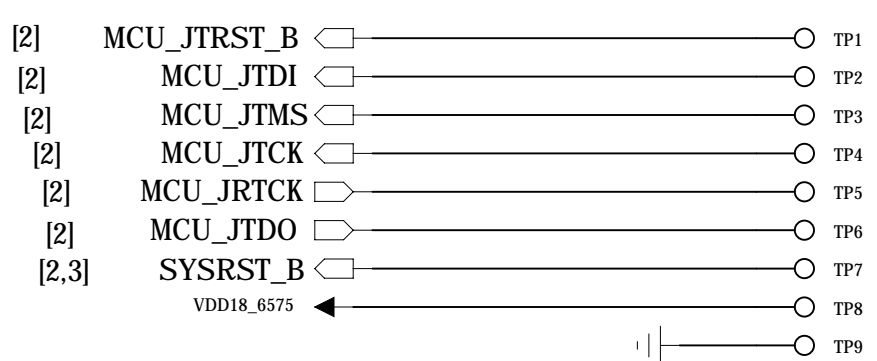
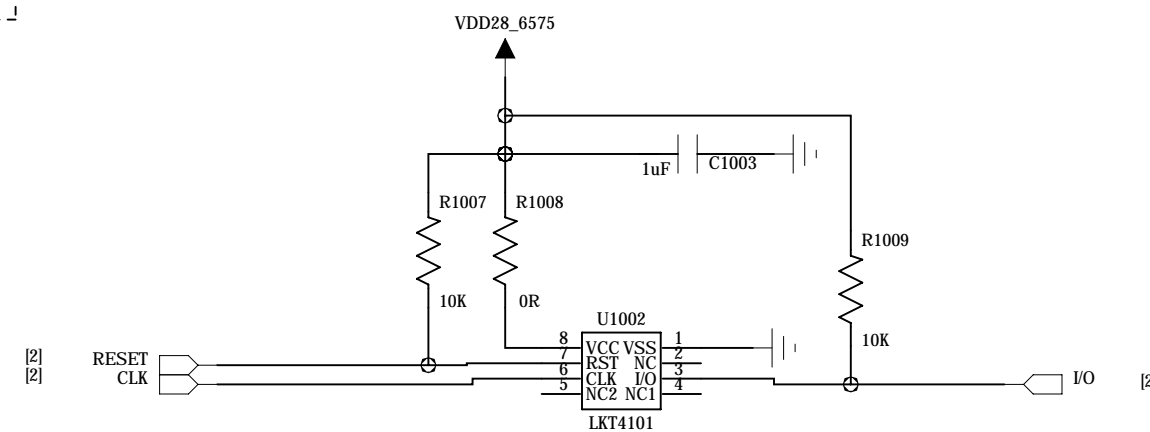
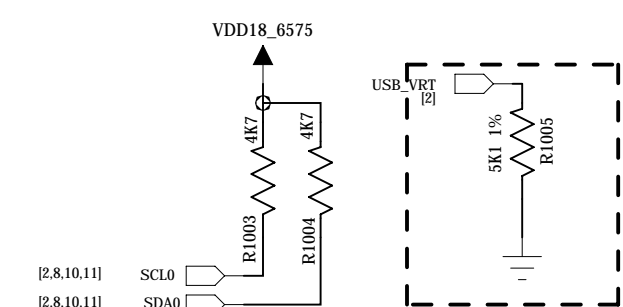
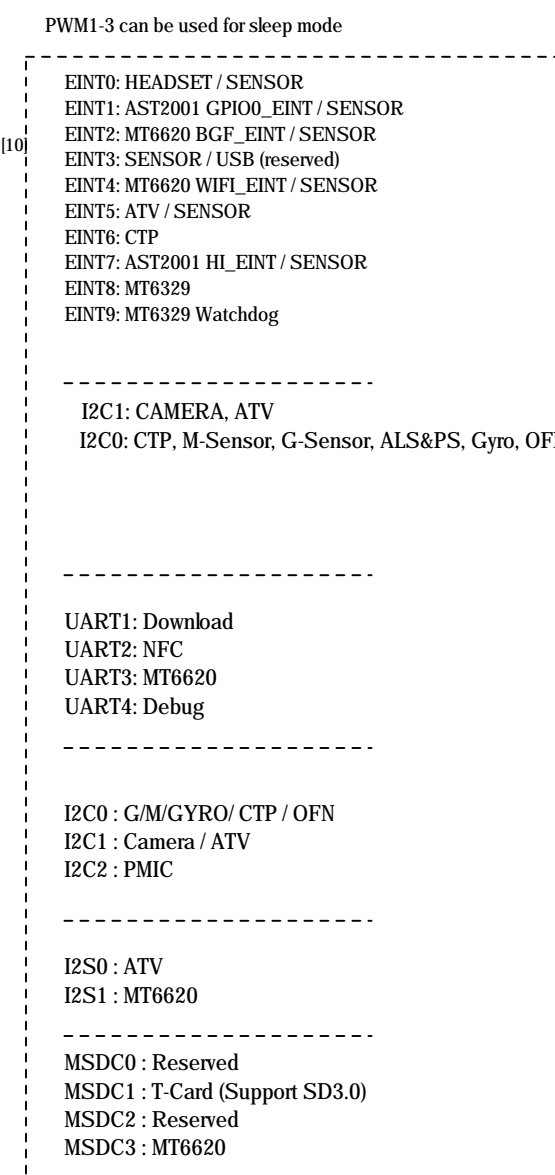
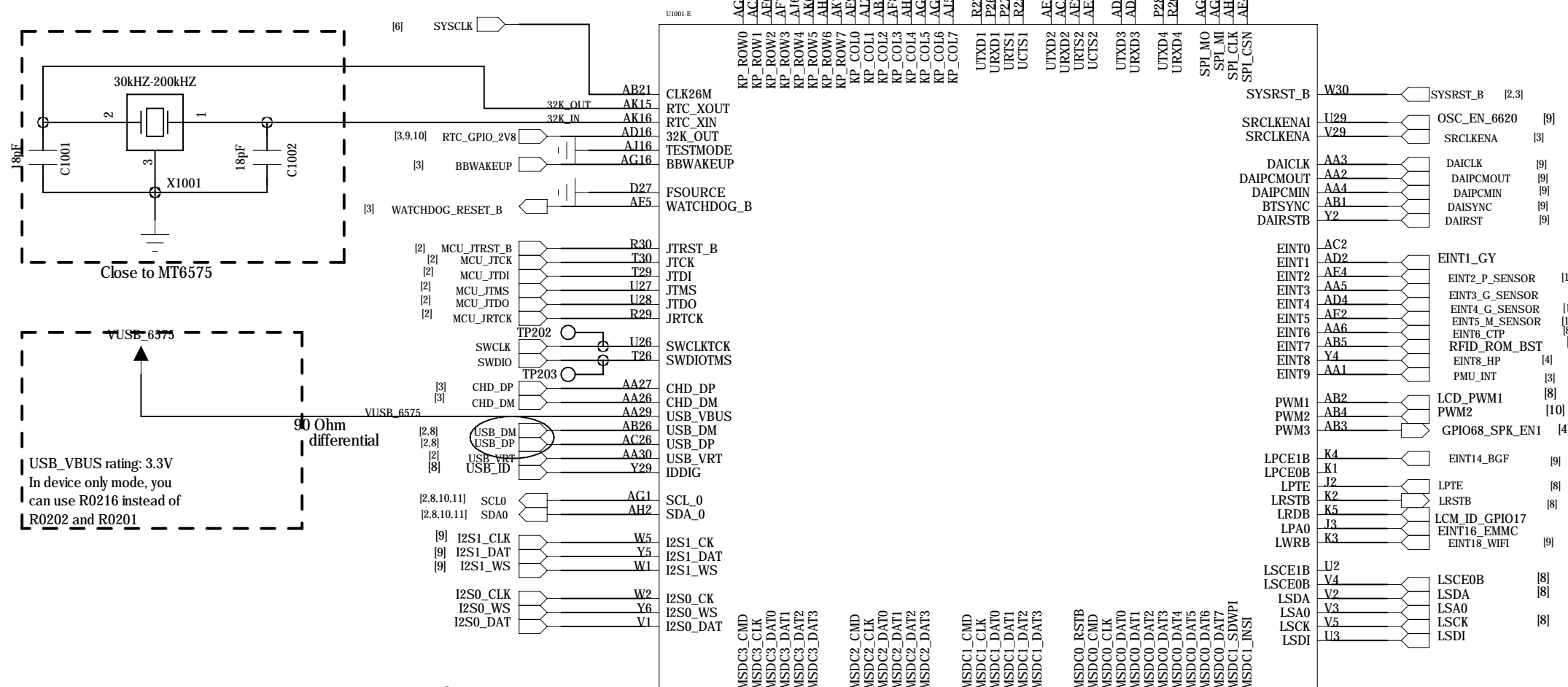
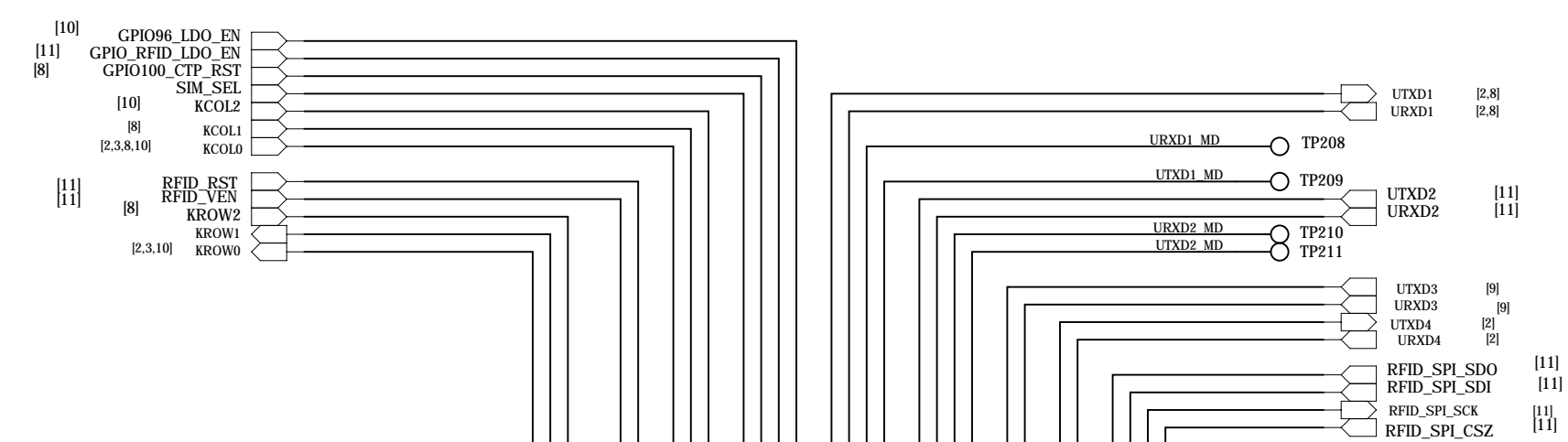
\* Camera \*

**\* RE \***

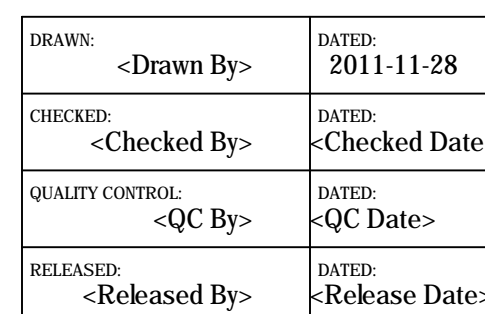
## \* LCD INTERFACE \*



\* MT6575 EMI \*



**\* TEST POINT \***



DATED:  
2011-11-28

CHECKED: <Checked By>	DATED: <Checked Date>
--------------------------	--------------------------

DATED:  
<Checked Date

QUALITY CONTROL:	DATED:
<QC By>	<QC Date>

QC Date

<Released By>	<Release Date>
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Release Date:

**COMPANY:**

**TITLE:**

CODE:

SIZE:

DRAWING NO:

REV:

V4.1

SCALE: &lt;Scale&gt;

SHEET: 2F 11

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

D

C

B

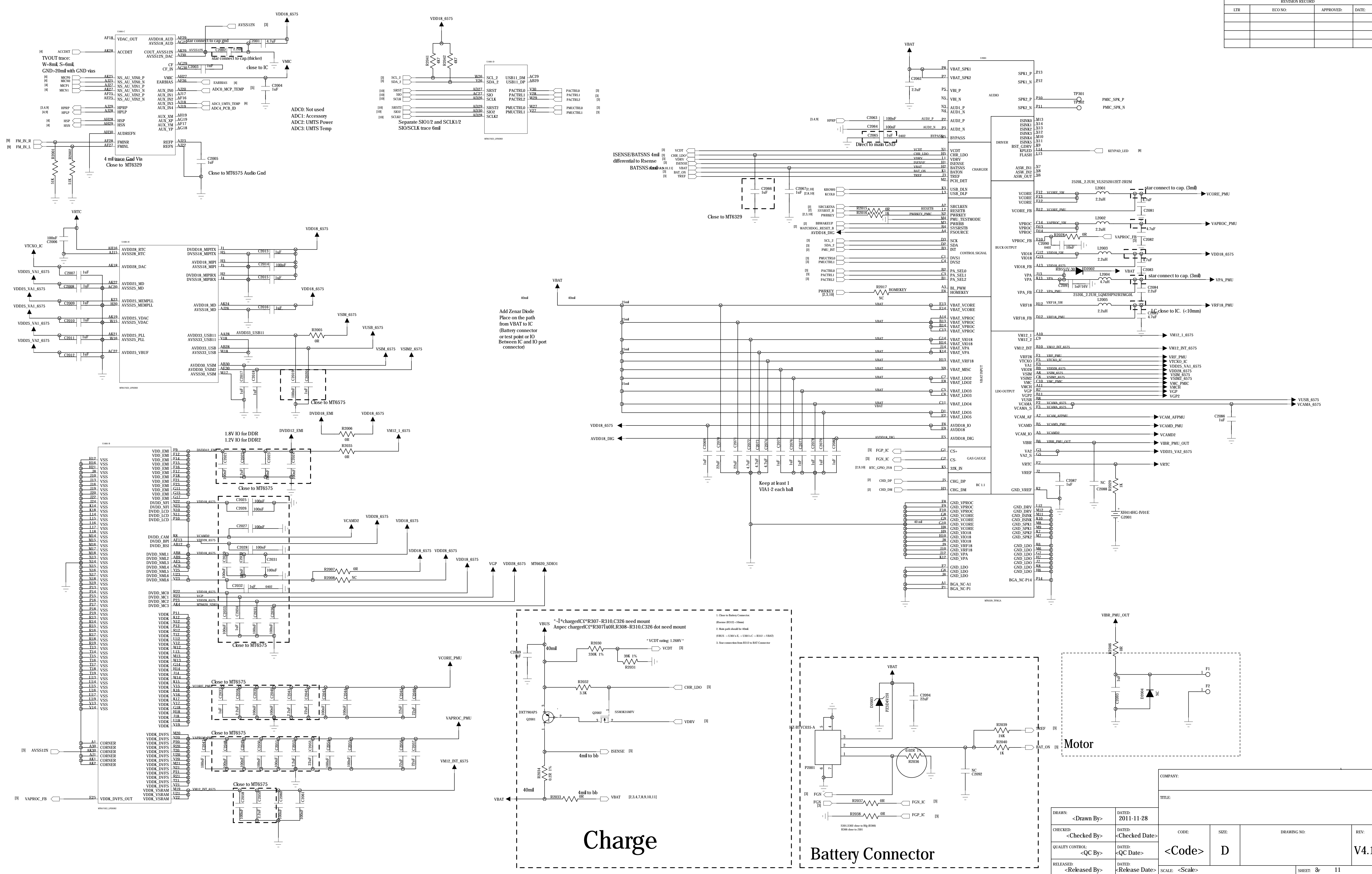
A

D

C

B

A



Charge

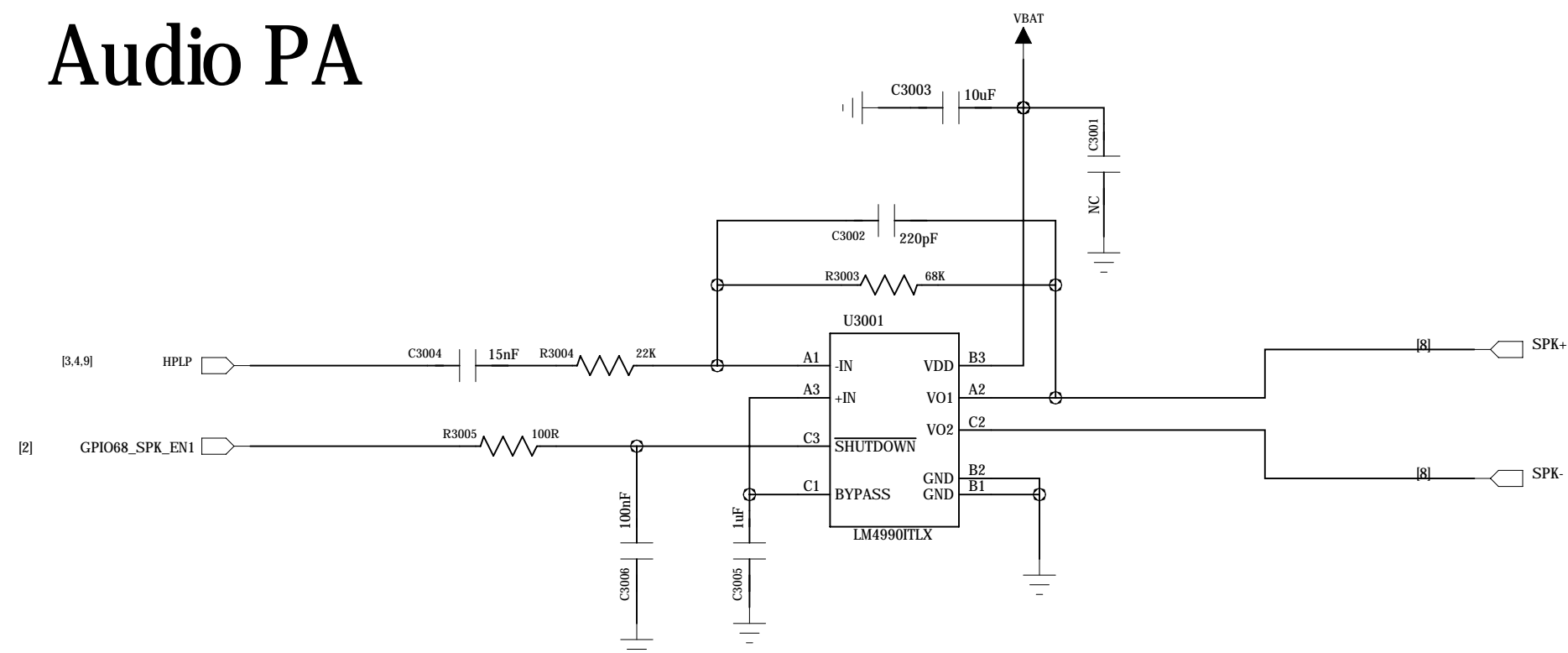
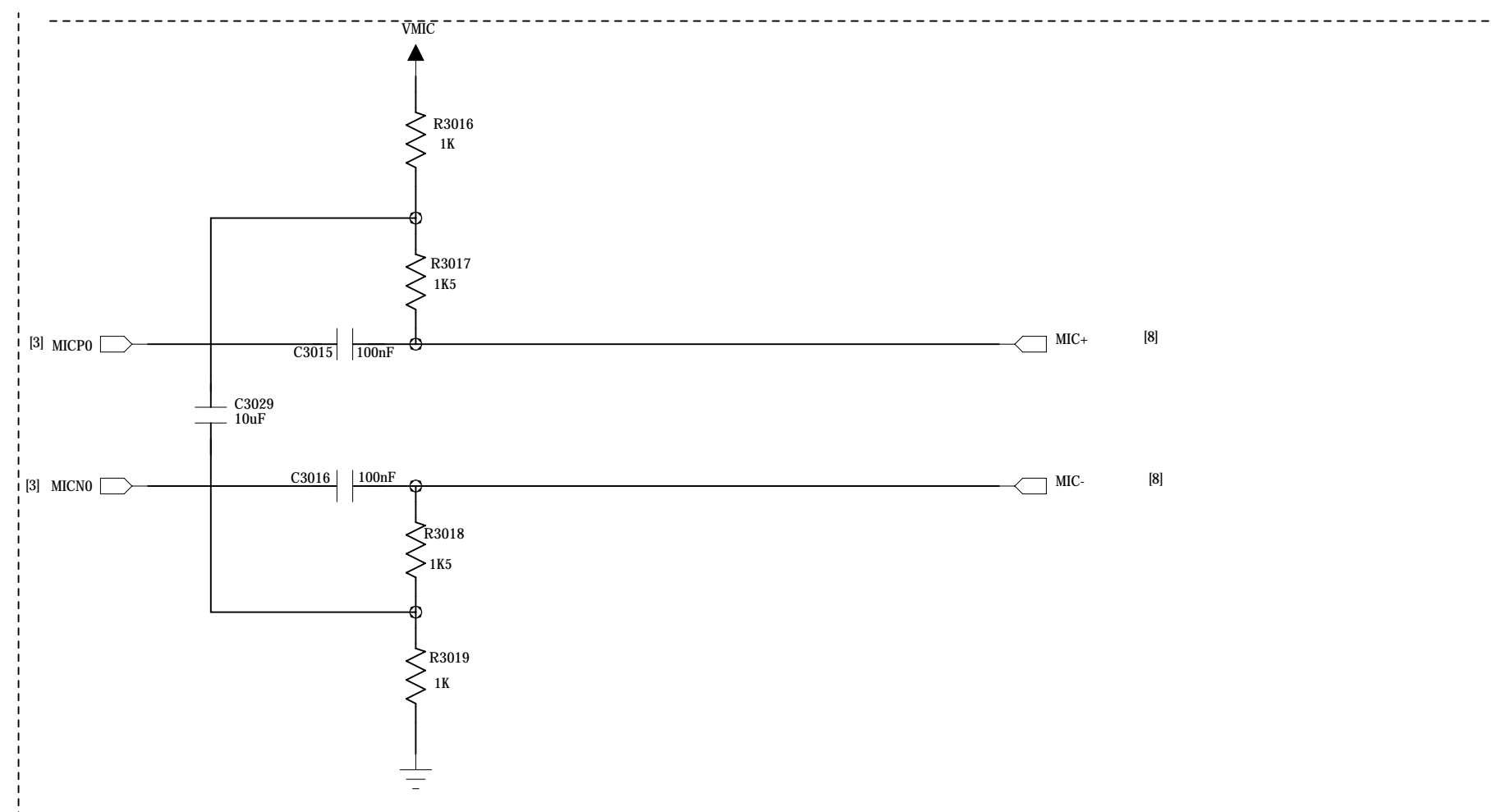
Battery Connector

Motor

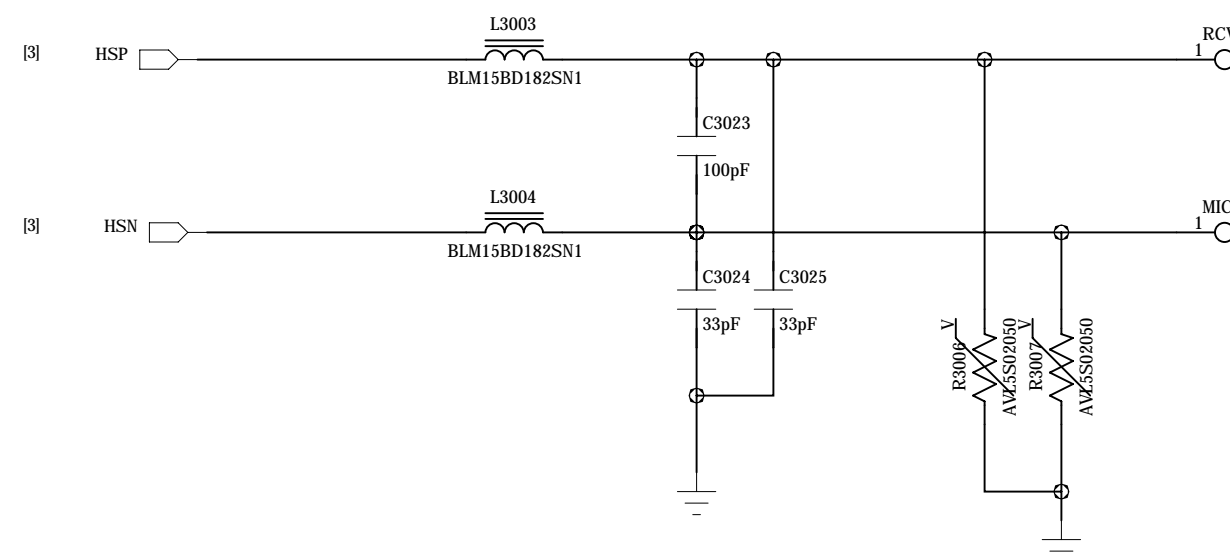
DRAWN:	<Drawn By>	DATED:	2011-11-28
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:		TITLE:	
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	D		V4.1
SCALE: <Scale>		SHEET 3f 11	

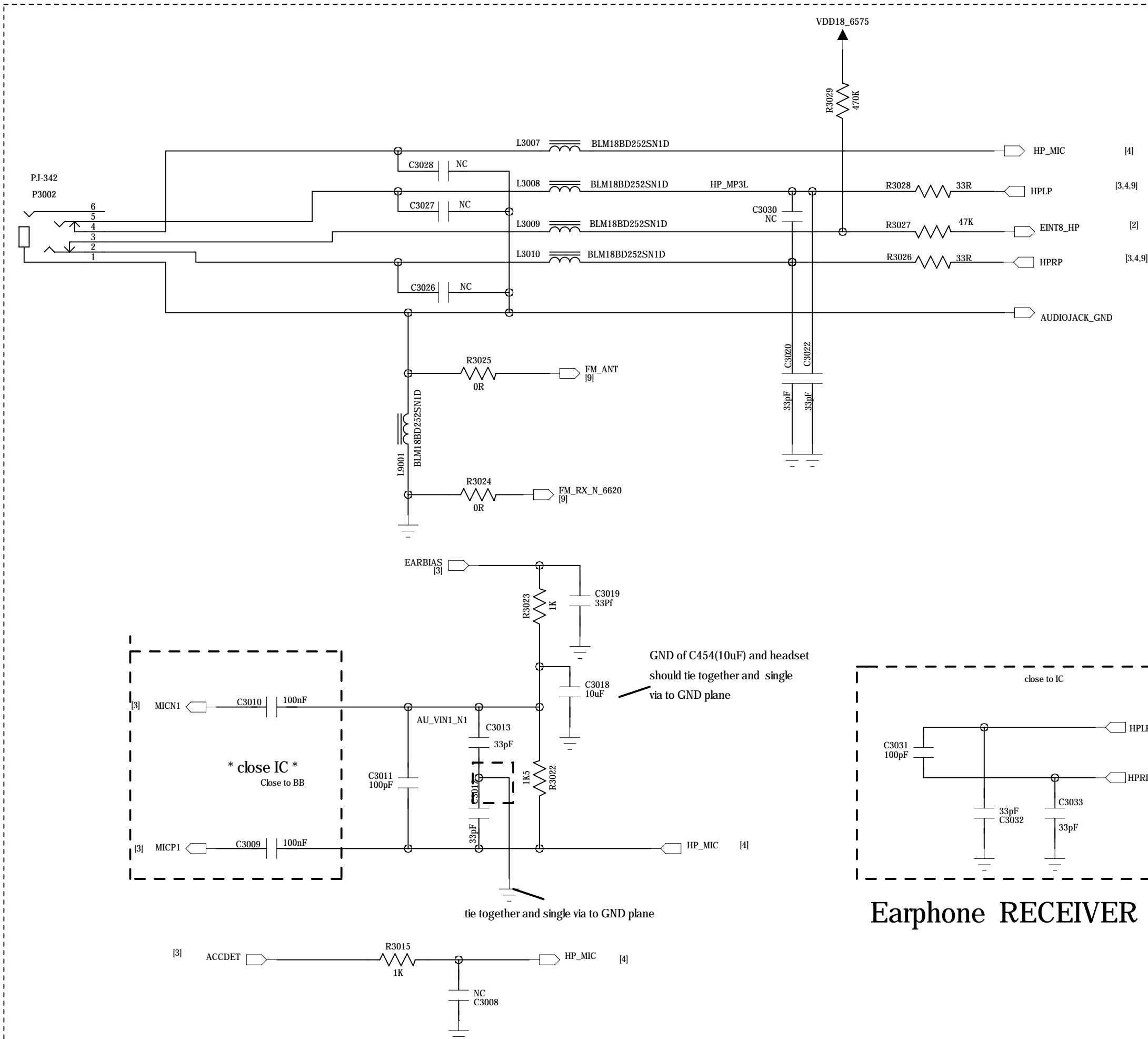
REVISION RECORD			
I/JR	ECO NO:	APPROVED:	DATE:



## RECEIVER



**SPEAKER**



## Earphone RECEIVER

## Earphone MICPHONE

### F3.5 Earphone

VIBRATOR(on sub board)

default on : UART1

# IO CONNECTOR

		COMPANY:			
		TITLE:			
DRAWN: <Drawn By>	DATED: 2011-11-28				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	D 03	_AUDIO_I/O_USB/MOT	V4.1
RELEASED: <Released By>	DATED: <Release Date>				
		SCALE: <Scale>	SHEET: 46		11

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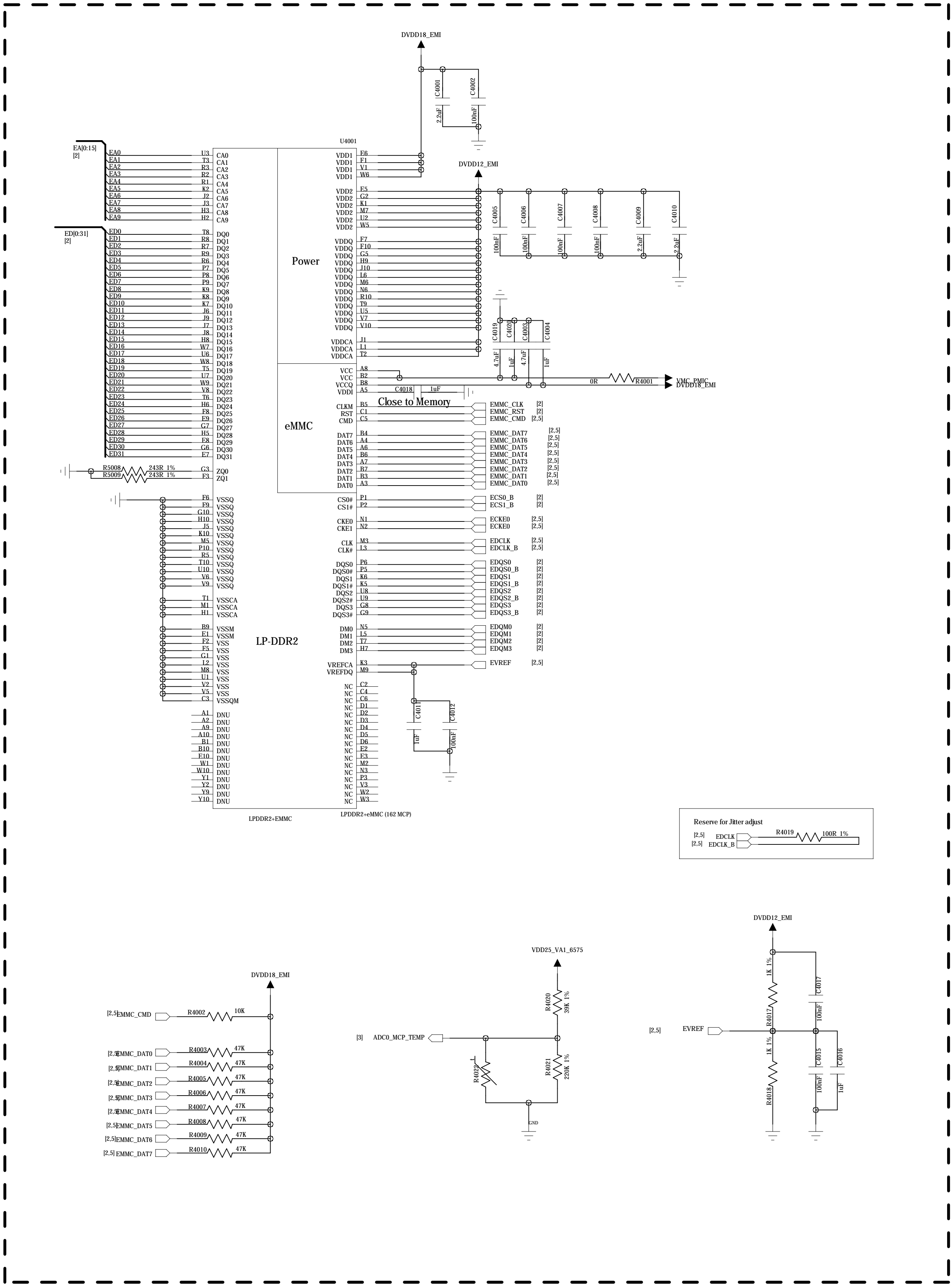
4

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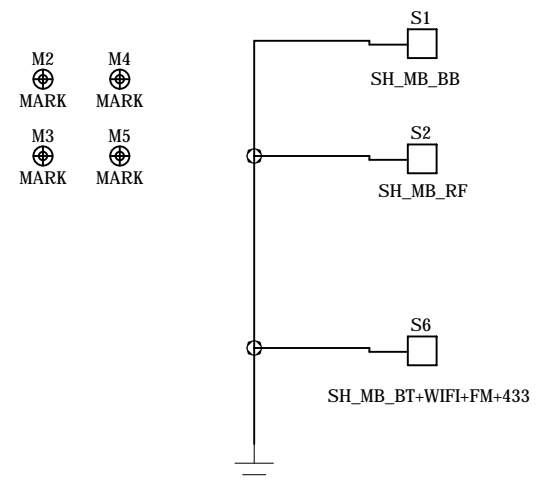
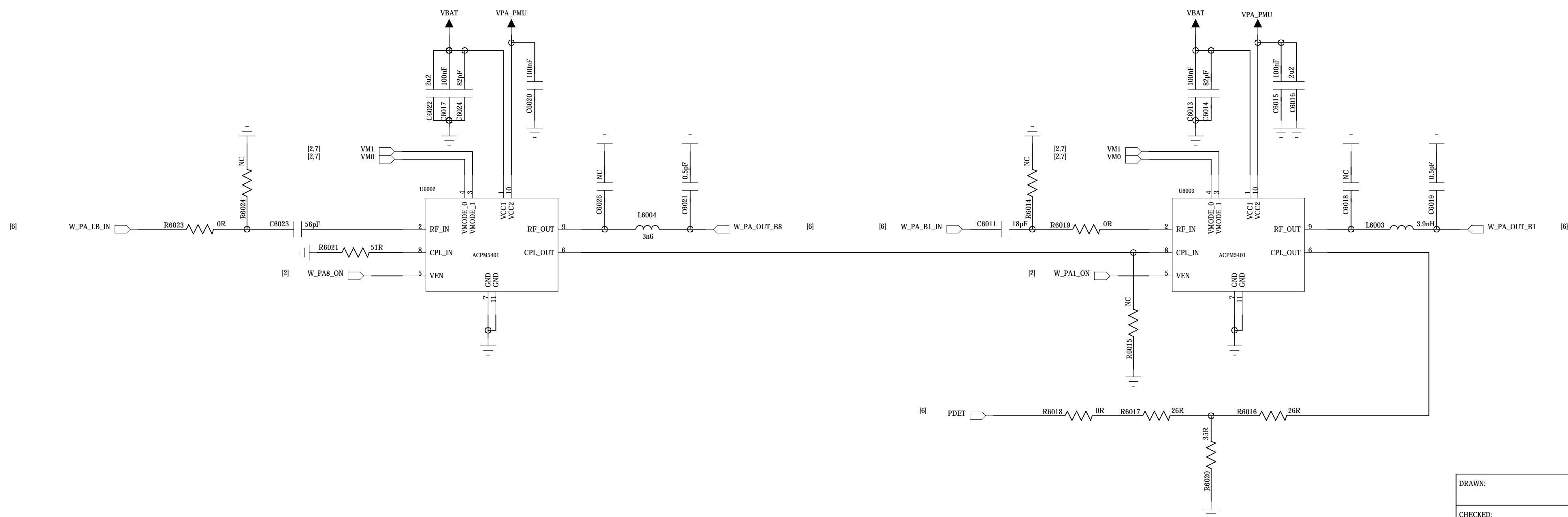
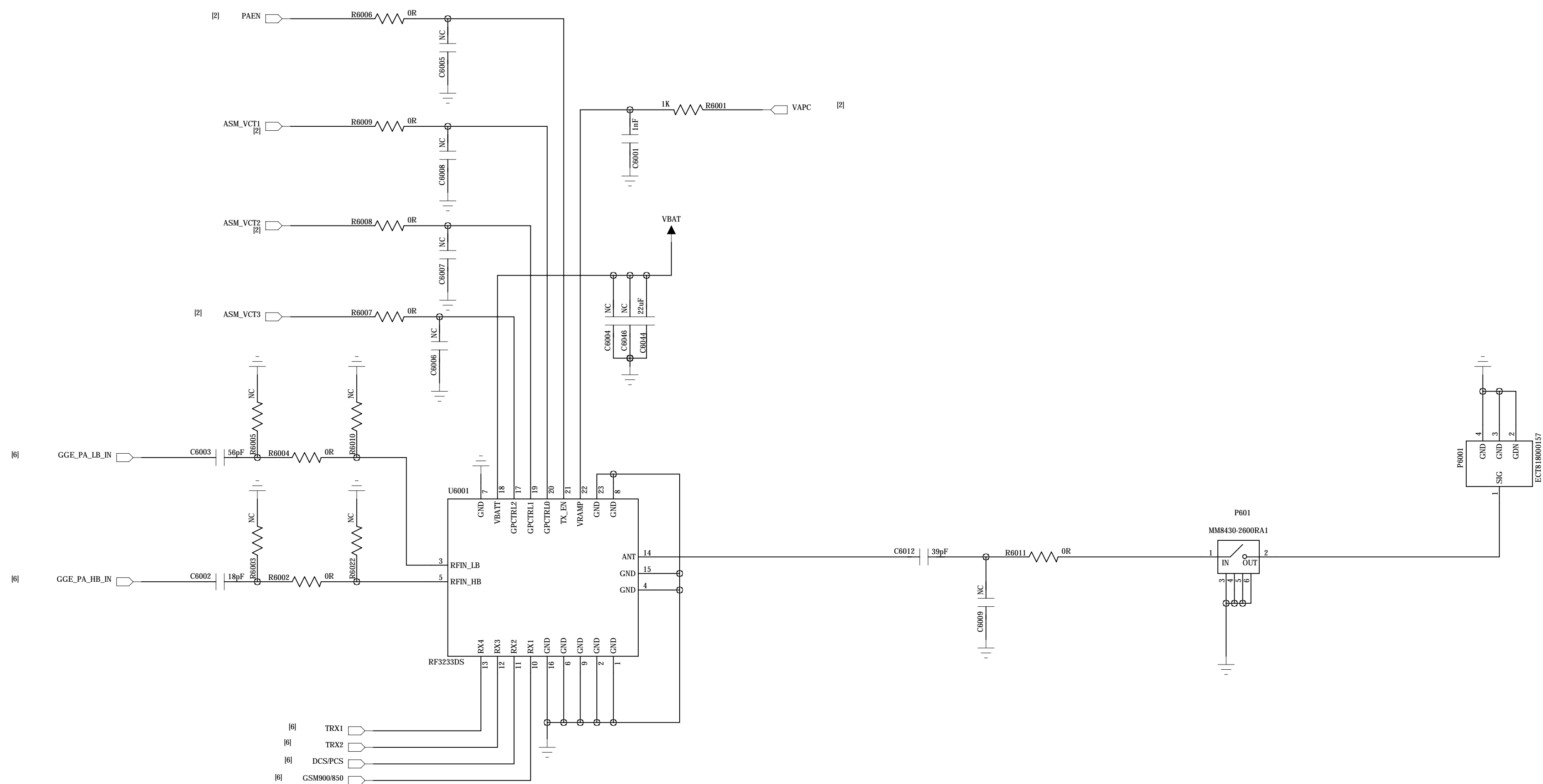
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:





## RF\_TX\_ASM

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN:	DATED: 2011-11-28				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	D		V4.1
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: ㉞ 11

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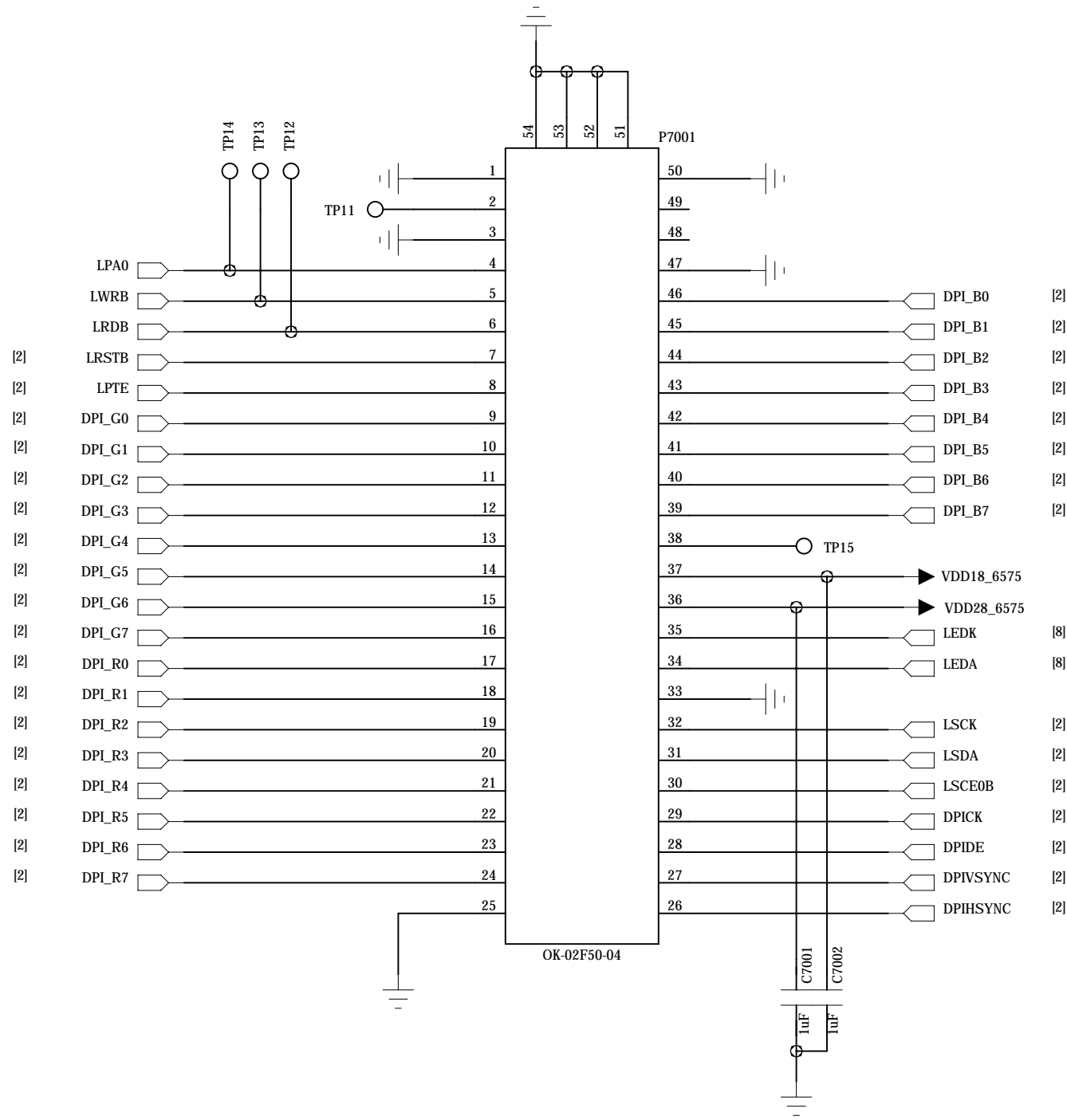
4

3

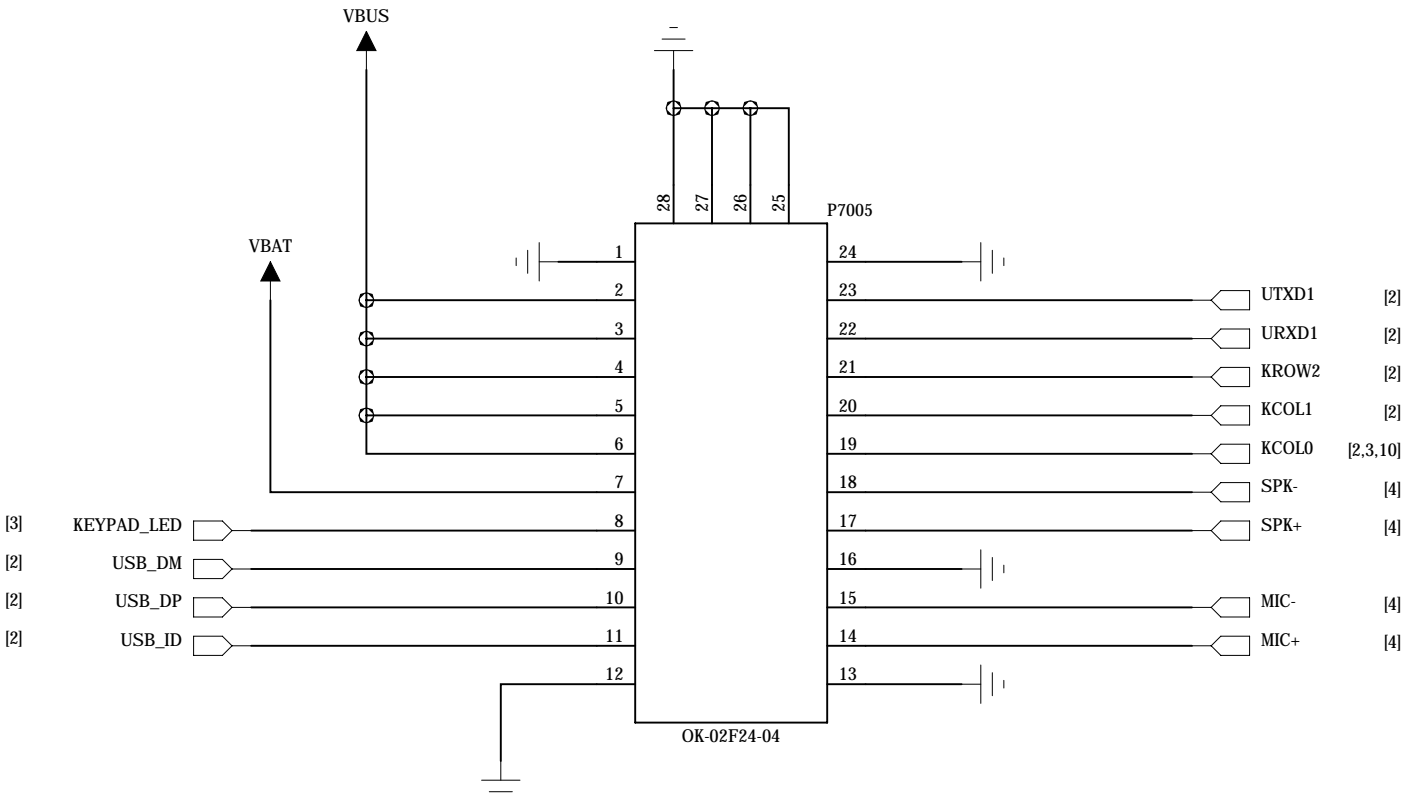
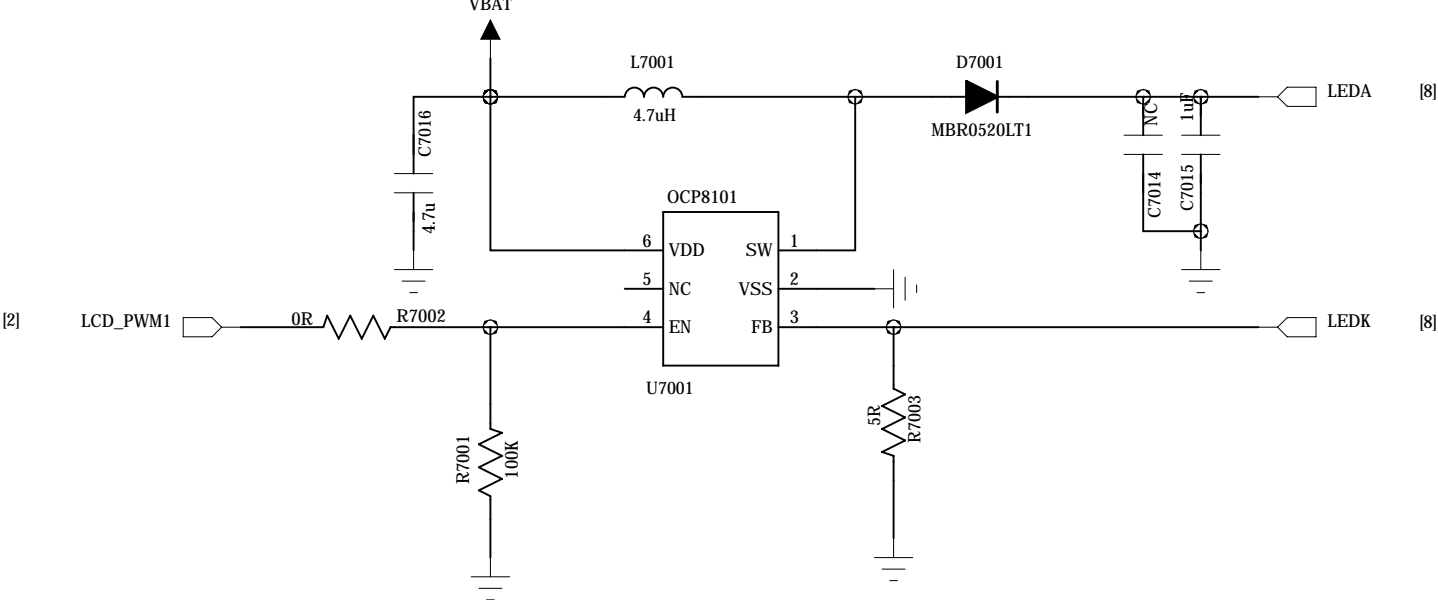
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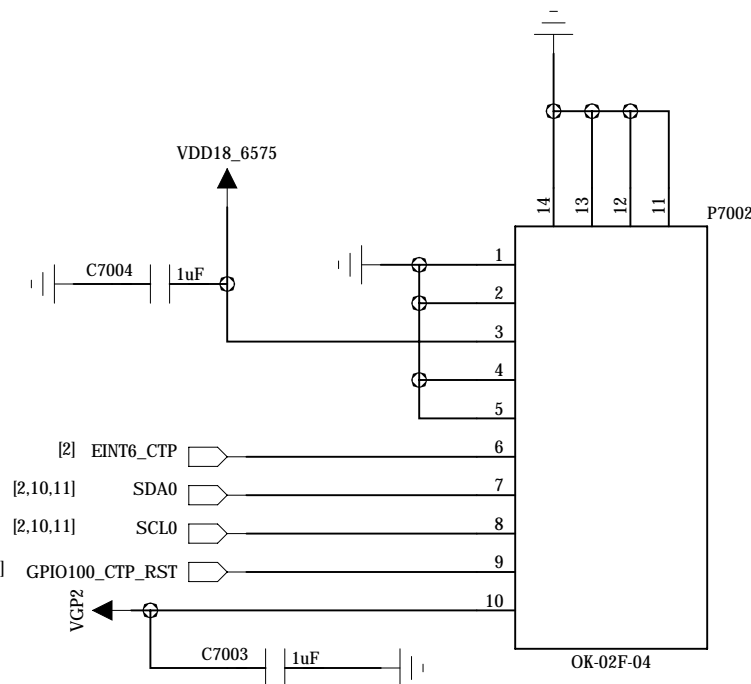
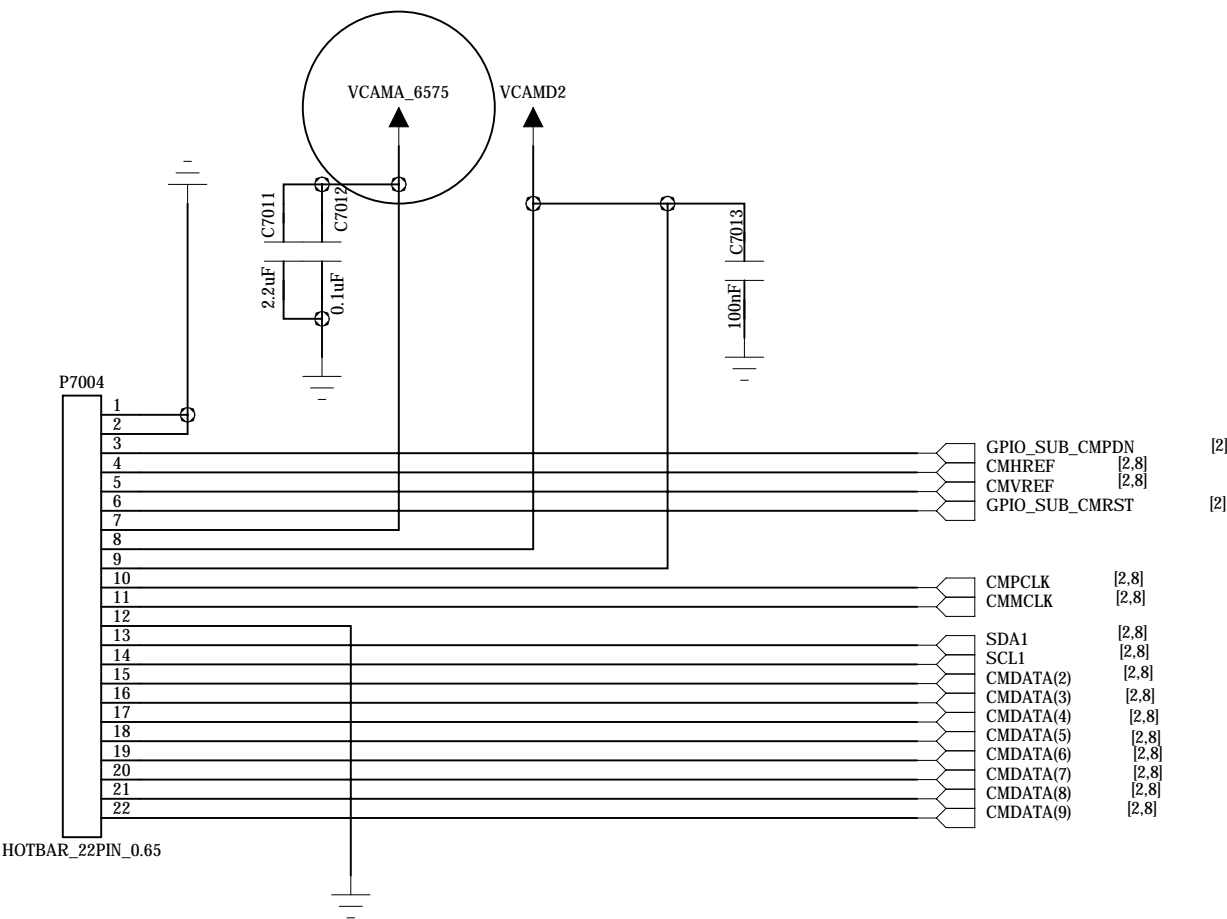
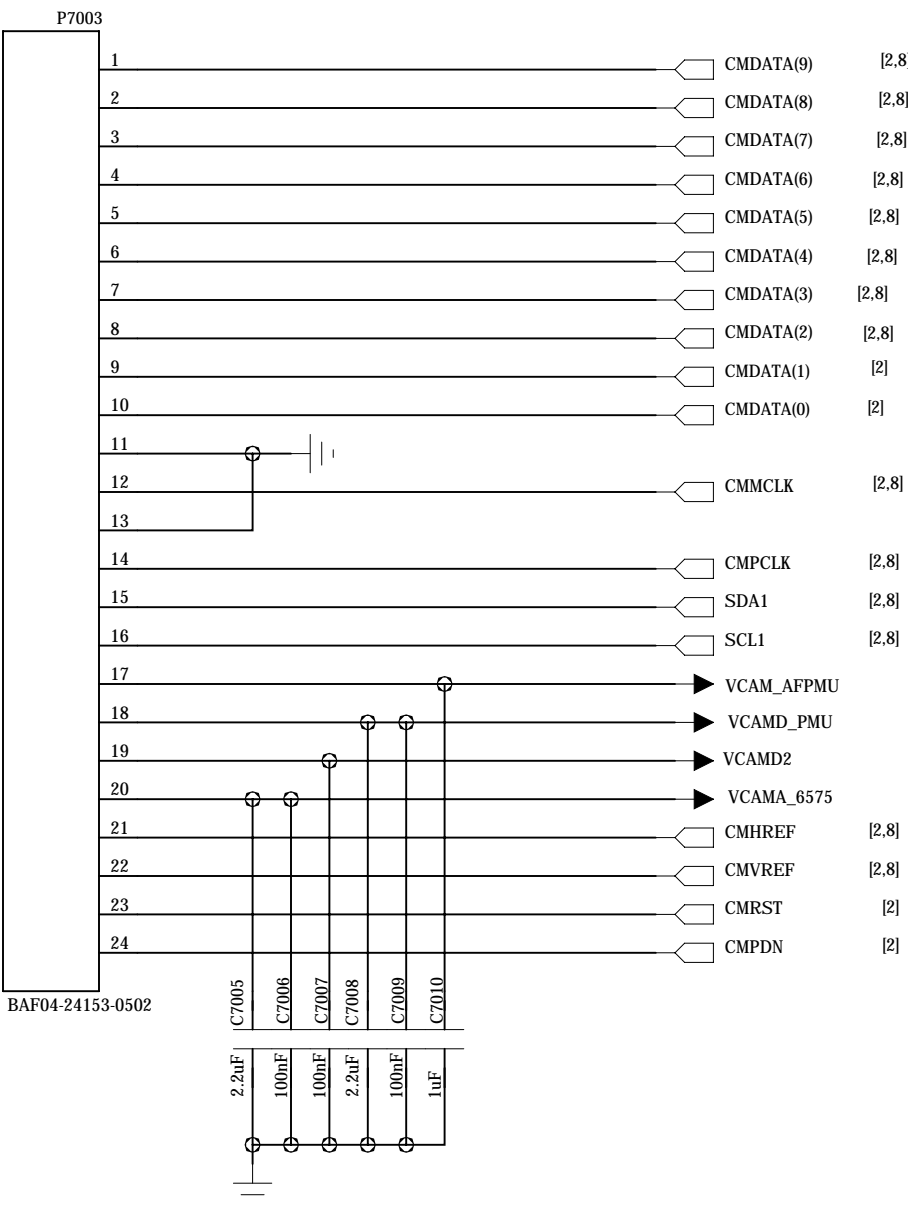
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



# LCD BACKLIGHT



# Sub Board Connector



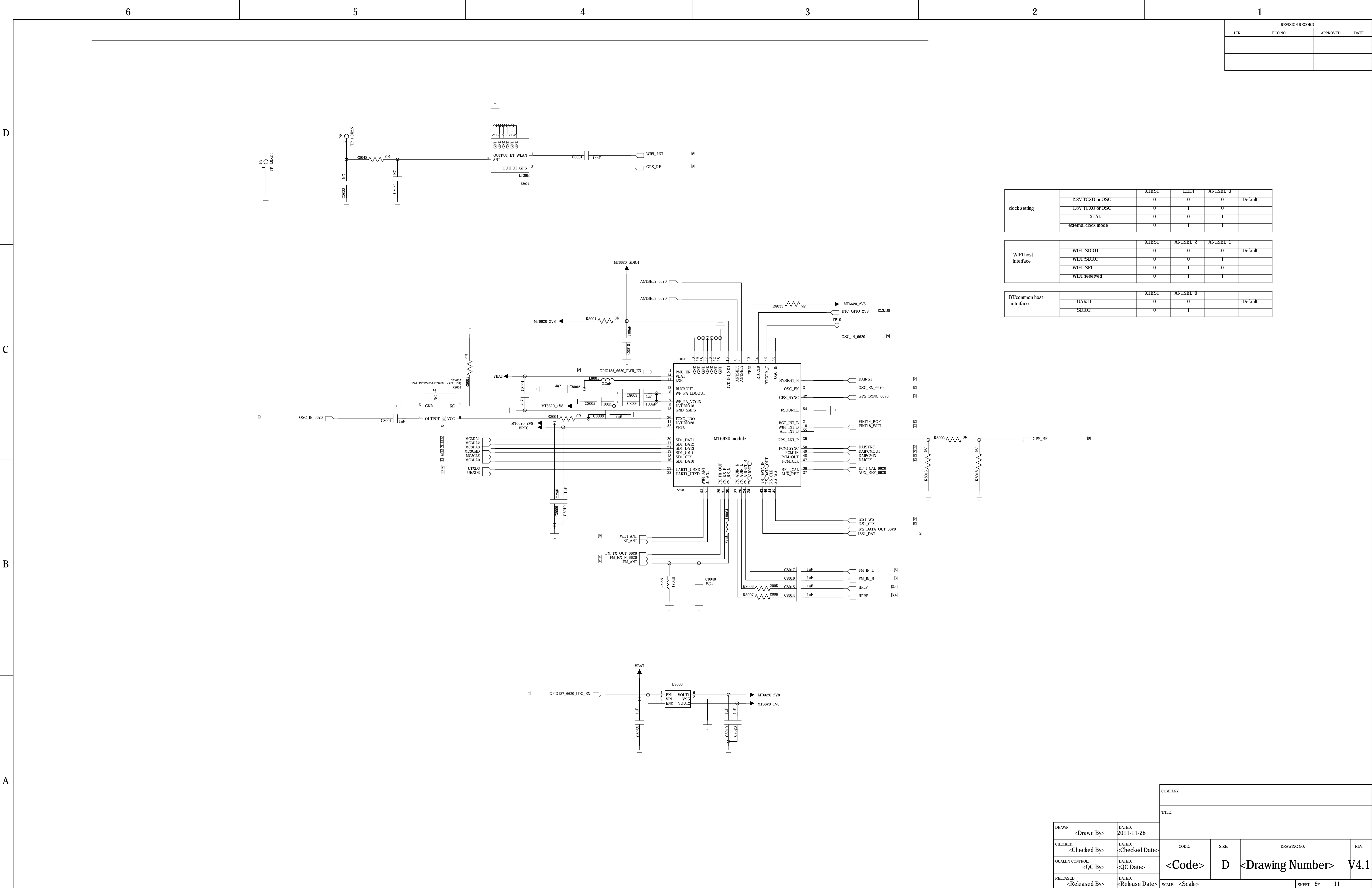
# Touch Panel

Main Camera

# Camera

COMPANY:			
TITLE:			
DRAWN: <Drawn By>	DATED: 2011-11-28	CODE: <Code>	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: <Drawing Number>	REV: V4.1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
RELEASED: <Released By>	DATED: <Release Date>	SHEET: 8f 11	



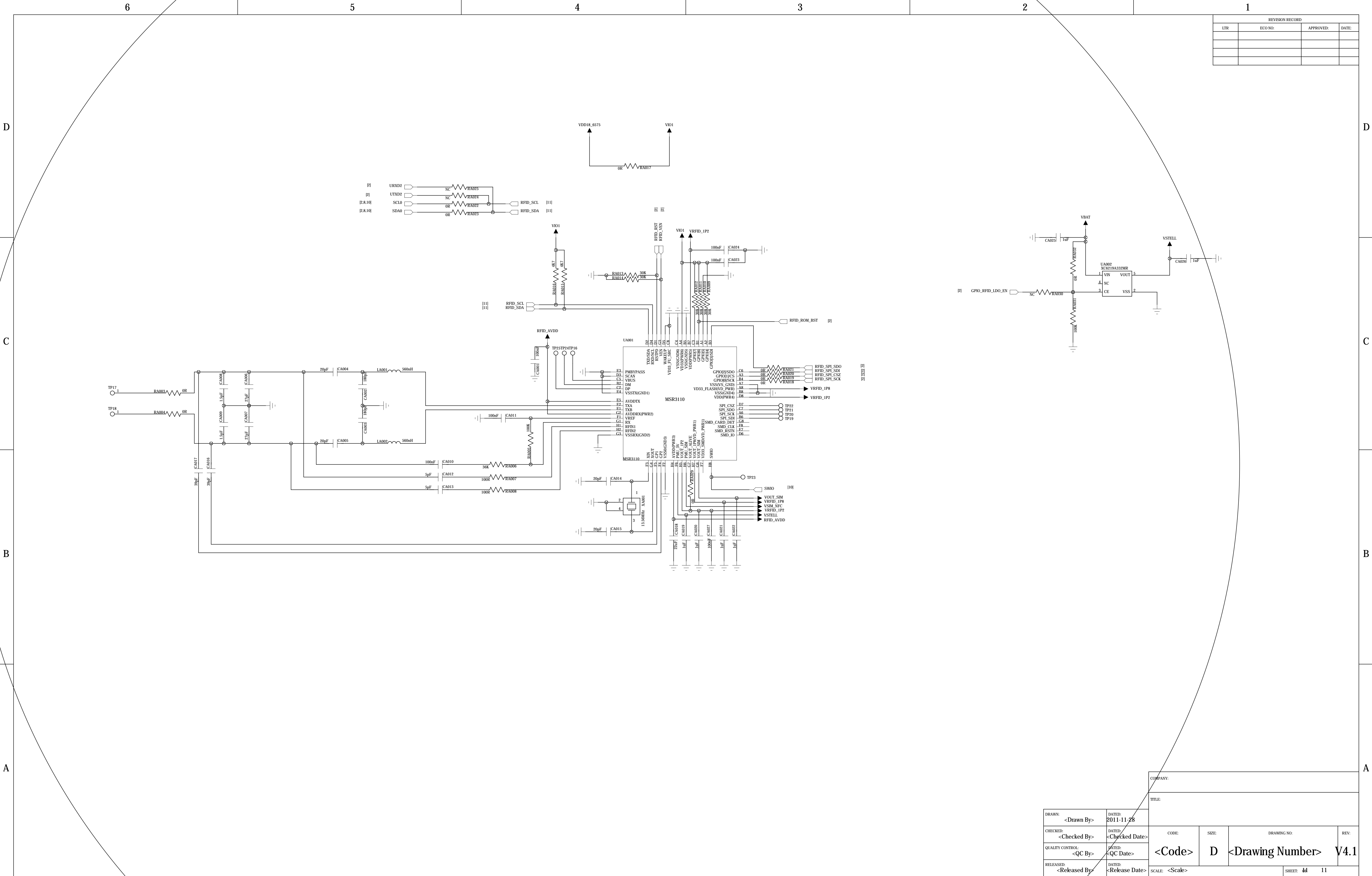


REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

clock setting	2.8V TCXO or OSC	XTEST	EED	ANISEL_3	Default
	1.8V TCXO or OSC	0	1	0	
	XTAL	0	0	1	
	external clock mode	0	1	1	
WiFi host interface	WiFi:SDIO1	XTEST	ANISEL_2	ANISEL_1	Default
	WiFi:SDIO2	0	0	1	
	WiFi:SPI	0	1	0	
	WiFi:reserved	0	1	1	
BT/common host interface	UART1	XTEST	ANISEL_0		Default
	SDIO2	0	1		

COMPANY:				
TITLE:				
DRAWN: <Drawn By>	DATED: 2011-11-28	CODE:	SIZE:	DRAWING NO:
CHECKED: <Checked By>	DATED: <Checked Date>	QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: V4.1
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 0f	11





REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

DRAWN: <Drawn By>		DATED: 2011-11-28						
CHECKED: <Checked By>		DATED: <Checked Date>						
QUALITY CONTROL: <QC By>		DATED: <QC Date>		<Code>	D	<Drawing Number>		V4.1
RELEASED: <Released By>		DATED: <Release Date>						
SCALE: <Scale>				SHEET: 11		11		