

## Список литературы

- [Bennett III & Rieffel(2000)] Bennett III, F.H. & Rieffel, E. (2000) Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 43–52, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Bradley *et al.*(2000)Bradley, Ortega-Sanchez & Tyrrell] Bradley, D., Ortega-Sanchez, C. & Tyrrell, A. (2000) Embryonics + immunotronics: A bio-inspired approach to fault tolerance. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 205–224, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Coello *et al.*(2000)Coello, Aguirre & Buckles] Coello, C., Aguirre, A. & Buckles, B. (2000) Evolutionary multiobjective design of combinational logic circuits. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 161–170, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [de Garis *et al.*(2000)de Garis, Buller, Dob, Honlet, Guttikonda & Decesare] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P. & Decesare, D. (2000) Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 225–234, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Flockton & Sheehan(2000)] Flockton, S. & Sheehan, K. (2000) Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 117–124, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Hollingworth *et al.*(2000)Hollingworth, Smith & Tyrrell] Hollingworth, G., Smith, S. & Tyrrell, A. (2000) Safe intrinsic evolution of virtex devices. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 195–202, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Imamura *et al.*(2000)Imamura, Foster & Krings] Imamura, K., Foster, J. & Krings, A. (2000) Bidirectional incremental evolution in extrinsic evolvable hardware. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 75–80, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Jonathan *et al.*(2000)Jonathan, Zebulum, Pacheco & Vellasco] Jonathan, M., Zebulum, R., Pacheco, M. & Vellasco, M. (2000) Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 133–140, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Kalganova(2000)] Kalganova, T. (2000) Bidirectional incremental evolution in extrinsic evolvable hardware. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 65–74, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Korkin *et al.*(2000)Korkin, Fehr & Jeffery] Korkin, M., Fehr, G. & Jeffery, G. (2000) Evolving hardware on a large scale. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 173–182, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.

- [Koza *et al.*(2000)Koza, Yu, Keane & Mydlowec] Koza, J.R., Yu, J., Keane, M.A. & Mydlowec, W. (2000) Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 5–16, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Lee *et al.*(2000)Lee, Hall, Perkowski & Jun] Lee, C., Hall, D., Perkowski, M. & Jun, D. (2000) Self-repairable eplds: Design, self-repair, and evaluation methodology. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 183–194, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Levi(2000)] Levi, D. (2000) Hereboy: A fast evolutionary algorithm. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 17–24, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Levy *et al.*(2000)Levy, Lepri, Sanchez, Ritter & Sipper] Levy, R., Lepri, S., Sanchez, E., Ritter, G. & Sipper, M. (2000) Slate of the art: An evolving fpga-based board for handwritten-digit recognition. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 237–244, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Mange *et al.*(2000)Mange, Sipper, Stauffer & Tempesti] Mange, D., Sipper, M., Stauffer, A. & Tempesti, G. (2000) Toward self-repairing and self-replicating hardware: The embryonics approach. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 205–214, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Marston *et al.*(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka & Higuchi] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K. & Higuchi, T. (2000) An evolutionary approach to ghz digital systems. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 125–131, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Masner *et al.*(2000)Masner, Cavalieri, Frenzel & Foster] Masner, J., Cavalieri, J., Frenzel, J. & Foster, J. (2000) Size versus robustness in evolved sorting networks: Is bigger better? *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 81–87, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Milano & Koumoutsakos(2000)] Milano, M. & Koumoutsakos, P. (2000) A clustering genetic algorithm for actuator optimization in flow control. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 263–270, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Pollack & Lipson(2000)] Pollack, J. & Lipson, H. (2000) The golem project: Evolving hardware bodies and brains. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 37–42, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Seok *et al.*(2000)Seok, Lee, Zhang, Lee & Sim] Seok, H., Lee, K., Zhang, B., Lee, D. & Sim, K. (2000) Genetic programming of process decomposition strategies for evolvable hardware. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 25–34, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Stoica *et al.*(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel & Duong] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R. & Duong, V. (2000) Evolution of analog circuits on field programmable transistor arrays. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen),

- pp. 99–108, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Thompson & Wasshuber(2000)] Thompson, A. & Wasshuber, C. (2000) Evolutionary design of single electron systems. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 109–116, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Torresen(2000)] Torresen, J. (2000) Scalable evolvable hardware applied to road image recognition. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 245–252, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Tuft & Haddow(2000)] Tuft, G. & Haddow, P. (2000) Evolving an adaptive digital filter. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 143–150, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Vassilev & Miller(2000)] Vassilev, V. & Miller, J. (2000) Scalability problems of digital circuit evolution: Evolvability and efficient designs. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 55–64, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Yasunaga *et al.*(2000)Yasunaga, Nakamura, Yoshihara & Kim] Yasunaga, M., Nakamura, T., Yoshihara, I. & Kim, J. (2000) Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 253–262, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.
- [Zebulum *et al.*(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco & Szwarcman] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M. & Szwarcman, M. (2000) A reconfigurable platform for the automatic synthesis of analog circuits. *The Second NASA/DoD workshop on Evolvable Hardware* (eds. J. Lohn, A. Stoica & D. Keymeulen), pp. 91–98, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California.