Список литературы

[1] Koza JR, Yu J, Keane MA, Mydlowec W. Use of Conditional Developmental Operators and Free Variables in Automatically Synthesizing Generalized Circuits using Genetic Programming. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 5–16.

[2] Levi D. HereBoy: A Fast Evolutionary Algorithm. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 17–24.

[3] Seok H, Lee K, Zhang B, Lee D, Sim K. Genetic Programming of Process Decomposition Strategies for Evolvable Hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 25–34.

[4] Pollack J, Lipson H. The GOLEM Project: Evolving Hardware Bodies and Brains. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 37–42.

[5] Bennett III FH, Rieffel E. Design of Decentralized Controllers for Self-Reconfigurable Modular Robots using Genetic Programming. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 43–52.

[6] Vassilev V, Miller J. Scalability Problems of Digital Circuit Evolution: Evolvability and Efficient Designs. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 55–64.

[7] Kalganova T. Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 65–74.

[8] Imamura K, Foster J, Krings A. Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 75–80.

[9] Masner J, Cavalieri J, Frenzel J, Foster J. Size versus Robustness in Evolved Sorting Networks: Is Bigger Better? In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 81–87.

[10] Zebulum R, Sinohara H, Vellasco M, Santini C, Pacheco M, Szwarcman M. A Reconfigurable Platform for the Automatic Synthesis of Analog Circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 91–98.

[11] Stoica A, Keymeulen D, Zebulum R, Thakoor A, Daud T, Klimeck G, Jin Y, Tawel R, Duong V. Evolution of Analog Circuits on Field Programmable Transistor Arrays. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 99–108.

[12] Thompson A, Wasshuber C. Evolutionary Design of Single Electron Systems. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 109–116.

[13] Flockton S, Sheehan K. Behavior of a Building Block for Intrinsic Evolution of Analogue Signal Shaping and Filtering Circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 117–124.

[14] Marston N, Takahashi E, Murakawa M, Kasai Y, Adachi T, Takasuka K, Higuchi T. An Evolutionary Approach to GHz Digital Systems. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 125–131.

[15] Jonathan M, Zebulum R, Pacheco M, Vellasco M. Multiobjective Optimization Techniques: A Study of the Energy Minimization Method and Its Application to the Synthesis of Ota Amplifiers. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 133–140.

[16] Tufte G, Haddow P. Evolving an Adaptive Digital Filter. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 143–150.

[17] Coello C, Aguirre A, Buckles B. Evolutionary Multiobjective Design of Combinational Logic Circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 161–170.

[18] Korkin M, Fehr G, Jeffery G. Evolving Hardware on a Large Scale. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 173–182.

[19] Lee C, Hall D, Perkowski M, Jun D. Self-Repairable EPLDs: Design, Self-Repair, and Evaluation Methodology. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 183–194.

[20] Hollingworth G, Smith S, Tyrrell A. Safe Intrinsic Evolution of Virtex Devices. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 195–202.

[21] Mange D, Sipper M, Stauffer A, Tempesti G. Toward Self-Repairing and Self-Replicating Hardware: The Embryonics Approach. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 205–214.

[22] Bradley D, Ortega-Sanchez C, Tyrrell A. Embryonics + Immunotronics: A Bio-Inspired Approach to Fault Tolerance. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 205–224.

[23] de Garis H, Buller A, Dob T, Honlet J, Guttikonda P, Decesare D. Building Multimodule Systems with Unlimited Evolvable Capacities from Modules with Limited Evolvable Capacities (MECs). In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 225–234.

[24] Levy R, Lepri S, Sanchez E, Ritter G, Sipper M. Slate of the Art: An Evolving FPGA-based Board for Handwritten-Digit Recognition. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 237–244.

[25] Torresen J. Scalable Evolvable Hardware Applied to Road Image Recognition. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 245–252.

[26] Yasunaga M, Nakamura T, Yoshihara I, Kim J. Kernel-based Pattern Recognition Hardware: Its Design Methodology using Evolved Truth Tables. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 253–262.

[27] Milano M, Koumoutsakos P. A Clustering Genetic Algorithm for Actuator Optimization in Flow Control. In: *The Second NASA/DoD workshop on Evolvable Hardware*, edited by Lohn J, Stoica A, Keymeulen D. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society. 2000; pp. 263–270.