

Список литературы

- [1] Koza, J. R., Yu, J., Keane, M. A., and Mydlowec, W. (13-15 July, 2000) Use of Conditional Developmental Operators and Free Variables in Automatically Synthesizing Generalized Circuits using Genetic Programming. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 5–16.
- [2] Levi, D. (13-15 July, 2000) HereBoy: A Fast Evolutionary Algorithm. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 17–24.
- [3] Seok, H., Lee, K., Zhang, B., Lee, D., and Sim, K. (13-15 July, 2000) Genetic Programming of Process Decomposition Strategies for Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 25–34.
- [4] Pollack, J. and Lipson, H. (13-15 July, 2000) The GOLEM Project: Evolving Hardware Bodies and Brains. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 37–42.
- [5] Bennett III, F. H. and Rieffel, E. (13-15 July, 2000) Design of Decentralized Controllers for Self-Reconfigurable Modular Robots using Genetic Programming. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 43–52.
- [6] Vassilev, V. and Miller, J. (13-15 July, 2000) Scalability Problems of Digital Circuit Evolution: Evolvability and Efficient Designs. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 55–64.
- [7] Kalganova, T. (13-15 July, 2000) Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 65–74.
- [8] Imamura, K., Foster, J., and Krings, A. (13-15 July, 2000) Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 75–80.
- [9] Masner, J., Cavalieri, J., Frenzel, J., and Foster, J. (13-15 July, 2000) Size versus Robustness in Evolved Sorting Networks: Is Bigger Better?. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 81–87.
- [10] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M., and Szwarcman, M. (13-15 July, 2000) A Reconfigurable Platform for the Automatic Synthesis of Analog Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 91–98.
- [11] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R., and Duong, V. (13-15 July, 2000) Evolution of Analog Circuits on Field Programmable Transistor Arrays. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 99–108.

- [12] Thompson, A. and Wasshuber, C. (13-15 July, 2000) Evolutionary Design of Single Electron Systems. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 109–116.
- [13] Flockton, S. and Sheehan, K. (13-15 July, 2000) Behavior of a Building Block for Intrinsic Evolution of Analogue Signal Shaping and Filtering Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 117–124.
- [14] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K., and Higuchi, T. (13-15 July, 2000) An Evolutionary Approach to GHz Digital Systems. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 125–131.
- [15] Jonathan, M., Zebulum, R., Pacheco, M., and Vellasco, M. (13-15 July, 2000) Multiobjective Optimization Techniques: A Study of the Energy Minimization Method and Its Application to the Synthesis of Ota Amplifiers. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 133–140.
- [16] Tufte, G. and Haddow, P. (13-15 July, 2000) Evolving an Adaptive Digital Filter. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 143–150.
- [17] Coello, C., Aguirre, A., and Buckles, B. (13-15 July, 2000) Evolutionary Multiobjective Design of Combinational Logic Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 161–170.
- [18] Korkin, M., Fehr, G., and Jeffery, G. (13-15 July, 2000) Evolving Hardware on a Large Scale. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 173–182.
- [19] Lee, C., Hall, D., Perkowski, M., and Jun, D. (13-15 July, 2000) Self-Repairable EPLDs: Design, Self-Repair, and Evaluation Methodology. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 183–194.
- [20] Hollingworth, G., Smith, S., and Tyrrell, A. (13-15 July, 2000) Safe Intrinsic Evolution of Virtex Devices. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 195–202.
- [21] Mange, D., Sipper, M., Stauffer, A., and Tempesti, G. (13-15 July, 2000) Toward Self-Repairing and Self-Replicating Hardware: The Embryonics Approach. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 205–214.
- [22] Bradley, D., Ortega-Sanchez, C., and Tyrrell, A. (13-15 July, 2000) Embryonics + Immunotronics: A Bio-Inspired Approach to Fault Tolerance. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 205–224.

- [23] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P., and Decesare, D. (13-15 July, 2000) Building Multimodule Systems with Unlimited Evolvable Capacities from Modules with Limited Evolvable Capacities (MECs). In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 225–234.
- [24] Levy, R., Lepri, S., Sanchez, E., Ritter, G., and Sipper, M. (13-15 July, 2000) Slate of the Art: An Evolving FPGA-based Board for Handwritten-Digit Recognition. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 237–244.
- [25] Torresen, J. (13-15 July, 2000) Scalable Evolvable Hardware Applied to Road Image Recognition. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 245–252.
- [26] Yasunaga, M., Nakamura, T., Yoshihara, I., and Kim, J. (13-15 July, 2000) Kernel-based Pattern Recognition Hardware: Its Design Methodology using Evolved Truth Tables. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 253–262.
- [27] Milano, M. and Koumoutsakos, P. (13-15 July, 2000) A Clustering Genetic Algorithm for Actuator Optimization in Flow Control. In Lohn, J., Stoica, A., and Keymeulen, D., (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, CaliforniaJet Propulsion Laboratory, California Institute of Technology: IEEE Computer Society pp. 263–270.