## Список литературы


[Bennett III and Rieffel, 2000] Bennett III, F. H. and Rieffel, E. (2000). "Design of Decentralized Controllers for Self-Reconfigurable Modular Robots using Genetic Programming". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 43–52, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Bradley et al., 2000] Bradley, D.; Ortega-Sanchez, C.; and Tyrrell, A. (2000). "Embryonics + Immunotronics: A Bio-Inspired Approach to Fault Tolerance". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–224, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Coello et al., 2000] Coello, C.; Aguirre, A.; and Buckles, B. (2000). "Evolutionary Multiobjective Design of Combinational Logic Circuits". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 161–170, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis et al., 2000] de Garis, H.; Buller, A.; Dob, T.; Honlet, J.; Guttikonda, P.; and Decesare, D. (2000). "Building Multimodule Systems with Unlimited Evolvable Capacities from Modules with Limited Evolvable Capacities (MECs)". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 225–234, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Flockton and Sheehan, 2000] Flockton, S. and Sheehan, K. (2000). "Behavior of a Building Block for Intrinsic Evolution of Analogue Signal Shaping and Filtering Circuits". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 117–124, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hollingworth et al., 2000] Hollingworth, G.; Smith, S.; and Tyrrell, A. (2000). "Safe Intrinsic Evolution of Virtex Devices". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 195–202, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Imamura et al., 2000] Imamura, K.; Foster, J.; and Krings, A. (2000). "Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 75–80, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Jonathan et al., 2000] Jonathan, M.; Zebulum, R.; Pacheco, M.; and Vellasco, M. (2000). "Multiobjective Optimization Techniques: A Study of the Energy Minimization Method and Its Application to the Synthesis of Ota Amplifiers". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 133–140, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Kalganova, 2000] Kalganova, T. (2000). "Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 65–74, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Korkin et al., 2000] Korkin, M.; Fehr, G.; and Jeffery, G. (2000). "Evolving Hardware on a Large Scale". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 173–182, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Koza et al., 2000] Koza, J. R.; Yu, J.; Keane, M. A.; and Mydlowec, W. (2000). "Use of Conditional Developmental Operators and Free Variables in Automatically Synthesizing Generalized Circuits using Genetic Programming". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second*

*NASA/DoD workshop on Evolvable Hardware*, pages 5–16, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Lee et al., 2000] Lee, C.; Hall, D.; Perkowski, M.; and Jun, D. (2000). "Self-Repairable EPLDs: Design, Self-Repair, and Evaluation Methodology". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 183–194, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levi, 2000] Levi, D. (2000). "HereBoy: A Fast Evolutionary Algorithm". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 17–24, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levy et al., 2000] Levy, R.; Lepri, S.; Sanchez, E.; Ritter, G.; and Sipper, M. (2000). "Slate of the Art: An Evolving FPGA-based Board for Handwritten-Digit Recognition". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 237–244, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Mange et al., 2000] Mange, D.; Sipper, M.; Stauffer, A.; and Tempesti, G. (2000). "Toward Self-Repairing and Self-Replicating Hardware: The Embryonics Approach". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–214, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Marston et al., 2000] Marston, N.; Takahashi, E.; Murakawa, M.; Kasai, Y.; Adachi, T.; Takasuka, K.; and Higuchi, T. (2000). "An Evolutionary Approach to GHz Digital Systems". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 125–131, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Masner et al., 2000] Masner, J.; Cavalieri, J.; Frenzel, J.; and Foster, J. (2000). "Size versus Robustness in Evolved Sorting Networks: Is Bigger Better?". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 81–87, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Milano and Koumoutsakos, 2000] Milano, M. and Koumoutsakos, P. (2000). "A Clustering Genetic Algorithm for Actuator Optimization in Flow Control". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 263–270, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Pollack and Lipson, 2000] Pollack, J. and Lipson, H. (2000). "The GOLEM Project: Evolving Hardware Bodies and Brains". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 37–42, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Seok et al., 2000] Seok, H.; Lee, K.; Zhang, B.; Lee, D.; and Sim, K. (2000). "Genetic Programming of Process Decomposition Strategies for Evolvable Hardware". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 25–34, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica et al., 2000] Stoica, A.; Keymeulen, D.; Zebulum, R.; Thakoor, A.; Daud, T.; Klimeck, G.; Jin, Y.; Tawel, R.; and Duong, V. (2000). "Evolution of Analog Circuits on Field Programmable Transistor Arrays". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 99–108, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Thompson and Wasshuber, 2000] Thompson, A. and Wasshuber, C. (2000). "Evolutionary Design of Single Electron Systems". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 109–116, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Torresen, 2000] Torresen, J. (2000). "Scalable Evolvable Hardware Applied to Road Image Recognition". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 245–252, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tufte and Haddow, 2000] Tufte, G. and Haddow, P. (2000). "Evolving an Adaptive Digital Filter". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 143–150, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Vassilev and Miller, 2000] Vassilev, V. and Miller, J. (2000). "Scalability Problems of Digital Circuit Evolution: Evolvability and Efficient Designs". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 55–64, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Yasunaga et al., 2000] Yasunaga, M.; Nakamura, T.; Yoshihara, I.; and Kim, J. (2000). "Kernel-based Pattern Recognition Hardware: Its Design Methodology using Evolved Truth Tables". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 253–262, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Zebulum et al., 2000] Zebulum, R.; Sinohara, H.; Vellasco, M.; Santini, C.; Pacheco, M.; and Szwarcman, M. (2000). "A Reconfigurable Platform for the Automatic Synthesis of Analog Circuits". In Lohn, J.; Stoica, A.; and Keymeulen, D., editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 91–98, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.