

Список литературы

- [1] M. Abramovici, J. M. Emmert, and C. E. Stroud, "Roving stars: An integrated approach to on-line testing, diagnosis, and fault tolerance for fpgas in adaptive computing systems," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 73–92.
- [2] D. W. Bradley and A. M. Tyrell, "The architecture for a hardware immune system," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 193–200.
- [3] O. Castillo, O. Montiel, R. Sepulveda, and P. Melin, "Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 146–153.
- [4] A. G. Darren, R. Conde, B. Chern, P. Luers, S. Jurczyk, and C. Mills, "Adaptive instrument module: Space instrument controller "brain" through programmable logic devices," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 256–260.
- [5] H. de Garis, L. de Penning, A. Bullner, and D. Decesare, "Early experiments on the cam-brain machine (cbm)," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 211–219.
- [6] B. Dolin, F. H. Bennett III, and E. G. Rieffel, "Methods for evolving robust distributed robot control software: coevolutionary and single population techniques," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 21–29.
- [7] R. T. Edwards and C. J. Kim, "Breaking the resistivity barrier," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 167–171.
- [8] J. C. Gallagher, "A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 48–55.
- [9] R. I. Graham and T. Arslan, "Rule evolution in order based diagnostic systems," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 280–286.
- [10] P. C. Haddow and G. Tufte, "Bridging the genotype-phenotype mapping for digital fpgas," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 109–115.
- [11] A. Hernandez-Aguirre, B. P. Buckles, and C. A. C. Coello, "On learning knf boolean formulas," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 240–246.

- [12] B. I. Hounsell and T. Arslan, "Evolutionary design and adaption of digital filters within an embedded fault," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 127–135.
- [13] —, "Evolutionary design and adaption of digital filters within an embedded fault," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 127–135.
- [14] A. H. Jackson and A. M. Tyrrell, "Asynchronous embryonics," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 201–210.
- [15] S. Kazadi, Y. Qi, I. Park, N. Huang, P. Hwu, B. Kwan, W. Lue, and H. Li, "Insufficiency of piecewise evolution," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 223–231.
- [16] J. Langeheine, J. Becker, S. Foilling, K. Meire, and J. Schemmel, "A cmos fpga chip for intrinsic hardware evolution of analog electronic circuits," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 172–175.
- [17] D. S. Linden, "A system for evolving antennas in-situ," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 249–255.
- [18] J. W. Lockwood, "Evolvable internet hardware platforms," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 271–279.
- [19] J. F. Miller and M. Hartmann, "Evolving messy gates for fault tolerance: Some preliminary findings," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 116–123.
- [20] J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany, "An in-system routing strategy for evolvable hardware programmable platforms," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 157–166.
- [21] —, "An in-system routing strategy for evolvable hardware programmable platforms," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 157–166.
- [22] J. O. Pfaffmann and K. P. Zauner, "Scouting context-sensitive components," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 14–20.
- [23] R. Porter, M. Gokhale, N. Harvey, S. Perkins, and C. Young, "Evolving network architectures with custom computers for multi-spectral feature identification," in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 261–270.

- [24] E. Ramsden, “The isppac family of reconfigurable analog circuits,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 176–181.
- [25] J. H. Saleh, D. E. Hastings, and D. J. Newman, “Extracting the essence of flexibility in system design,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 59–72.
- [26] C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco, and M. H. Szwarcman, “Pama-programmable analog multiplexer array,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 36–43.
- [27] T. Schiner, X. Yao, and P. Liu, “Digital filter design using multiple pareto fronts,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 136–145.
- [28] J. L. Segovia-Juarez and S. Colombano, “Mutation buffering capabilities of the hypernetwork model,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 7–13.
- [29] H. T. Sinohara, M. A. C. Pacheco, and M. M. R. Vellasco, “Repair of analog circuits: Extrinsic and intrinsic evolutionary techniques,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 44–47.
- [30] A. Stauffer, D. Mange, G. Tempesti, and C. Teuscher, “Biowatch: A giant electronic bio-inspired watch,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 185–192.
- [31] A. Stoica, R. Zebulum, and D. Keymeulen, “Progress and challenges in building evolvable devices,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 33–35.
- [32] A. M. Tyrrell, G. Hollingworth, and S. L. Smith, “Evolutionary strategies and intrinsic fault tolerance,” in *The Third NASA/DoD workshop on Evolvable Hardware*, D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 12-14 July 2001, pp. 98–106.