

Список литературы

- [BOST00] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell, *Embryonics + immunotronics: A bio-inspired approach to fault tolerance*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 205–224.
- [BR00] F. H Bennett III and E. Rieffel, *Design of decentralized controllers for self-reconfigurable modular robots using genetic programming*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 43–52.
- [CAB00] C. Coello, A. Aguirre, and B. Buckles, *Evolutionary multiobjective design of combinational logic circuits*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 161–170.
- [dBD⁺00] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare, *Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs)*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 225–234.
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- [JZPV00] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco, *Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 133–140.
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- [KFJ00] M. Korkin, G. Fehr, and G. Jeffery, *Evolving hardware on a large scale*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 173–182.

- [KYKM00] John R. Koza, Jessen Yu, Martin A. Keane, and William Mydlowec, *Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 5–16.
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- [PL00] J. Pollack and H. Lipson, *The golem project: Evolving hardware bodies and brains*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 37–42.
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- [TH00] G. Tufte and P. Haddow, *Evolving an adaptive digital filter*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 143–150.
- [Tor00] J. Torresen, *Scalable evolvable hardware applied to road image recognition*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 245–252.
- [TW00] A. Thompson and C. Wasshuber, *Evolutionary design of single electron systems*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 109–116.
- [VM00] V. Vassilev and J. Miller, *Scalability problems of digital circuit evolution: Evolvability and efficient designs*, The Second NASA/DoD workshop on Evolvable Hardware (Palo Alto, California) (Jason Lohn, Adrian Stoica, and Didier Keymeulen, eds.), Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 13-15 July 2000, pp. 55–64.
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