

Список литературы

- [1] Araujo, S. G., Mesquita, A., and Pedroza, A. C. P., Using genetic programming and high level synthesis to design optimized datapath, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 434–445, Trondheim, Norway, 2003, Springer-Verlag.
- [2] Aunet, S. and Hartmann, M., Real-time reconfigurable linear threshold elements and some applications to neural hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 365–376, Trondheim, Norway, 2003, Springer-Verlag.
- [3] Azhar, M. A. H. B. and Dimond, K. R., Hardware implementation of a genetic controller and effects of training on evolution, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 344–354, Trondheim, Norway, 2003, Springer-Verlag.
- [4] Van Belle, W., Mens, T., and D’Hondt, T., Using genetic programming to generate protocol adaptors for interprocess communication, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 422–433, Trondheim, Norway, 2003, Springer-Verlag.
- [5] Bentley, P. J., Evolving fractal proteins, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 81–92, Trondheim, Norway, 2003, Springer-Verlag.
- [6] Blynell, J., Evolving reinforcement learning-like abilities for robots, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 320–331, Trondheim, Norway, 2003, Springer-Verlag.
- [7] Canham, R. and Tyrrell, A. M., A learning, multi-layered, hardware artificial immune system implemented upon an embryonic array, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 174–185, Trondheim, Norway, 2003, Springer-Verlag.
- [8] Coello, C. A. C., Luna, E. H., and Aguirre, A. H., Use of particle swarm optimization to design combinational logic circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 398–409, Trondheim, Norway, 2003, Springer-Verlag.
- [9] Corno, F., Cumani, F., and Squillero, G., Exploiting auto-adaptive μ -GP for highly effective test programs generation, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 262–273, Trondheim, Norway, 2003, Springer-Verlag.
- [10] Downing, K. L., Developmental models for emergent computation, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 105–116, Trondheim, Norway, 2003, Springer-Verlag.
- [11] Eriksson, J. et al., Spiking neural networks for reconfigurable POETic tissue, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 165–173, Trondheim, Norway, 2003, Springer-Verlag.
- [12] Estrada, G. G., A note on designing logical circuits using SAT, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 410–421, Trondheim, Norway, 2003, Springer-Verlag.

- [13] de Degaris, H., Gaur, A., and Sriram, R., Quantum versus evolutionary systems. total versus sampled search, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 457–466, Trondheim, Norway, 2003, Springer-Verlag.
- [14] Garvie, M. and Thompson, A., Evolution of self-diagnosing hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 238–248, Trondheim, Norway, 2003, Springer-Verlag.
- [15] Goldsmith, R., Real world hardware evolution: A mobile platform for sensor evolution, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 355–364, Trondheim, Norway, 2003, Springer-Verlag.
- [16] Greensted, A. J. and Tyrrell, A. M., Fault tolerance via endocrinologic based communication for multiprocessor systems, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 24–34, Trondheim, Norway, 2003, Springer-Verlag.
- [17] van de Haar, R. and Hoekstra, J., Simulation of a neural node using SET technology, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 377–386, Trondheim, Norway, 2003, Springer-Verlag.
- [18] Aguirre, A. H., Equihua, E. C. G., and Coello Coello, C. A., Synthesis of boolean functions using information theory, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 218–227, Trondheim, Norway, 2003, Springer-Verlag.
- [19] Kumar, S. and Bentley, P. J., Biologically inspired evolutionary development, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 57–68, Trondheim, Norway, 2003, Springer-Verlag.
- [20] Li, J. H. and Lim, M. H., Evolvable fuzzy system for ATM cell scheduling, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 208–217, Trondheim, Norway, 2003, Springer-Verlag.
- [21] Lohn, J., Larchev, G., and DeMara, R., A genetic representation for evolutionary fault recovery in Virtex FPGAs, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 47–56, Trondheim, Norway, 2003, Springer-Verlag.
- [22] Lund, H. H., Larsen, R. L., and Østergaard, E. H., Distributed control in self-reconfigurable robots, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 296–307, Trondheim, Norway, 2003, Springer-Verlag.
- [23] Miller, J. F. and Thomson, P., A developmental method for growing graphs and circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 93–104, Trondheim, Norway, 2003, Springer-Verlag.
- [24] Ortega-Sanchez, C., Torres-Jimenez, J., and Morales-Cruz, J., Routing of embryonic arrays using genetic algorithms, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 249–261, Trondheim, Norway, 2003, Springer-Verlag.

- [25] Østergaard, E. H. and Lund, H. H., Co-evolving complex robot behavior, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 308–319, Trondheim, Norway, 2003, Springer-Verlag.
- [26] van Remortel, P., Ceuppens, J., Defaweux, A., Lenaerts, T., and Manderick, B., Developmental effects on tuneable fitness landscapes, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 117–128, Trondheim, Norway, 2003, Springer-Verlag.
- [27] Roggen, D., Floreano, D., and Mattiussi, C., A morphogenetic evolutionary system: Phylogenesis of the POEtic circuit, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 153–164, Trondheim, Norway, 2003, Springer-Verlag.
- [28] Schmitz, T., Hohmann, S., Meier, K., Schemmel, J., and Schurmann, F., Speeding up hardware evolution: A coprocessor for evolutionary algorithms, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 274–285, Trondheim, Norway, 2003, Springer-Verlag.
- [29] Schnier, T. and Yao, X., Using negative correlation to evolve fault-tolerant circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 35–46, Trondheim, Norway, 2003, Springer-Verlag.
- [30] Sekanina, L., Virtual reconfigurable circuits for real-world applications of evolvable hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 186–197, Trondheim, Norway, 2003, Springer-Verlag.
- [31] Smith, S. L., Crouch, D. P., and Tyrrell, A. M., Evolving image processing operations for an evolvable hardware environment, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 332–343, Trondheim, Norway, 2003, Springer-Verlag.
- [32] Tanaka, F., Kameda, A., Yamamoto, M., and Ohuchi, A., The effect of the bulge loop upon the hybridization process in DNA computing, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 446–456, Trondheim, Norway, 2003, Springer-Verlag.
- [33] Tempesti, G. et al., Ontogenetic development and fault tolerance in the POEtic tissue, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 141–152, Trondheim, Norway, 2003, Springer-Verlag.
- [34] Teuscher, C. and Capcarrere, M. S., On fireflies, cellular systems, and evolware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 1–12, Trondheim, Norway, 2003, Springer-Verlag.
- [35] Torresen, J., Evolving multiplier circuits by training set and training vector partitioning, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 228–237, Trondheim, Norway, 2003, Springer-Verlag.
- [36] Tufte, G. and Haddow, P. C., Building knowledge into developmental rules for circuit design, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 69–80, Trondheim, Norway, 2003, Springer-Verlag.

- [37] Tyrrell, A. M. et al., POETic tissue: An integrated architecture for bio-inspired hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 129–140, Trondheim, Norway, 2003, Springer-Verlag.
- [38] Venkateswaran, N. and Chandramouli, C., General purpose processor architecture for modeling stochastic biological neuronal assemblies, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 387–397, Trondheim, Norway, 2003, Springer-Verlag.
- [39] Yasunaga, M., Yoshihara, I., and Kim, J. H., Gene finding using evolvable reasoning hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 198–207, Trondheim, Norway, 2003, Springer-Verlag.
- [40] Zebulum, R. S. et al., Automatic evolution of signal separators using reconfigurable hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 286–295, Trondheim, Norway, 2003, Springer-Verlag.
- [41] Zinchenko, L., Muhlenbein, H., Kureichik, V., and Mahnig, T., A comparison of different circuit representations for evolutionary analog circuit design, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by Tyrrell, A. M., Haddow, P. C., and Torresen, J., volume 2606 of *LNCS*, pages 13–23, Trondheim, Norway, 2003, Springer-Verlag.