Список литературы

- [Abra 01] M. Abramovici, J. M. Emmert, and C. E. Stroud. "Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault TOlerance for FPGAs in Adaptive Computing Systems". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 73–92, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Brad 01] D. W. Bradley and A. M. Tyrell. "The Architecture for a Hardware Immune System". In:
 D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 193–200, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Cast 01] O. Castillo, O. Montiel, R. Sepulveda, and P. Melin. "Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter". In:
 D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 146–153, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Darr 01] A. G. Darren, R. Conde, B. Chern, P. Luers, S. Jurczyk, and C. Mills. "Adaptive Instrument Module: Space Instrument Controller "Brain"through Progammable Logic Devices". In:
 D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 256–260, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [de G 01] H. de Garis, L. de Penning, A. Bullner, and D. Decesare. "Early Experiments on the CAM-Brain Machine (CBM)". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 211–219, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Doli 01] B. Dolin, F. H. Bennett III, and E. G. Rieffel. "Methods for evolving robust distributed robot control software: coevolutionary and single population techniques". In:
 D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 21–29, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Edwa 01] R. T. Edwards and C. J. Kim. "Breaking the Resistivity Barrier". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 167–171, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Gall 01] J. C. Gallagher. "A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 48–55, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Grah 01] R. I. Graham and T. Arslan. "Rule Evolution in Order Based Diagnostic Systems". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 280–286, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Hadd 01] P. C. Haddow and G. Tufte. "Bridging the Genotype-Phenotype Mapping for Digital FPGAs". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 109–115, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.

- [Hern 01] A. Hernandez-Aguirre, B. P. Buckles, and C. A. C. Coello. "On Learning KDNF Boolean Formulas". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 240–246, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Houn 01a] B. I. Hounsell and T. Arslan. "Evolutionary Design and Adaption of Digital Filters within an Embedded Fault". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 127–135, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Houn 01b] B. I. Hounsell and T. Arslan. "Evolutionary Design and Adaption of Digital Filters within an Embedded Fault". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 127–135, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Jack 01] A. H. Jackson and A. M. Tyrrell. "Asynchronous Embryonics". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 201–210, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Kaza 01] S. Kazadi, Y. Qi, I. Park, N. Huang, P. Hwu, B. Kwan, W. Lue, and H. Li. "Insufficiency of Piecewise Evolution". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 223–231, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Lang 01] J. Langeheine, J. Becker, S. Foilling, K. Meire, and J. Schemmel. "A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analong Electronic Circuits". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 172–175, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Lind 01] D. S. Linden. "A System for Evolving Antennas In-Situ". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 249–255, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Lock 01] J. W. Lockwood. "Evovable Internet Hardware Platforms". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 271–279, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Mill 01] J. F. Miller and M. Hartmann. "Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 116–123, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [More 01a] J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany. "An In-System Routing Strategy for Evolvable Hardware Programmable Platforms". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 157– 166, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [More 01b] J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany. "An In-System Routing Strategy for Evolvable Hardware Programmable Platforms". In: D. Keymeulen, A. Stoica, J. Lohn, and

- R. S. Zebulum, Eds., *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 157–166, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Pfaf 01] J. O. Pfaffmann and K. P. Zauner. "Scouting COntext-Sensitive Components". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 14–20, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Port 01] R. Porter, M. Gokhale, N. Harvey, S. Perkins, and C. Young. "Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification". In:
 D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 261–270, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Rams 01] E. Ramsden. "The ispPAC Family of Reconfigurable Analog Circuits". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 176–181, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Sale 01] J. H. Saleh, D. E. Hastings, and D. J. Newman. "Extracting the Essence of Flexibility in System Design". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 59–72, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Sant 01] C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco, and M. H. Szwarcman. "PAMA-Programmable Analog Multiplexter Array". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 36–43, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Schi 01] T. Schiner, X. Yao, and P. Liu. "Digital filter Design Using Multiple Pareto Fronts". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 136–145, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Sego 01] J. L. Segovia-Juarez and S. Colombano. "Mutation Buffering Capabilities of the Hypernetwork Model". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 7–13, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Sino 01] H. T. Sinohara, M. A. C. Pacheco, and M. M. R. Vellasco. "Repair of Analog Circuits: Extrinsic and Instrinsic Evolutionary Techniques". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 44–47, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Stau 01] A. Stauffer, D. Mange, G. Tempesti, and C. Teuscher. "BioWatch: A Giant Electronic Bio-Inspired Watch". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 185–192, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.
- [Stoi 01] A. Stoica, R. Zebulum, and D. Keymeulen. "Progress and Challenges in Building Evolvable Devices". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., The Third NASA/DoD workshop on Evolvable Hardware, pp. 33–35, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.

[Tyrr 01] A. M. Tyrrell, G. Hollingworth, and S. L. Smith. "Evolutionary Strategies and Intrinsic Fault Tolerance". In: D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, Eds., *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 98–106, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California, 12-14 July 2001.