Список литературы

[Bennett III and Rieffel(2000)] F. H Bennett III and E. Rieffel. 2000. Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 43–52, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Bradley et al.(2000)Bradley, Ortega-Sanchez, and Tyrrell] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell. 2000. Embryonics + immunotronics: A bio-inspired approach to fault tolerance. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–224, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Coello et al.(2000)Coello, Aguirre, and Buckles] C. Coello, A. Aguirre, and B. Buckles. 2000. Evolutionary multiobjective design of combinational logic circuits. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 161–170, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis et al.(2000)de Garis, Buller, Dob, Honlet, Guttikonda, and Decesare] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare. 2000. Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 225–234, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Flockton and Sheehan(2000)] S. Flockton and K. Sheehan. 2000. Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 117–124, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hollingworth et al.(2000)Hollingworth, Smith, and Tyrrell] G. Hollingworth, S. Smith, and A. Tyrrell. 2000. Safe intrinsic evolution of virtex devices. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 195–202, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Imamura et al.(2000)Imamura, Foster, and Krings] K. Imamura, J. Foster, and A. Krings. 2000. Bidirectional incremental evolution in extrinsic evolvable hardware. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 75–80, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Jonathan et al.(2000)Jonathan, Zebulum, Pacheco, and Vellasco] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco. 2000. Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 133–140, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Kalganova(2000)] T. Kalganova. 2000. Bidirectional incremental evolution in extrinsic evolvable hardware. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 65–74, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Korkin et al.(2000)Korkin, Fehr, and Jeffery] M. Korkin, G. Fehr, and G. Jeffery. 2000. Evolving hardware on a large scale. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 173–182, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Koza et al.(2000)Koza, Yu, Keane, and Mydlowec] John R. Koza, Jessen Yu, Martin A. Keane, and William Mydlowec. 2000. Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 5–16, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Lee et al.(2000)Lee, Hall, Perkowski, and Jun] C. Lee, D. Hall, M. Perkowski, and D. Jun. 2000. Self-repairable epld: Design, self-repair, and evaluation methodology. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 183–194, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levi(2000)] D. Levi. 2000. Hereboy: A fast evolutionary algorithm. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 17–24, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levy et al.(2000)Levy, Lepri, Sanchez, Ritter, and Sipper] R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper. 2000. Slate of the art: An evolving fpga-based board for handwritten-digit recognition. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 237–244, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Mange et al.(2000)Mange, Sipper, Stauffer, and Tempesti] D. Mange, M. Sipper, A. Stauffer, and G. Tempesti. 2000. Toward self-repairing and self-replicating hardware: The embryonics approach. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–214, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Marston et al.(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka, and Higuchi] N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi. 2000. An evolutionary approach to ghz digital systems. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 125–131, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Masner et al.(2000)Masner, Cavalieri, Frenzel, and Foster] J. Masner, J. Cavalieri, J. Frenzel, and J. Foster. 2000. Size versus robustness in evolved sorting networks: Is bigger better? In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 81–87, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Milano and Koumoutsakos(2000)] M. Milano and P. Koumoutsakos. 2000. A clustering genetic algorithm for actuator optimization in flow control. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 263–270, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Pollack and Lipson(2000)] J. Pollack and H. Lipson. 2000. The golem project: Evolving hardware bodies and brains. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 37–42, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Seok et al.(2000)Seok, Lee, Zhang, Lee, and Sim] H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim. 2000. Genetic programming of process decomposition strategies for evolvable hardware. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 25–34, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica et al.(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel, and Duong] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong. 2000. Evolution of analog circuits on field programmable transistor arrays. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 99–108, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Thompson and Wasshuber(2000)] A. Thompson and C. Wasshuber. 2000. Evolutionary design of single electron systems. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 109–116, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Torresen(2000)] J. Torresen. 2000. Scalable evolvable hardware applied to road image recognition. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 245–252, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tufte and Haddow(2000)] G. Tufte and P. Haddow. 2000. Evolving an adaptive digital filter. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 143–150, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Vassilev and Miller(2000)] V. Vassilev and J. Miller. 2000. Scalability problems of digital circuit evolution: Evolvability and efficient designs. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 55–64, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Yasunaga et al.(2000)Yasunaga, Nakamura, Yoshihara, and Kim] M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim. 2000. Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 253–262, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Zebulum et al.(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco, and Szwarcman] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman. 2000. A reconfigurable platform for the automatic synthesis of analog circuits. In *The Second NASA/DoD workshop on Evolvable Hardware*, pages 91–98, Palo Alto, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.