## Список литературы

- [1] Koza, J. R., Yu, J., Keane, M. A., and Mydlowec, W. (2000) Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 5-16, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [2] Levi, D. (2000) Hereboy: A fast evolutionary algorithm. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 17–24, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [3] Seok, H., Lee, K., Zhang, B., Lee, D., and Sim, K. (2000) Genetic programming of process decomposition strategies for evolvable hardware. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 25-34, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [4] Pollack, J. and Lipson, H. (2000) The golem project: Evolving hardware bodies and brains. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 37-42, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [5] Bennett III, F. H. and Rieffel, E. (2000) Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 43–52, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [6] Vassilev, V. and Miller, J. (2000) Scalability problems of digital circuit evolution: Evolvability and efficient designs. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 55-64, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [7] Kalganova, T. (2000) Bidirectional incremental evolution in extrinsic evolvable hardware. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 65-74, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [8] Imamura, K., Foster, J., and Krings, A. (2000) Bidirectional incremental evolution in extrinsic evolvable hardware. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 75–80, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [9] Masner, J., Cavalieri, J., Frenzel, J., and Foster, J. (2000) Size versus robustness in evolved sorting networks: Is bigger better? Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 81–87, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [10] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M., and Szwarcman, M. (2000) A reconfigurable platform for the automatic synthesis of analog circuits. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 91–98, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [11] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R., and Duong, V. (2000) Evolution of analog circuits on field programmable transistor arrays. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 99–108, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [12] Thompson, A. and Wasshuber, C. (2000) Evolutionary design of single electron systems. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 109–116, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [13] Flockton, S. and Sheehan, K. (2000) Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 117–124, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [14] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K., and Higuchi, T. (2000) An evolutionary approach to ghz digital systems. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 125–131, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [15] Jonathan, M., Zebulum, R., Pacheco, M., and Vellasco, M. (2000) Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 133-140, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [16] Tufte, G. and Haddow, P. (2000) Evolving an adaptive digital filter. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 143-150, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [17] Coello, C., Aguirre, A., and Buckles, B. (2000) Evolutionary multiobjective design of combinational logic circuits. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 161–170, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [18] Korkin, M., Fehr, G., and Jeffery, G. (2000) Evolving hardware on a large scale. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 173–182, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [19] Lee, C., Hall, D., Perkowski, M., and Jun, D. (2000) Self-repairable eplds: Design, self-repair, and evaluation methodology. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 183-194, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [20] Hollingworth, G., Smith, S., and Tyrrell, A. (2000) Safe intrinsic evolution of virtex devices. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 195–202, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [21] Mange, D., Sipper, M., Stauffer, A., and Tempesti, G. (2000) Toward self-repairing and self-replicating hardware: The embryonics approach. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 205–214, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [22] Bradley, D., Ortega-Sanchez, C., and Tyrrell, A. (2000) Embryonics + immunotronics: A bio-inspired approach to fault tolerance. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 205–224, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [23] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P., and Decesare, D. (2000) Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 225-234, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [24] Levy, R., Lepri, S., Sanchez, E., Ritter, G., and Sipper, M. (2000) Slate of the art: An evolving fpga-based board for handwritten-digit recognition. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 237–244, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [25] Torresen, J. (2000) Scalable evolvable hardware applied to road image recognition. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, Palo Alto, California, 13-15 July, pp. 245–252, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [26] Yasunaga, M., Nakamura, T., Yoshihara, I., and Kim, J. (2000) Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 253–262, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [27] Milano, M. and Koumoutsakos, P. (2000) A clustering genetic algorithm for actuator optimization in flow control. Lohn, J., Stoica, A., and Keymeulen, D. (eds.), The Second NASA/DoD workshop on Evolvable Hardware, Palo Alto, California, 13-15 July, pp. 263–270, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.