

## Список литературы

- [1] J. L. SEGOVIA-JUAREZ and S. COLOMBANO, Mutation Buffering Capabilities of the Hypernetwork Model, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 7–13, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [2] J. O. PFAFFMANN and K. P. ZAUNER, Scouting COntext-Sensitive Components, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 14–20, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [3] B. DOLIN, F. H. BENNETT III, and E. G. RIEFFEL, Methods for evolving robust distributed robot control software: coevolutionary and single population techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 21–29, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [4] A. STOICA, R. ZEBULUM, and D. KEYMEULEN, Progress and Challenges in Building Evolvable Devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 33–35, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [5] C. C. SANTINI, R. ZEBULUM, M. A. C. PACHECO, M. M. R. VELLASCO, and M. H. SZWARCMAN, PAMA-Programmable Analog Multiplexer Array, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 36–43, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [6] H. T. SINOHARA, M. A. C. PACHECO, and M. M. R. VELLASCO, Repair of Analog Circuits: Extrinsic and Intrinsic Evolutionary Techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 44–47, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [7] J. C. GALLAGHER, A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 48–55, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [8] J. H. SALEH, D. E. HASTINGS, and D. J. NEWMAN, Extracting the Essence of Flexibility in System Design, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 59–72, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [9] M. ABRAMOVICI, J. M. EMMERT, and C. E. STROUD, Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault Tolerance for FPGAs in Adaptive Computing Systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 73–92, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [10] A. M. TYRRELL, G. HOLLINGWORTH, and S. L. SMITH, Evolutionary Strategies and Intrinsic Fault Tolerance, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 98–106, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [11] P. C. HADDOW and G. TUFTE, Bridging the Genotype-Phenotype Mapping for Digital FPGAs, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 109–115, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [12] J. F. MILLER and M. HARTMANN, Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 116–123, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [13] B. I. HOUNSELL and T. ARSLAN, Evolutionary Design and Adaption of Digital Filters within an Embedded Fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [14] B. I. HOUNSELL and T. ARSLAN, Evolutionary Design and Adaption of Digital Filters within an Embedded Fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [15] T. SCHINER, X. YAO, and P. LIU, Digital filter Design Using Multiple Pareto Fronts, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 136–145, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [16] O. CASTILLO, O. MONTIEL, R. SEPULVEDA, and P. MELIN, Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 146–153, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [17] J. M. MORENO AROSTEGUI, E. SANCHEZ, and J. CABESTANY, An In-System Routing Strategy for Evolvable Hardware Programmable Platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [18] J. M. MORENO AROSTEGUI, E. SANCHEZ, and J. CABESTANY, An In-System Routing Strategy for Evolvable Hardware Programmable Platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [19] R. T. EDWARDS and C. J. KIM, Breaking the Resistivity Barrier, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 167–171, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [20] J. LANGEHEINE, J. BECKER, S. FOILLING, K. MEIRE, and J. SCHEMMEL, A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analog Electronic Circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 172–175, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [21] E. RAMSDEN, The ispPAC Family of Reconfigurable Analog Circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 176–181, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [22] A. STAUFFER, D. MANGE, G. TEMPESTI, and C. TEUSCHER, BioWatch: A Giant Electronic Bio-Inspired Watch, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 185–192, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [23] D. W. BRADLEY and A. M. TYRELL, The Architecture for a Hardware Immune System, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 193–200, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [24] A. H. JACKSON and A. M. TYRRELL, Asynchronous Embryonics, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 201–210, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [25] H. DE GARIS, L. DE PENNING, A. BULLNER, and D. DECESARE, Early Experiments on the CAM-Brain Machine (CBM), in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 211–219, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [26] S. KAZADI, Y. QI, I. PARK, N. HUANG, P. HWU, B. KWAN, W. LUE, and H. LI, Insufficiency of Piecewise Evolution, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 223–231, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [27] A. HERNANDEZ-AGUIRRE, B. P. BUCKLES, and C. A. C. COELLO, On Learning KDNF Boolean Formulas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 240–246, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [28] D. S. LINDEN, A System for Evolving Antennas In-Situ, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 249–255, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [29] A. G. DARREN, R. CONDE, B. CHERN, P. LUERS, S. JURCZYK, and C. MILLS, Adaptive Instrument Module: Space Instrument Controller "Brain" through Programmable Logic Devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 256–260, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [30] R. PORTER, M. GOKHALE, N. HARVEY, S. PERKINS, and C. YOUNG, Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 261–270, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [31] J. W. LOCKWOOD, Evolvable Internet Hardware Platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 271–279, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [32] R. I. GRAHAM and T. ARSLAN, Rule Evolution in Order Based Diagnostic Systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. KEYMEULEN, A. STOICA, J. LOHN, and R. S. ZEBULUM, pp. 280–286, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.