Список литературы

[Abramovici 01]

M. Abramovici, J. M. Emmert & C. E. Stroud. Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault Tolerance for FPGAs in Adaptive Computing Systems. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 73–92, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Bradley 01]

D. W. Bradley & A. M. Tyrell. *The Architecture for a Hardware Immune System*. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 193–200, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Castillo 01]

O. Castillo, O. Montiel, R. Sepulveda & P. Melin. Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 146–153, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Darren 01]

A. G. Darren, R. Conde, B. Chern, P. Luers, S. Jurczyk & C. Mills. Adaptive Instrument Module: Space Instrument Controller "Brain"through Progammable Logic Devices. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 256–260, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis 01]

H. de Garis, L. de Penning, A. Bullner & D. Decesare. Early Experiments on the CAM-Brain Machine (CBM). In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 211–219, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Dolin 01]

Brad Dolin, Forrest H Bennett III & Eleanor G. Rieffel. Methods for evolving robust distributed robot control software: coevolutionary and single population techniques. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 21–29, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Edwards 01]

R. T. Edwards & C. J. Kim. Breaking the Resistivity Barrier. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 167–171, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Gallagher 01]

J. C. Gallagher. A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 48–55, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Graham 01]

R. I. Graham & T. Arslan. Rule Evolution in Order Based Diagnostic Systems. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 280–286, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Haddow 01]

P. C. Haddow & G. Tufte. Bridging the Genotype-Phenotype Mapping for Digital FPGAs. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 109–115, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hernandez-Aguirre 01] A. Hernandez-Aguirre, B. P. Buckles & C. A. C. Coello. On Learning KDNF Boolean Formulas. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 240–246, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hounsell 01a]

B. I. Hounsell & T. Arslan. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 127–135, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hounsell 01b]

B. I. Hounsell & T. Arslan. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 127–135, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Jackson 01]

A. H. Jackson & A. M. Tyrrell. Asynchronous Embryonics. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 201–210, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Kazadi 01]

S. Kazadi, Y. Qi, I. Park, N. Huang, P. Hwu, B. Kwan, W. Lue & H. Li. Insufficiency of Piecewise Evolution. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 223–231, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Langeheine 01]

J. Langeheine, J. Becker, S. Foilling, K. Meire & J. Schemmel. A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analong Electronic Circuits. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 172–175, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Linden 01]

D. S. Linden. A System for Evolving Antennas In-Situ. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 249–255, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Lockwood 01]

Evovable Internet Hardware Platforms. J. W. Lockwood. Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 271–279, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Miller 01]

J. F. Miller & M. Hartmann. Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 116–123, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Moreno Arostegui 01a] J. M. Moreno Arostegui, E. Sanchez & J. Cabestany. Routing Strategy for Evolvable Hardware Programmable Platforms. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 157–166, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Moreno Arostegui 01b] J. M. Moreno Arostegui, E. Sanchez & J. Cabestany. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 157–166, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Pfaffmann 01]

J. O. Pfaffmann & K. P. Zauner. Scouting Context-Sensitive Components. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 14–20, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Porter 01]

R. Porter, M. Gokhale, N. Harvey, S. Perkins & C. Young. Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 261–270, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Ramsden 01]

E. Ramsden. The ispPAC Family of Reconfigurable Analog Circuits. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 176–181, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Saleh 01]

J. H. Saleh, D. E. Hastings & D. J. Newman. Extracting the Essence of Flexibility in System Design. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 59-72, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Santini 01]

C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco & M. H. Szwarcman. PAMA-Programmable Analog Multiplexter Array. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 36–43, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Schiner 01]

T. Schiner, X. Yao & P. Liu. Digital filter Design Using Multiple Pareto Fronts. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 136–145, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Segovia-Juarez 01]

J. L. Segovia-Juarez & S. Colombano. *Mutation Buffering Capabilities of the Hypernetwork Model*. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 7–13, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Sinohara 01]

H. T. Sinohara, M. A. C. Pacheco & M. M. R. Vellasco. Repair of Analog Circuits: Extrinsic and Instrinsic Evolutionary Techniques. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 44–47, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stauffer 01]

A. Stauffer, D. Mange, G. Tempesti & C. Teuscher. *BioWatch: A Giant Electronic Bio-Inspired Watch*. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 185–192, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica 01]

A. Stoica, R. Zebulum & D. Keymeulen. *Progress and Challenges in Building Evolvable Devices*. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 33–35, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tyrrell 01]

A. M. Tyrrell, G. Hollingworth & S. L. Smith. *Evolutionary Strategies and Intrinsic Fault Tolerance*. In Didier Keymeulen, Adrian Stoica, Jason Lohn & Ricardo S. Zebulum, editeurs, The Third NASA/DoD workshop on Evolvable Hardware, pages 98–106, Long Beach, California, 12-14 July 2001. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.