

Список литературы

- [1] S. G. Araujo, A. Mesquita, and A. C. P. Pedroza, Using genetic programming and high level synthesis to design optimized datapath, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 434–445, Trondheim, Norway, 2003, Springer-Verlag.
- [2] S. Aunet and M. Hartmann, Real-time reconfigurable linear threshold elements and some applications to neural hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 365–376, Trondheim, Norway, 2003, Springer-Verlag.
- [3] M. A. H. B. Azhar and K. R. Dimond, Hardware implementation of a genetic controller and effects of training on evolution, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 344–354, Trondheim, Norway, 2003, Springer-Verlag.
- [4] W. Van Belle, T. Mens, and T. D’Hondt, Using genetic programming to generate protocol adaptors for interprocess communication, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 422–433, Trondheim, Norway, 2003, Springer-Verlag.
- [5] P. J. Bentley, Evolving fractal proteins, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 81–92, Trondheim, Norway, 2003, Springer-Verlag.
- [6] J. Blynel, Evolving reinforcement learning-like abilities for robots, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 320–331, Trondheim, Norway, 2003, Springer-Verlag.
- [7] R. Canham and A. M. Tyrrell, A learning, multi-layered, hardware artificial immune system implemented upon an embryonic array, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 174–185, Trondheim, Norway, 2003, Springer-Verlag.
- [8] C. A. C. Coello, E. H. Luna, and A. H. Aguirre, Use of particle swarm optimization to design combinational logic circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 398–409, Trondheim, Norway, 2003, Springer-Verlag.
- [9] F. Corno, F. Cumani, and G. Squillero, Exploiting auto-adaptive μ -GP for highly effective test programs generation, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 262–273, Trondheim, Norway, 2003, Springer-Verlag.
- [10] K. L. Downing, Developmental models for emergent computation, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 105–116, Trondheim, Norway, 2003, Springer-Verlag.
- [11] J. Eriksson *et al.*, Spiking neural networks for reconfigurable POEtic tissue, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 165–173, Trondheim, Norway, 2003, Springer-Verlag.
- [12] G. G. Estrada, A note on designing logical circuits using SAT, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 410–421, Trondheim, Norway, 2003, Springer-Verlag.

- [13] H. de Degaris, A. Gaur, and R. Sriram, Quantum versus evolutionary systems. total versus sampled search, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 457–466, Trondheim, Norway, 2003, Springer-Verlag.
- [14] M. Garvie and A. Thompson, Evolution of self-diagnosing hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 238–248, Trondheim, Norway, 2003, Springer-Verlag.
- [15] R. Goldsmith, Real world hardware evolution: A mobile platform for sensor evolution, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 355–364, Trondheim, Norway, 2003, Springer-Verlag.
- [16] A. J. Greensted and A. M. Tyrrell, Fault tolerance via endocrinologic based communication for multiprocessor systems, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 24–34, Trondheim, Norway, 2003, Springer-Verlag.
- [17] R. van de Haar and J. Hoekstra, Simulation of a neural node using SET technology, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 377–386, Trondheim, Norway, 2003, Springer-Verlag.
- [18] A. H. Aguirre, E. C. G. Equihua, and C. A. Coello Coello, Synthesis of boolean functions using information theory, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 218–227, Trondheim, Norway, 2003, Springer-Verlag.
- [19] S. Kumar and P. J. Bentley, Biologically inspired evolutionary development, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 57–68, Trondheim, Norway, 2003, Springer-Verlag.
- [20] J. H. Li and M. H. Lim, Evolvable fuzzy system for ATM cell scheduling, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 208–217, Trondheim, Norway, 2003, Springer-Verlag.
- [21] J. Lohn, G. Larchev, and R. DeMara, A genetic representation for evolutionary fault recovery in Virtex FPGAs, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 47–56, Trondheim, Norway, 2003, Springer-Verlag.
- [22] H. H. Lund, R. L. Larsen, and E. H. Østergaard, Distributed control in self-reconfigurable robots, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 296–307, Trondheim, Norway, 2003, Springer-Verlag.
- [23] J. F. Miller and P. Thomson, A developmental method for growing graphs and circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 93–104, Trondheim, Norway, 2003, Springer-Verlag.
- [24] C. Ortega-Sanchez, J. Torres-Jimenez, and J. Morales-Cruz, Routing of embryonic arrays using genetic algorithms, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 249–261, Trondheim, Norway, 2003, Springer-Verlag.

- [25] E. H. Østergaard and H. H. Lund, Co-evolving complex robot behavior, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 308–319, Trondheim, Norway, 2003, Springer-Verlag.
- [26] P. van Remortel, J. Ceuppens, A. Defaweux, T. Lenaerts, and B. Manderick, Developmental effects on tuneable fitness landscapes, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 117–128, Trondheim, Norway, 2003, Springer-Verlag.
- [27] D. Roggen, D. Floreano, and C. Mattiussi, A morphogenetic evolutionary system: Phylogenesis of the POEtic circuit, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 153–164, Trondheim, Norway, 2003, Springer-Verlag.
- [28] T. Schmitz, S. Hohmann, K. Meier, J. Schemmel, and F. Schurmann, Speeding up hardware evolution: A coprocessor for evolutionary algorithms, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 274–285, Trondheim, Norway, 2003, Springer-Verlag.
- [29] T. Schnier and X. Yao, Using negative correlation to evolve fault-tolerant circuits, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 35–46, Trondheim, Norway, 2003, Springer-Verlag.
- [30] L. Sekanina, Virtual reconfigurable circuits for real-world applications of evolvable hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 186–197, Trondheim, Norway, 2003, Springer-Verlag.
- [31] S. L. Smith, D. P. Crouch, and A. M. Tyrrell, Evolving image processing operations for an evolvable hardware environment, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 332–343, Trondheim, Norway, 2003, Springer-Verlag.
- [32] F. Tanaka, A. Kameda, M. Yamamoto, and A. Ohuchi, The effect of the bulge loop upon the hybridization process in DNA computing, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 446–456, Trondheim, Norway, 2003, Springer-Verlag.
- [33] G. Tempesti *et al.*, Ontogenetic development and fault tolerance in the POEtic tissue, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 141–152, Trondheim, Norway, 2003, Springer-Verlag.
- [34] C. Teuscher and M. S. Capcarrere, On fireflies, cellular systems, and evolware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 1–12, Trondheim, Norway, 2003, Springer-Verlag.
- [35] J. Torresen, Evolving multiplier circuits by training set and training vector partitioning, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 228–237, Trondheim, Norway, 2003, Springer-Verlag.
- [36] G. Tufte and P. C. Haddow, Building knowledge into developmental rules for circuit design, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 69–80, Trondheim, Norway, 2003, Springer-Verlag.

- [37] A. M. Tyrrell *et al.*, POEtic tissue: An integrated architecture for bio-inspired hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 129–140, Trondheim, Norway, 2003, Springer-Verlag.
- [38] N. Venkateswaran and C. Chandramouli, General purpose processor architecture for modeling stochastic biological neuronal assemblies, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 387–397, Trondheim, Norway, 2003, Springer-Verlag.
- [39] M. Yasunaga, I. Yoshihara, and J. H. Kim, Gene finding using evolvable reasoning hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 198–207, Trondheim, Norway, 2003, Springer-Verlag.
- [40] R. S. Zebulum *et al.*, Automatic evolution of signal separators using reconfigurable hardware, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 286–295, Trondheim, Norway, 2003, Springer-Verlag.
- [41] L. Zinchenko, H. Muhlenbein, V. Kureichik, and T. Mahnig, A comparison of different circuit representations for evolutionary analog circuit design, in *Evolvable Systems: From Biology to Hardware, Fifth International Conference, ICES 2003*, edited by A. M. Tyrrell, P. C. Haddow, and J. Torresen, volume 2606 of *LNCS*, pp. 13–23, Trondheim, Norway, 2003, Springer-Verlag.