Список литературы

[Abramovici et al., 2001] Abramovici, M.; Emmert, J. M.; and Stroud, C. E. (2001). "Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis,and Fault TOlerance for FPGAs in Adaptive Computing Systems". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 73–92, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Bradley and Tyrell, 2001] Bradley, D. W. and Tyrell, A. M. (2001). "The Architecture for a Hardware Immune System". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 193–200, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Castillo et al., 2001] Castillo, O.; Montiel, O.; Sepulveda, R.; and Melin, P. (2001). "Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 146–153, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Darren et al., 2001] Darren, A. G.; Conde, R.; Chern, B.; Luers, P.; Jurczyk, S.; and Mills, C. (2001). "Adaptive Instrument Module: Space Instrument Controller "Brain"through Progammable Logic Devices". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 256–260, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis et al., 2001] de Garis, H.; de Penning, L.; Bullner, A.; and Decesare, D. (2001). "Early Experiments on the CAM-Brain Machine (CBM)". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 211–219, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Dolin et al., 2001] Dolin, B.; Bennett III, F. H.; and Rieffel, E. G. (2001). "Methods for evolving robust distributed robot control software: coevolutionary and single population techniques". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 21–29, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Edwards and Kim, 2001] Edwards, R. T. and Kim, C. J. (2001). "Breaking the Resistivity Barrier". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 167–171, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Gallagher, 2001] Gallagher, J. C. (2001). "A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 48–55, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Graham and Arslan, 2001] Graham, R. I. and Arslan, T. (2001). "Rule Evolution in Order Based Diagnostic Systems". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 280–286, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Haddow and Tufte, 2001] Haddow, P. C. and Tufte, G. (2001). "Bridging the Genotype-Phenotype Mapping for Digital FPGAs". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 109–115, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hernandez-Aguirre et al., 2001] Hernandez-Aguirre, A.; Buckles, B. P.; and Coello, C. A. C. (2001). "On Learning KDNF Boolean Formulas". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum,

R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 240–246, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hounsell and Arslan, 2001a] Hounsell, B. I. and Arslan, T. (2001a). "Evolutionary Design and Adaption of Digital Filters within an Embedded Fault". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 127–135, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hounsell and Arslan, 2001b] Hounsell, B. I. and Arslan, T. (2001b). "Evolutionary Design and Adaption of Digital Filters within an Embedded Fault". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 127–135, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Jackson and Tyrrell, 2001] Jackson, A. H. and Tyrrell, A. M. (2001). "Asynchronous Embryonics". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 201–210, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Kazadi et al., 2001] Kazadi, S.; Qi, Y.; Park, I.; Huang, N.; Hwu, P.; Kwan, B.; Lue, W.; and Li, H. (2001). "Insufficiency of Piecewise Evolution". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 223–231, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Langeheine et al., 2001] Langeheine, J.; Becker, J.; Foilling, S.; Meire, K.; and Schemmel, J. (2001). "A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analong Electronic Circuits". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 172–175, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Linden, 2001] Linden, D. S. (2001). "A System for Evolving Antennas In-Situ". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 249–255, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Lockwood, 2001] Lockwood, J. W. (2001). "Evovable Internet Hardware Platforms". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 271–279, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Miller and Hartmann, 2001] Miller, J. F. and Hartmann, M. (2001). "Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 116–123, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Moreno Arostegui et al., 2001a] Moreno Arostegui, J. M.; Sanchez, E.; and Cabestany, J. (2001a). "An In-System Routing Strategy for Evolvable Hardware Programmable Platforms". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 157–166, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Moreno Arostegui et al., 2001b] Moreno Arostegui, J. M.; Sanchez, E.; and Cabestany, J. (2001b). "An In-System Routing Strategy for Evolvable Hardware Programmable Platforms". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 157–166, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Pfaffmann and Zauner, 2001] Pfaffmann, J. O. and Zauner, K. P. (2001). "Scouting COntext-Sensitive Components". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 14–20, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Porter et al., 2001] Porter, R.; Gokhale, M.; Harvey, N.; Perkins, S.; and Young, C. (2001). "Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 261–270, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Ramsden, 2001] Ramsden, E. (2001). "The ispPAC Family of Reconfigurable Analog Circuits". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 176–181, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Saleh et al., 2001] Saleh, J. H.; Hastings, D. E.; and Newman, D. J. (2001). "Extracting the Essence of Flexibility in System Design". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 59–72, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Santini et al., 2001] Santini, C. C.; Zebulum, R.; Pacheco, M. A. C.; Vellasco, M. M. R.; and Szwarcman, M. H. (2001). "PAMA-Programmable Analog Multiplexter Array". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 36–43, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Schiner et al., 2001] Schiner, T.; Yao, X.; and Liu, P. (2001). "Digital filter Design Using Multiple Pareto Fronts". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 136–145, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Segovia-Juarez and Colombano, 2001] Segovia-Juarez, J. L. and Colombano, S. (2001). "Mutation Buffering Capabilities of the Hypernetwork Model". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 7–13, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Sinohara et al., 2001] Sinohara, H. T.; Pacheco, M. A. C.; and Vellasco, M. M. R. (2001). "Repair of Analog Circuits: Extrinsic and Instrinsic Evolutionary Techniques". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 44–47, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stauffer et al., 2001] Stauffer, A.; Mange, D.; Tempesti, G.; and Teuscher, C. (2001). "BioWatch: A Giant Electronic Bio-Inspired Watch". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 185–192, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica et al., 2001] Stoica, A.; Zebulum, R.; and Keymeulen, D. (2001). "Progress and Challenges in Building Evolvable Devices". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 33–35, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tyrrell et al., 2001] Tyrrell, A. M.; Hollingworth, G.; and Smith, S. L. (2001). "Evolutionary Strategies and Intrinsic Fault Tolerance". In Keymeulen, D.; Stoica, A.; Lohn, J.; and Zebulum, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pages 98–106, Long Beach, California. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.