

Список литературы

- [1] F. H Bennett III and E. Rieffel. Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 43–52, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: bennett:2000:EH

- [2] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell. Embryonics + immunotronics: A bio-inspired approach to fault tolerance. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–224, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Bradley:2000:eh

- [3] C. Coello, A. Aguirre, and B. Buckles. Evolutionary multiobjective design of combinational logic circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 161–170, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Coello:2000:eh

- [4] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare. Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 225–234, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: deGaris:2000:eh

- [5] S. Flockton and K. Sheehan. Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 117–124, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Flockton:2000:eh

- [6] G. Hollingworth, S. Smith, and A. Tyrrell. Safe intrinsic evolution of virtex devices. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 195–202, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Hollingworth:2000:eh

- [7] K. Imamura, J. Foster, and A. Krings. Bidirectional incremental evolution in extrinsic evolvable hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 75–80, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Imamura:2000:eh

- [8] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco. Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 133–140, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Johnathan:2000:eh

- [9] T. Kalganova. Bidirectional incremental evolution in extrinsic evolvable hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 65–74, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Kalganova:2000:eh

- [10] M. Korkin, G. Fehr, and G. Jeffery. Evolving hardware on a large scale. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 173–182, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Korkin:2000:eh

- [11] John R. Koza, Jessen Yu, Martin A. Keane, and William Mydlowec. Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 5–16, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: koza:2000:EH

- [12] C. Lee, D. Hall, M. Perkowski, and D. Jun. Self-repairable eplds: Design, self-repair, and evaluation methodology. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 183–194, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Lee:2000:eh

- [13] D. Levi. Hereboy: A fast evolutionary algorithm. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 17–24, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Levi:2000:eh

- [14] R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper. Slate of the art: An evolving fpga-based board for handwritten-digit recognition. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 237–244, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Levy:2000:eh

- [15] D. Mange, M. Sipper, A. Stauffer, and G. Tempesti. Toward self-repairing and self-replicating hardware: The embryonics approach. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 205–214, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Mange:2000:eh

- [16] N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi. An evolutionary approach to ghz digital systems. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 125–131, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Marston:2000:eh

- [17] J. Masner, J. Cavalieri, J. Frenzel, and J. Foster. Size versus robustness in evolved sorting networks: Is bigger better? In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 81–87, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Masner:2000:eh

- [18] M. Milano and P. Koumoutsakos. A clustering genetic algorithm for actuator optimization in flow control. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 263–270, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Milano:2000:eh

- [19] J. Pollack and H. Lipson. The golem project: Evolving hardware bodies and brains. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 37–42, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Pollack:2000:eh

- [20] H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim. Genetic programming of process decomposition strategies for evolvable hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 25–34, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: seok:2000:EH

- [21] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong. Evolution of analog circuits on field programmable transistor arrays. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 99–108, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Stoica:2000:eh

- [22] A. Thompson and C. Wasshuber. Evolutionary design of single electron systems. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 109–116, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Thompson:2000:eh

- [23] J. Torresen. Scalable evolvable hardware applied to road image recognition. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 245–252, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Torresen:2000:eh

- [24] G. Tufte and P. Haddow. Evolving an adaptive digital filter. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 143–150, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Tufte:2000:eh

- [25] V. Vassilev and J. Miller. Scalability problems of digital circuit evolution: Evolvability and efficient designs. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 55–64, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Vassilev:2000:eh

- [26] M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim. Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 253–262, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Yasunaga:2000:eh

- [27] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman. A reconfigurable platform for the automatic synthesis of analog circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen, editors, *The Second NASA/DoD workshop on Evolvable Hardware*, pages 91–98, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

KEY: Zebulum:2000:eh