Список литературы

[1] C. Coello, E. Alba, G. Luque, and A. Aguirre, Comparing Different Serial and Parallel Heuristics to Design Combinatorial Logic Circuits, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 3–12, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[2] A. Aguirre and C. Coello, Fitness Landscape and Evolutionary Boolean Synthesis using Information Theory Concepts, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 13–20, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[3] S. J. Louis, Learning for Evolutionary Design, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 17–21, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[4] A.Stoica, R.Zebulum, X.Guo, D.Keymeulen, V. Duong, and M.I.Ferguson, Silicon Validation of Evolution-Designed Circuits, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 21–25, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[5] K. Vinger and J. Torresen, Implementing Evolution of FIR-Filters Efficiently in an FPGA, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 26–29, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[6] J. Koza, M. Keane, and M. Streeter, the Importance of Reuse and Development in Evolvable Hardware, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 33–42, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[7] J. Gallagher, The Once and Future Anaolg Alternative: Evolvable Hardware and Analog Computation, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 43–49, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[8] J. Botelho, B. Leonardo, P. Vieira, and A. Mesquita, An Experiment on Nonlinear synthesis Using Evolutionary Techniques Based only on CMOS Transistors, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 50–58, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[9] G. Greenwood, E. Ramsden, and S. Ahmed, An Empirical Comparison of Evolutionary Algorithms for Evolvable Hardware with Minimum Time-To-Reconfigure requirements, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 59–66, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[10] V. Aggarwal, Evolving Sinusoidal Oscillators Using Genetic Algorithms, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 67–76, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[11] J. Plante, H. Shaw, L. Mickens, and C. Johnson-Be, Overview of Field Programmable Analog Arrays as Enabling Technology for Evolvable Hardware for High Reliability Systems, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 77–78, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[12] D. Gwaltney and M. I. Ferguson, Intrinsic Hardware Evolution for the Design and Reconfiguration of Analog Speed Controllers for a DC Motor, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 81–90, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[13] A. H. Jackson, R. Canham, and A. M. Tyrrell, Robot Fault-Tolerance Using and Embryonic Array, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 91–100, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[14] J. F. Amaral, C. Santini, R. Tanscheit, M. Vellasco, M. Pacheco, and A. Mesquita, Evolvable Building Blocks for Analog Fuzzy Logic Controllers, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 101–110, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[15] E. Takahashi, M. Murakawa, Y. Kasai, and T. Higuchi, Power Dissipation Reductions with Genetic Algorithms, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 111–116, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[16] L. Tian and T. Arslan, An Evolutionary Power Management algorithm for SoC Based EHW Ststems, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 117–124, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[17] R. Thomson and T. Arslan, The Evolutionary Design and Synthesis of Non-Linear Digital VLSI Systems, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 125–134, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[18] L. Sekanina and R. Ruzicka, Easily Testable Image Operators: The Class of Circuits Where Evolution Beats Engineers, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 135–144, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[19] L. Zinchenko and S. Sorokin, Fitness Estimations for Evolutionary Antenna Design, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 155–166, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[20] M. Garvie and A. Thompson, Evolution of Combinationial and Sequential On-Line Self-Diagnosing Hardware, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 167–173, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[21] A. P. Shanthi and R.Parthasarathi, Exploring FPGA Structures for Evolving Fault Tolerant Hardware, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 174–181, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[22] R.Zebulum, A.Stoica, X.Guo, D.Keymeulen, V. Duong, and M.I.Ferguson, Experimental Results in Evolutionary Fault-Recovery for Field Programmble, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 182–188, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[23] D. Roggen, S. Hofmann, Y. Thoma, and D. Floreano, Hardware Spiking Neural Network with Run-time Reconfigurable Connectivity in and Autonomous Robot, in *2003*

NASA/DoD Conference on Evolvable Hardware, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 189–198, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[24] A. H. J. R. Canham and A. Tyrrell, Robot Error Detection Using an Artificial Immune System, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 199–207, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[25] S. Kamio, H. Liu, H. Mitsuhasi, and H. Iba, Researches on Ingeniously Behaving Agents, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 208–220, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[26] S. Harding and J. F. Miller, A Scalable Platform for Intrinsic Hardware and in materio Evolution, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 221–224, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[27] G. R. Kramer and J. Gallagher, Improvements to the *CGA Enabling Online Intrinsic Evolution in Compact EH Devices, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 225–234, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[28] A. Stauffer and M. Sipper, Data and Signals: A New Kind of Cellular Automation for Growing Systems, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 235–241, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[29] H. Sayama, Self-Protection Maintains Diversity of Artificial Self-Replicators Evolving in Cellular Automata, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 242–254, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[30] G. Tempesti, D. Mange, E. Petraglio, A. Stauffer, and Y. Thoma, Developmental Processes in silicon: An Engineering Perspective, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 255–264, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.

[31] J. Dinerstein, N. Dinerstein, and H. de Garis, Automatic Multi-Module Neural Network Evolution in an Artificial Brain, in *2003 NASA/DoD Conference on Evolvable Hardware*, edited by J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, pp. 273–276, Chicago, Illinois, 2003, NASA Ames Research Center, IEEE Computer Society.