Список литературы

- [Bennett III and Rieffel(2000)] F. H Bennett III and E. Rieffel. Design of Decentralized Controllers for Self-Reconfigurable Modular Robots using Genetic Programming. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 43– 52. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Bradley et al.(2000)Bradley, Ortega-Sanchez, and Tyrrell] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell. Embryonics + Immunotronics: A Bio-Inspired Approach to Fault Tolerance. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 205–224. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Coello et al.(2000)Coello, Aguirre, and Buckles] C. Coello, A. Aguirre, and B. Buckles. Evolutionary Multiobjective Design of Combinational Logic Circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 161–170. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [de Garis et al.(2000)de Garis, Buller, Dob, Honlet, Guttikonda, and Decesare] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare. Building Multimodule Systems with Unlimited Evolvable Capacities from Modules with Limited Evolvable Capacities (MECs). In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 225–234. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Flockton and Sheehan(2000)] S. Flockton and K. Sheehan. Behavior of a Building Block for Intrinsic Evolution of Analogue Signal Shaping and Filtering Circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 117–124. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Hollingworth et al.(2000)Hollingworth, Smith, and Tyrrell] G. Hollingworth, S. Smith, and A. Tyrrell. Safe Intrinsic Evolution of Virtex Devices. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 195–202. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Imamura et al.(2000)Imamura, Foster, and Krings] K. Imamura, J. Foster, and A. Krings. Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 75–80. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Jonathan et al.(2000) Jonathan, Zebulum, Pacheco, and Vellasco] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco. *Multiobjective Optimization Techniques: A Study of the Energy Minimization Method and Its Application to the Synthesis of Ota Amplifiers.* In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 133–140. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Kalganova(2000)] T. Kalganova. Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 65–74. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Korkin et al.(2000)Korkin, Fehr, and Jeffery] M. Korkin, G. Fehr, and G. Jeffery. Evolving Hardware on a Large Scale. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 173–182. Jet Propulsion Laboratory, California

- Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Koza et al.(2000)Koza, Yu, Keane, and Mydlowec] John R. Koza, Jessen Yu, Martin A. Keane, and William Mydlowec. Use of Conditional Developmental Operators and Free Variables in Automatically Synthesizing Generalized Circuits using Genetic Programming. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 5–16. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Lee et al.(2000)Lee, Hall, Perkowski, and Jun] C. Lee, D. Hall, M. Perkowski, and D. Jun. Self-Repairable EPLDs: Design, Self-Repair, and Evaluation Methodology. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 183–194. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Levi(2000)] D. Levi. HereBoy: A Fast Evolutionary Algorithm. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 17–24. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Levy et al.(2000)Levy, Lepri, Sanchez, Ritter, and Sipper] R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper. Slate of the Art: An Evolving FPGA-based Board for Handwritten-Digit Recognition. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 237–244. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Mange et al.(2000)Mange, Sipper, Stauffer, and Tempesti] D. Mange, M. Sipper, A. Stauffer, and G. Tempesti. Toward Self-Repairing and Self-Replicating Hardware: The Embryonics Approach. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 205–214. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Marston et al.(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka, and Higuchi]
 N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi.
 An Evolutionary Approach to GHz Digital Systems. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 125–131. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Masner et al.(2000)Masner, Cavalieri, Frenzel, and Foster] J. Masner, J. Cavalieri, J. Frenzel, and J. Foster. Size versus Robustness in Evolved Sorting Networks: Is Bigger Better? In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 81–87. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Milano and Koumoutsakos(2000)] M. Milano and P. Koumoutsakos. A Clustering Genetic Algorithm for Actuator Optimization in Flow Control. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 263–270. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Pollack and Lipson(2000)] J. Pollack and H. Lipson. The GOLEM Project: Evolving Hardware Bodies and Brains. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 37–42. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Seok et al.(2000)Seok, Lee, Zhang, Lee, and Sim] H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim. Genetic Programming of Process Decomposition Strategies for Evolvable Hardware. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable

- *Hardware*, pp. 25–34. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Stoica et al.(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel, and Duong] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong. Evolution of Analog Circuits on Field Programmable Transistor Arrays. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 99–108. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Thompson and Wasshuber(2000)] A. Thompson and C. Wasshuber. Evolutionary Design of Single Electron Systems. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 109–116. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Torresen(2000)] J. Torresen. Scalable Evolvable Hardware Applied to Road Image Recognition. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 245–252. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Tufte and Haddow(2000)] G. Tufte and P. Haddow. Evolving an Adaptive Digital Filter. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 143–150. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Vassilev and Miller(2000)] V. Vassilev and J. Miller. Scalability Problems of Digital Circuit Evolution: Evolvability and Efficient Designs. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 55–64. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Yasunaga et al.(2000) Yasunaga, Nakamura, Yoshihara, and Kim] M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim. Kernel-based Pattern Recognition Hardware: Its Design Methodology using Evolved Truth Tables. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 253–262. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.
- [Zebulum et al.(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco, and Szwarcman] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman. A Reconfigurable Platform for the Automatic Synthesis of Analog Circuits. In Jason Lohn, Adrian Stoica, and Didier Keymeulen (eds.) The Second NASA/DoD workshop on Evolvable Hardware, pp. 91–98. Jet Propulsion Laboratory, California Institute of Technology (IEEE Computer Society, Palo Alto, California, 2000). ISBN 0-7695-0762-X.