Multiplexers
Evolution and Multi-Objective Fitness
Analog and Digital CMOS Circuits
Preliminary Study of Populational Fault Tolerance
Amplifiers
"CoDi-1Bit"Model Evolved Neural Net Modules can do with Digital and Analog I/O
Temporal Logic Constraints
Applications
Self-Reconfigurable Infinitely Scalable Architecture
Implicit Self-Configuration
Algorithms
Partial Input/Output Sequences