Список литературы

- [Bennett III and Rieffel(2000)] F. H. Bennett III and E. Rieffel, "Design of decentralized controllers for self-reconfigurable modular robots using genetic programming," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 43-52.
- [Bradley et al.(2000)Bradley, Ortega-Sanchez, and Tyrrell] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell, "Embryonics + immunotronics: A bio-inspired approach to fault tolerance," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 205–224.
- [Coello et al.(2000)Coello, Aguirre, and Buckles] C. Coello, A. Aguirre, and B. Buckles, "Evolutionary multiobjective design of combinational logic circuits," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 161–170.
- [de Garis et al.(2000)de Garis, Buller, Dob, Honlet, Guttikonda, and Decesare] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare, "Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs)," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 225–234.
- [Flockton and Sheehan(2000)] S. Flockton and K. Sheehan, "Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 117–124.
- [Hollingworth et al.(2000)Hollingworth, Smith, and Tyrrell] G. Hollingworth, S. Smith, and A. Tyrrell, "Safe intrinsic evolution of virtex devices," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 195–202.
- [Imamura et al.(2000)Imamura, Foster, and Krings] K. Imamura, J. Foster, and A. Krings, "Bidirectional incremental evolution in extrinsic evolvable hardware," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 75–80.
- [Jonathan et al.(2000) Jonathan, Zebulum, Pacheco, and Vellasco] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco, "Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 133-140.
- [Kalganova(2000)] T. Kalganova, "Bidirectional incremental evolution in extrinsic evolvable hardware," in The Second NASA/DoD workshop on Evolvable Hardware, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 65-74.
- [Korkin et al.(2000)Korkin, Fehr, and Jeffery] M. Korkin, G. Fehr, and G. Jeffery, "Evolving hardware on a large scale," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 173–182.

- [Koza et al.(2000)Koza, Yu, Keane, and Mydlowec] J. R. Koza, J. Yu, M. A. Keane, and W. Mydlowec, "Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 5–16.
- [Lee et al.(2000)Lee, Hall, Perkowski, and Jun] C. Lee, D. Hall, M. Perkowski, and D. Jun, "Self-repairable eplds: Design, self-repair, and evaluation methodology," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 183–194.
- [Levi(2000)] D. Levi, "Hereboy: A fast evolutionary algorithm," in The Second NASA/DoD workshop on Evolvable Hardware, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 17-24.
- [Levy et al.(2000)Levy, Lepri, Sanchez, Ritter, and Sipper] R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper, "Slate of the art: An evolving fpga-based board for handwritten-digit recognition," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 237–244.
- [Mange et al.(2000)Mange, Sipper, Stauffer, and Tempesti] D. Mange, M. Sipper, A. Stauffer, and G. Tempesti, "Toward self-repairing and self-replicating hardware: The embryonics approach," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 205–214.
- [Marston et al.(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka, and Higuchi] N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi, "An evolutionary approach to ghz digital systems," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 125–131.
- [Masner et al.(2000)Masner, Cavalieri, Frenzel, and Foster] J. Masner, J. Cavalieri, J. Frenzel, and J. Foster, "Size versus robustness in evolved sorting networks: Is bigger better?" in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 81–87.
- [Milano and Koumoutsakos(2000)] M. Milano and P. Koumoutsakos, "A clustering genetic algorithm for actuator optimization in flow control," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 263–270.
- [Pollack and Lipson(2000)] J. Pollack and H. Lipson, "The golem project: Evolving hardware bodies and brains," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 37-42.
- [Seok et al.(2000)Seok, Lee, Zhang, Lee, and Sim] H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim, "Genetic programming of process decomposition strategies for evolvable hardware," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 25–34.

- [Stoica et al.(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel, and Duong] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong, "Evolution of analog circuits on field programmable transistor arrays," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 99–108.
- [Thompson and Wasshuber(2000)] A. Thompson and C. Wasshuber, "Evolutionary design of single electron systems," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 109–116.
- [Torresen(2000)] J. Torresen, "Scalable evolvable hardware applied to road image recognition," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 245–252.
- [Tufte and Haddow(2000)] G. Tufte and P. Haddow, "Evolving an adaptive digital filter," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 143-150.
- [Vassilev and Miller(2000)] V. Vassilev and J. Miller, "Scalability problems of digital circuit evolution: Evolvability and efficient designs," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 55-64.
- [Yasunaga et al.(2000) Yasunaga, Nakamura, Yoshihara, and Kim] M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim, "Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 253–262.
- [Zebulum et al.(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco, and Szwarcman] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman, "A reconfigurable platform for the automatic synthesis of analog circuits," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 91–98.