Список литературы

- [Bennett III and Rieffel(2000)] Bennett III, F.H. and Rieffel, E. (2000) 'Design of decentralized controllers for self-reconfigurable modular robots using genetic programming'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 43–52.
- [Bradley et al.(2000)Bradley, Ortega-Sanchez and Tyrrell] Bradley, D., Ortega-Sanchez, C. and Tyrrell, A. (2000) 'Embryonics + immunotronics: A bio-inspired approach to fault tolerance'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 205–224.
- [Coello et al.(2000)Coello, Aguirre and Buckles] Coello, C., Aguirre, A. and Buckles, B. (2000) 'Evolutionary multiobjective design of combinational logic circuits'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 161–170.
- [de Garis et al.(2000)de Garis, Buller, Dob, Honlet, Guttikonda and Decesare] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P. and Decesare, D. (2000) 'Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs)'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 225–234.
- [Flockton and Sheehan(2000)] Flockton, S. and Sheehan, K. (2000) 'Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 117–124.
- [Hollingworth et al.(2000)Hollingworth, Smith and Tyrrell] Hollingworth, G., Smith, S. and Tyrrell, A. (2000) 'Safe intrinsic evolution of virtex devices'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 195–202.
- [Imamura et al.(2000)Imamura, Foster and Krings] Imamura, K., Foster, J. and Krings, A. (2000) 'Bidirectional incremental evolution in extrinsic evolvable hardware'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 75–80.
- [Jonathan et al.(2000) Jonathan, Zebulum, Pacheco and Vellasco] Jonathan, M., Zebulum, R., Pacheco, M. and Vellasco, M. (2000) 'Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 133–140.
- [Kalganova(2000)] Kalganova, T. (2000) 'Bidirectional incremental evolution in extrinsic evolvable hardware'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 65–74.
- [Korkin et al.(2000)Korkin, Fehr and Jeffery] Korkin, M., Fehr, G. and Jeffery, G. (2000) 'Evolving hardware on a large scale'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 173–182.

- [Koza et al.(2000)Koza, Yu, Keane and Mydlowec] Koza, J.R., Yu, J., Keane, M.A. and Mydlowec, W. (2000) 'Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 5–16.
- [Lee et al.(2000)Lee, Hall, Perkowski and Jun] Lee, C., Hall, D., Perkowski, M. and Jun, D. (2000) 'Self-repairable eplds: Design, self-repair, and evaluation methodology'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 183–194.
- [Levi(2000)] Levi, D. (2000) 'Hereboy: A fast evolutionary algorithm'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 17–24.
- [Levy et al.(2000)Levy, Lepri, Sanchez, Ritter and Sipper] Levy, R., Lepri, S., Sanchez, E., Ritter, G. and Sipper, M. (2000) 'Slate of the art: An evolving fpga-based board for handwritten-digit recognition'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 237–244.
- [Mange et al.(2000)Mange, Sipper, Stauffer and Tempesti] Mange, D., Sipper, M., Stauffer, A. and Tempesti, G. (2000) 'Toward self-repairing and self-replicating hardware: The embryonics approach'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 205–214.
- [Marston et al.(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka and Higuchi]
 Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K. and Higuchi,
 T. (2000) 'An evolutionary approach to ghz digital systems'. In J. Lohn, A. Stoica and
 D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion
 Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society,
 pp. 125–131.
- [Masner et al.(2000)Masner, Cavalieri, Frenzel and Foster] Masner, J., Cavalieri, J., Frenzel, J. and Foster, J. (2000) 'Size versus robustness in evolved sorting networks: Is bigger better?' In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 81–87.
- [Milano and Koumoutsakos(2000)] Milano, M. and Koumoutsakos, P. (2000) 'A clustering genetic algorithm for actuator optimization in flow control'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 263–270.
- [Pollack and Lipson(2000)] Pollack, J. and Lipson, H. (2000) 'The golem project: Evolving hardware bodies and brains'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 37–42.
- [Seok et al.(2000)Seok, Lee, Zhang, Lee and Sim] Seok, H., Lee, K., Zhang, B., Lee, D. and Sim, K. (2000) 'Genetic programming of process decomposition strategies for evolvable hardware'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 25–34.
- [Stoica et al.(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel and Duong] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel,

- R. and Duong, V. (2000) 'Evolution of analog circuits on field programmable transistor arrays'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 99–108.
- [Thompson and Wasshuber (2000)] Thompson, A. and Wasshuber, C. (2000) 'Evolutionary design of single electron systems'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 109–116.
- [Torresen(2000)] Torresen, J. (2000) 'Scalable evolvable hardware applied to road image recognition'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 245–252.
- [Tufte and Haddow(2000)] Tufte, G. and Haddow, P. (2000) 'Evolving an adaptive digital filter'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) *The Second NASA/DoD workshop on Evolvable Hardware*. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 143–150.
- [Vassilev and Miller(2000)] Vassilev, V. and Miller, J. (2000) 'Scalability problems of digital circuit evolution: Evolvability and efficient designs'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 55–64.
- [Yasunaga et al.(2000) Yasunaga, Nakamura, Yoshihara and Kim] Yasunaga, M., Nakamura, T., Yoshihara, I. and Kim, J. (2000) 'Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 253–262.
- [Zebulum et al.(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco and Szwarcman] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M. and Szwarcman, M. (2000) 'A reconfigurable platform for the automatic synthesis of analog circuits'. In J. Lohn, A. Stoica and D. Keymeulen, (eds.) The Second NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society, pp. 91–98.