Список литературы

[1] Koza, J. R., Yu, J., Keane, M. A., and Mydlowec, W.: Use of Conditional Developmental Operators and Free Variables in Automatically Synthesizing Generalized Circuits using Genetic Programming. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 5–16. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[2] Levi, D.: HereBoy: A Fast Evolutionary Algorithm. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 17–24. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[3] Seok, H., Lee, K., Zhang, B., Lee, D., and Sim, K.: Genetic Programming of Process Decomposition Strategies for Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 25–34. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[4] Pollack, J. and Lipson, H.: The GOLEM Project: Evolving Hardware Bodies and Brains. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 37–42. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[5] Bennett III, F. H. and Rieffel, E.: Design of Decentralized Controllers for Self-Reconfigurable Modular Robots using Genetic Programming. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 43–52. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[6] Vassilev, V. and Miller, J.: Scalability Problems of Digital Circuit Evolution: Evolvability and Efficient Designs. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 55–64. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[7] Kalganova, T.: Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 65–74. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[8] Imamura, K., Foster, J., and Krings, A.: Bidirectional Incremental Evolution in Extrinsic Evolvable Hardware. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 75–80. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[9] Masner, J., Cavalieri, J., Frenzel, J., and Foster, J.: Size versus Robustness in Evolved Sorting Networks: Is Bigger Better? In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 81–87. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[10] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M., and Szwarcman, M.: A Reconfigurable Platform for the Automatic Synthesis of Analog Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 91–98. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[11] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R., and Duong, V.: Evolution of Analog Circuits on Field Programmable Transistor Arrays. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable*

Hardware, 99–108. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[12] Thompson, A. and Wasshuber, C.: Evolutionary Design of Single Electron Systems. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 109–116. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[13] Flockton, S. and Sheehan, K.: Behavior of a Building Block for Intrinsic Evolution of Analogue Signal Shaping and Filtering Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 117–124. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[14] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K., and Higuchi, T.: An Evolutionary Approach to GHz Digital Systems. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 125–131. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[15] Jonathan, M., Zebulum, R., Pacheco, M., and Vellasco, M.: Multiobjective Optimization Techniques: A Study of the Energy Minimization Method and Its Application to the Synthesis of Ota Amplifiers. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 133–140. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[16] Tufte, G. and Haddow, P.: Evolving an Adaptive Digital Filter. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 143–150. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[17] Coello, C., Aguirre, A., and Buckles, B.: Evolutionary Multiobjective Design of Combinational Logic Circuits. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 161–170. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[18] Korkin, M., Fehr, G., and Jeffery, G.: Evolving Hardware on a Large Scale. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 173–182. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[19] Lee, C., Hall, D., Perkowski, M., and Jun, D.: Self-Repairable EPLDs: Design, Self-Repair, and Evaluation Methodology. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 183–194. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[20] Hollingworth, G., Smith, S., and Tyrrell, A.: Safe Intrinsic Evolution of Virtex Devices. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 195–202. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[21] Mange, D., Sipper, M., Stauffer, A., and Tempesti, G.: Toward Self-Repairing and Self-Replicating Hardware: The Embryonics Approach. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 205–214. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[22] Bradley, D., Ortega-Sanchez, C., and Tyrrell, A.: Embryonics + Immunotronics: A Bio-Inspired Approach to Fault Tolerance. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second*

NASA/DoD workshop on Evolvable Hardware, 205–224. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[23] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P., and Decesare, D.: Building Multimodule Systems with Unlimited Evolvable Capacities from Modules with Limited Evolvable Capacities (MECs). In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 225–234. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[24] Levy, R., Lepri, S., Sanchez, E., Ritter, G., and Sipper, M.: Slate of the Art: An Evolving FPGA-based Board for Handwritten-Digit Recognition. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 237–244. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[25] Torresen, J.: Scalable Evolvable Hardware Applied to Road Image Recognition. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 245–252. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[26] Yasunaga, M., Nakamura, T., Yoshihara, I., and Kim, J.: Kernel-based Pattern Recognition Hardware: Its Design Methodology using Evolved Truth Tables. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 253–262. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X

[27] Milano, M. and Koumoutsakos, P.: A Clustering Genetic Algorithm for Actuator Optimization in Flow Control. In Lohn, J., Stoica, A., and Keymeulen, D., eds., *The Second NASA/DoD workshop on Evolvable Hardware*, 263–270. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Palo Alto, California (2000). ISBN 0-7695-0762-X