

Список литературы

- [Abramovici et al., 2001] Abramovici, M., Emmert, J. M., & Stroud, C. E. (2001). Roving stars: An integrated approach to on-line testing, diagnosis, and fault tolerance for fpgas in adaptive computing systems. *The Third NASA/DoD workshop on Evolvable Hardware*, 73–92
- [Bradley & Tyrell, 2001] Bradley, D. W. & Tyrell, A. M. (2001). The architecture for a hardware immune system. *The Third NASA/DoD workshop on Evolvable Hardware*, 193–200
- [Castillo et al., 2001] Castillo, O., Montiel, O., Sepulveda, R., & Melin, P. (2001). Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter. *The Third NASA/DoD workshop on Evolvable Hardware*, 146–153
- [Darren et al., 2001] Darren, A. G., Conde, R., Chern, B., Luers, P., Jurczyk, S., & Mills, C. (2001). Adaptive instrument module: Space instrument controller "brain" through programmable logic devices. *The Third NASA/DoD workshop on Evolvable Hardware*, 256–260
- [de Garis et al., 2001] de Garis, H., de Penning, L., Bullner, A., & Decesare, D. (2001). Early experiments on the cam-brain machine (cbm). *The Third NASA/DoD workshop on Evolvable Hardware*, 211–219
- [Dolin et al., 2001] Dolin, B., Bennett III, F. H., & Rieffel, E. G. (2001). Methods for evolving robust distributed robot control software: coevolutionary and single population techniques. *The Third NASA/DoD workshop on Evolvable Hardware*, 21–29
- [Edwards & Kim, 2001] Edwards, R. T. & Kim, C. J. (2001). Breaking the resistivity barrier. *The Third NASA/DoD workshop on Evolvable Hardware*, 167–171
- [Gallagher, 2001] Gallagher, J. C. (2001). A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations. *The Third NASA/DoD workshop on Evolvable Hardware*, 48–55
- [Graham & Arslan, 2001] Graham, R. I. & Arslan, T. (2001). Rule evolution in order based diagnostic systems. *The Third NASA/DoD workshop on Evolvable Hardware*, 280–286
- [Haddow & Tufte, 2001] Haddow, P. C. & Tufte, G. (2001). Bridging the genotype-phenotype mapping for digital fpgas. *The Third NASA/DoD workshop on Evolvable Hardware*, 109–115
- [Hernandez-Aguirre et al., 2001] Hernandez-Aguirre, A., Buckles, B. P., & Coello, C. A. C. (2001). On learning kdnf boolean formulas. *The Third NASA/DoD workshop on Evolvable Hardware*, 240–246
- [Hounsell & Arslan, 2001a] Hounsell, B. I. & Arslan, T. (2001a). Evolutionary design and adaption of digital filters within an embedded fault. *The Third NASA/DoD workshop on Evolvable Hardware*, 127–135
- [Hounsell & Arslan, 2001b] Hounsell, B. I. & Arslan, T. (2001b). Evolutionary design and adaption of digital filters within an embedded fault. *The Third NASA/DoD workshop on Evolvable Hardware*, 127–135
- [Jackson & Tyrrell, 2001] Jackson, A. H. & Tyrrell, A. M. (2001). Asynchronous embryonics. *The Third NASA/DoD workshop on Evolvable Hardware*, 201–210
- [Kazadi et al., 2001] Kazadi, S., Qi, Y., Park, I., Huang, N., Hwu, P., Kwan, B., Lue, W., & Li, H. (2001). Insufficiency of piecewise evolution. *The Third NASA/DoD workshop on Evolvable Hardware*, 223–231
- [Langeheine et al., 2001] Langeheine, J., Becker, J., Foilling, S., Meire, K., & Schemmel, J. (2001). A cmos fpga chip for intrinsic hardware evolution of analog electronic circuits. *The Third NASA/DoD workshop on Evolvable Hardware*, 172–175
- [Linden, 2001] Linden, D. S. (2001). A system for evolving antennas in-situ. *The Third NASA/DoD workshop on Evolvable Hardware*, 249–255

- [Lockwood, 2001] Lockwood, J. W. (2001). Evolvable internet hardware platforms. *The Third NASA/DoD workshop on Evolvable Hardware*, 271–279
- [Miller & Hartmann, 2001] Miller, J. F. & Hartmann, M. (2001). Evolving messy gates for fault tolerance: Some preliminary findings. *The Third NASA/DoD workshop on Evolvable Hardware*, 116–123
- [Moreno Arostegui et al., 2001a] Moreno Arostegui, J. M., Sanchez, E., & Cabestany, J. (2001a). An in-system routing strategy for evolvable hardware programmable platforms. *The Third NASA/DoD workshop on Evolvable Hardware*, 157–166
- [Moreno Arostegui et al., 2001b] Moreno Arostegui, J. M., Sanchez, E., & Cabestany, J. (2001b). An in-system routing strategy for evolvable hardware programmable platforms. *The Third NASA/DoD workshop on Evolvable Hardware*, 157–166
- [Pfaffmann & Zauner, 2001] Pfaffmann, J. O. & Zauner, K. P. (2001). Scouting context-sensitive components. *The Third NASA/DoD workshop on Evolvable Hardware*, 14–20
- [Porter et al., 2001] Porter, R., Gokhale, M., Harvey, N., Perkins, S., & Young, C. (2001). Evolving network architectures with custom computers for multi-spectral feature identification. *The Third NASA/DoD workshop on Evolvable Hardware*, 261–270
- [Ramsden, 2001] Ramsden, E. (2001). The isppac family of reconfigurable analog circuits. *The Third NASA/DoD workshop on Evolvable Hardware*, 176–181
- [Saleh et al., 2001] Saleh, J. H., Hastings, D. E., & Newman, D. J. (2001). Extracting the essence of flexibility in system design. *The Third NASA/DoD workshop on Evolvable Hardware*, 59–72
- [Santini et al., 2001] Santini, C. C., Zebulum, R., Pacheco, M. A. C., Vellasco, M. M. R., & Szwarcman, M. H. (2001). Pama-programmable analog multiplexer array. *The Third NASA/DoD workshop on Evolvable Hardware*, 36–43
- [Schiner et al., 2001] Schiner, T., Yao, X., & Liu, P. (2001). Digital filter design using multiple pareto fronts. *The Third NASA/DoD workshop on Evolvable Hardware*, 136–145
- [Segovia-Juarez & Colombano, 2001] Segovia-Juarez, J. L. & Colombano, S. (2001). Mutation buffering capabilities of the hypernetwork model. *The Third NASA/DoD workshop on Evolvable Hardware*, 7–13
- [Sinohara et al., 2001] Sinohara, H. T., Pacheco, M. A. C., & Vellasco, M. M. R. (2001). Repair of analog circuits: Extrinsic and intrinsic evolutionary techniques. *The Third NASA/DoD workshop on Evolvable Hardware*, 44–47
- [Stauffer et al., 2001] Stauffer, A., Mange, D., Tempesti, G., & Teuscher, C. (2001). Biowatch: A giant electronic bio-inspired watch. *The Third NASA/DoD workshop on Evolvable Hardware*, 185–192
- [Stoica et al., 2001] Stoica, A., Zebulum, R., & Keymeulen, D. (2001). Progress and challenges in building evolvable devices. *The Third NASA/DoD workshop on Evolvable Hardware*, 33–35
- [Tyrrell et al., 2001] Tyrrell, A. M., Hollingworth, G., & Smith, S. L. (2001). Evolutionary strategies and intrinsic fault tolerance. *The Third NASA/DoD workshop on Evolvable Hardware*, 98–106