Список литературы

[Abramovici et al. 2001]

An integrated approach to on-line testing, diagnosis, and fault tolerance for fpgas in adaptive computing systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 73–92, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

M. Abramovici, J. M. Emmert, and C. E. Stroud, Roving stars:

[Bradley and Tyrell 2001]

D. W. Bradley and A. M. Tyrell, The architecture for a hardware immune system, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 193–200, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Castillo et al. 2001]

O. Castillo, O. Montiel, R. Sepulveda, and P. Melin, Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 146–153, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Darren et al. 2001]

A. G. Darren, R. Conde, B. Chern, P. Luers, S. Jurczyk, and C. Mills, Adaptive instrument module: Space instrument controller "brain"through progammable logic devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 256–260, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis et al. 2001]

H. de Garis, L. de Penning, A. Bullner, and D. Decesare, Early experiments on the cam-brain machine (cbm), in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 211–219, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Dolin et al. 2001]

B. Dolin, F. H. Bennett III, and E. G. Rieffel, Methods for evolving robust distributed robot control software: coevolutionary and single population techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 21–29, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Edwards and Kim 2001]

R. T. Edwards and C. J. Kim, Breaking the resistivity barrier, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 167–171, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Gallagher 2001]

[Graham and Arslan 2001]

[Haddow and Tufte 2001]

[Hernandez-Aguirre et al. 2001]

[Hounsell and Arslan 2001a]

[Hounsell and Arslan 2001b]

[Jackson and Tyrrell 2001]

[Kazadi et al. 2001]

J. C. Gallagher, A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 48–55, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

R. I. Graham and T. Arslan, Rule evolution in order based diagnostic systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 280–286, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

P. C. Haddow and G. Tufte, Bridging the genotype-phenotype mapping for digital fpgas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 109–115, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

A. Hernandez-Aguirre, B. P. Buckles, and C. A. C. Coello, On learning kdnf boolean formulas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 240–246, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

B. I. Hounsell and T. Arslan, Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 127–135, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

B. I. Hounsell and T. Arslan, Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 127–135, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

A. H. Jackson and A. M. Tyrrell, Asynchronous embryonics, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 201–210, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

S. Kazadi, Y. Qi, I. Park, N. Huang, P. Hwu, B. Kwan, W. Lue, and H. Li, Insufficiency of piecewise evolution, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 223–231, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Langeheine et al. 2001]

[Linden 2001]

[Lockwood 2001]

[Miller and Hartmann 2001]

[Moreno Arostegui et al. 2001a]

[Moreno Arostegui et al. 2001b]

[Pfaffmann and Zauner 2001]

[Porter et al. 2001]

- J. Langeheine, J. Becker, S. Foilling, K. Meire, and J. Schemmel, A cmos fpta chip for intrinsic hardware evolution of analong electronic circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 172–175, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- D. S. Linden, A system for evolving antennas in-situ, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 249–255, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- J. W. Lockwood, Evovable internet hardware platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 271–279, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- J. F. Miller and M. Hartmann, Evolving messy gates for fault tolerance: Some preliminary findings, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 116–123, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany, An insystem routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 157–166, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany, An insystem routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 157–166, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- J. O. Pfaffmann and K. P. Zauner, Scouting context-sensitive components, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 14–20, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- R. Porter, M. Gokhale, N. Harvey, S. Perkins, and C. Young, Evolving network architectures with custom computers for multi-spectral feature identification, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 261–270, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Ramsden 2001]

The isppac family of reconfigurable analog E. Ramsden. circuits, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 176–181, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Saleh et al. 2001]

J. H. Saleh, D. E. Hastings, and D. J. Newman, Extracting the essence of flexibility in system design, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 59–72, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Santini et al. 2001]

C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco, and M. H. Szwarcman, Pama-programmable analog multiplexter array, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 36-43, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Schiner et al. 2001]

T. Schiner, X. Yao, and P. Liu, Digital filter design using multiple pareto fronts, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 136–145, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Segovia-Juarez and Colombano 2001] J. L. Segovia-Juarez and S. Colombano, Mutation buffering capabilities of the hypernetwork model, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 7–13, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Sinohara et al. 2001]

H. T. Sinohara, M. A. C. Pacheco, and M. M. R. Vellasco, Repair of analog circuits: Extrinsic and instrinsic evolutionary techniques, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 44-47, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stauffer et al. 2001]

A. Stauffer, D. Mange, G. Tempesti, and C. Teuscher, Biowatch: A giant electronic bio-inspired watch, Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 185– 192, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica et al. 2001]

A. Stoica, R. Zebulum, and D. Keymeulen, Progress and challenges in building evolvable devices, in The Third NASA/DoD workshop on Evolvable Hardware, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 33–35, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tyrrell et al. 2001]

A. M. Tyrrell, G. Hollingworth, and S. L. Smith, Evolutionary strategies and intrinsic fault tolerance, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pp. 98–106, Long Beach, California, 12-14 July 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.