

Список литературы

- [BOST00] D. Bradley, C. Ortega-Sanchez, and A. Tyrrell, “Embryonics + immunotronics: A bio-inspired approach to fault tolerance,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 205–224.
- [BR00] F. H. Bennett III and E. Rieffel, “Design of decentralized controllers for self-reconfigurable modular robots using genetic programming,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 43–52.
- [CAB00] C. Coello, A. Aguirre, and B. Buckles, “Evolutionary multiobjective design of combinational logic circuits,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 161–170.
- [dBD⁺00] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare, “Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs),” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 225–234.
- [FS00] S. Flockton and K. Sheehan, “Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 117–124.
- [HST00] G. Hollingworth, S. Smith, and A. Tyrrell, “Safe intrinsic evolution of virtex devices,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 195–202.
- [IFK00] K. Imamura, J. Foster, and A. Krings, “Bidirectional incremental evolution in extrinsic evolvable hardware,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 75–80.
- [JZPV00] M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco, “Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 133–140.
- [Kal00] T. Kalganova, “Bidirectional incremental evolution in extrinsic evolvable hardware,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 65–74.
- [KFJ00] M. Korkin, G. Fehr, and G. Jeffery, “Evolving hardware on a large scale,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 173–182.

- [KYKM00] J. R. Koza, J. Yu, M. A. Keane, and W. Mydlowec, "Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 5–16.
- [Lev00] D. Levi, "Hereboy: A fast evolutionary algorithm," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 17–24.
- [LHPJ00] C. Lee, D. Hall, M. Perkowski, and D. Jun, "Self-repairable eplds: Design, self-repair, and evaluation methodology," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 183–194.
- [LLS⁺00] R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper, "Slate of the art: An evolving fpga-based board for handwritten-digit recognition," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 237–244.
- [MCFF00] J. Masner, J. Cavalieri, J. Frenzel, and J. Foster, "Size versus robustness in evolved sorting networks: Is bigger better?" in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 81–87.
- [MK00] M. Milano and P. Koumoutsakos, "A clustering genetic algorithm for actuator optimization in flow control," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 263–270.
- [MSST00] D. Mange, M. Sipper, A. Stauffer, and G. Tempesti, "Toward self-repairing and self-replicating hardware: The embryonics approach," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 205–214.
- [MTM⁺00] N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi, "An evolutionary approach to ghz digital systems," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 125–131.
- [PL00] J. Pollack and H. Lipson, "The golem project: Evolving hardware bodies and brains," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 37–42.
- [SKZ⁺00] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong, "Evolution of analog circuits on field programmable transistor arrays," in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 99–108.
- [SLZ⁺00] H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim, "Genetic programming of process decomposition strategies for evolvable hardware," in *The Second NASA/DoD workshop*

- on *Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 25–34.
- [TH00] G. Tufte and P. Haddow, “Evolving an adaptive digital filter,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 143–150.
- [Tor00] J. Torresen, “Scalable evolvable hardware applied to road image recognition,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 245–252.
- [TW00] A. Thompson and C. Wasshuber, “Evolutionary design of single electron systems,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 109–116.
- [VM00] V. Vassilev and J. Miller, “Scalability problems of digital circuit evolution: Evolvability and efficient designs,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 55–64.
- [YNYK00] M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim, “Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 253–262.
- [ZSV⁺00] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman, “A reconfigurable platform for the automatic synthesis of analog circuits,” in *The Second NASA/DoD workshop on Evolvable Hardware*, J. Lohn, A. Stoica, and D. Keymeulen, Eds., Jet Propulsion Laboratory, California Institute of Technology. Palo Alto, California: IEEE Computer Society, 13-15 July 2000, pp. 91–98.