Список литературы

[1] Koza JR, Yu J, Keane MA, Mydlowec W. 2000 Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 5–16. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[2] Levi D. 2000 Hereboy: A fast evolutionary algorithm. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 17–24. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[3] Seok H, Lee K, Zhang B, Lee D, Sim K. 2000 Genetic programming of process decomposition strategies for evolvable hardware. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 25–34. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[4] Pollack J, Lipson H. 2000 The golem project: Evolving hardware bodies and brains. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 37–42. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[5] Bennett III FH, Rieffel E. 2000 Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 43–52. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[6] Vassilev V, Miller J. 2000 Scalability problems of digital circuit evolution: Evolvability and efficient designs. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 55–64. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[7] Kalganova T. 2000 Bidirectional incremental evolution in extrinsic evolvable hardware. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 65–74. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[8] Imamura K, Foster J, Krings A. 2000 Bidirectional incremental evolution in extrinsic evolvable hardware. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 75–80. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[9] Masner J, Cavalieri J, Frenzel J, Foster J. 2000 Size versus robustness in evolved sorting networks: Is bigger better? In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 81–87. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[10] Zebulum R, Sinohara H, Vellasco M, Santini C, Pacheco M, Szwarcman M. 2000 A reconfigurable platform for the automatic synthesis of analog circuits. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 91–98. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[11] Stoica A, Keymeulen D, Zebulum R, Thakoor A, Daud T, Klimeck G, Jin Y, Tawel R, Duong V. 2000 Evolution of analog circuits on field programmable transistor arrays. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 99–108. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[12] Thompson A, Wasshuber C. 2000 Evolutionary design of single electron systems. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 109–116. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[13] Flockton S, Sheehan K. 2000 Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 117–124. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[14] Marston N, Takahashi E, Murakawa M, Kasai Y, Adachi T, Takasuka K, Higuchi T. 2000 An evolutionary approach to ghz digital systems. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 125–131. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[15] Jonathan M, Zebulum R, Pacheco M, Vellasco M. 2000 Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 133–140. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[16] Tufte G, Haddow P. 2000 Evolving an adaptive digital filter. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 143–150. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[17] Coello C, Aguirre A, Buckles B. 2000 Evolutionary multiobjective design of combinational logic circuits. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 161–170. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[18] Korkin M, Fehr G, Jeffery G. 2000 Evolving hardware on a large scale. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 173–182. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[19] Lee C, Hall D, Perkowski M, Jun D. 2000 Self-repairable eplds: Design, self-repair, and evaluation methodology. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 183–194. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[20] Hollingworth G, Smith S, Tyrrell A. 2000 Safe intrinsic evolution of virtex devices. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 195–202. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[21] Mange D, Sipper M, Stauffer A, Tempesti G. 2000 Toward self-repairing and self-replicating hardware: The embryonics approach. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 205–214. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[22] Bradley D, Ortega-Sanchez C, Tyrrell A. 2000 Embryonics + immunotronics: A bio-inspired approach to fault tolerance. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 205–224. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[23] de Garis H, Buller A, Dob T, Honlet J, Guttikonda P, Decesare D. 2000 Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 225–234. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[24] Levy R, Lepri S, Sanchez E, Ritter G, Sipper M. 2000 Slate of the art: An evolving fpga-based board for handwritten-digit recognition. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 237–244. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[25] Torresen J. 2000 Scalable evolvable hardware applied to road image recognition. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 245–252. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[26] Yasunaga M, Nakamura T, Yoshihara I, Kim J. 2000 Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 253–262. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[27] Milano M, Koumoutsakos P. 2000 A clustering genetic algorithm for actuator optimization in flow control. In: Lohn J, Stoica A, Keymeulen D (eds.), *The Second NASA/DoD workshop on Evolvable Hardware*, pp. 263–270. Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.