

Список литературы

- [1] C. Coello, E. Alba, G. Luque, and A. Aguirre, “Comparing Different Serial and Parallel Heuristics to Design Combinatorial Logic Circuits,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 3–12, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [2] A. Aguirre and C. Coello, “Fitness Landscape and Evolutionary Boolean Synthesis using Information Theory Concepts,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 13–20, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [3] S. J. Louis, “Learning for Evolutionary Design,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 17–21, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [4] A. Stoica, R. Zebulum, X. Guo, D. Keymeulen, V. Duong, and M. I. Ferguson, “Silicon Validation of Evolution-Designed Circuits,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 21–25, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [5] K. Vinger and J. Torresen, “Implementing Evolution of FIR-Filters Efficiently in an FPGA,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 26–29, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [6] J. Koza, M. Keane, and M. Streeter, “the Importance of Reuse and Development in Evolvable Hardware,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 33–42, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [7] J. Gallagher, “The Once and Future Analog Alternative: Evolvable Hardware and Analog Computation,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 43–49, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [8] J. Botelho, B. Leonardo, P. Vieira, and A. Mesquita, “An Experiment on Nonlinear synthesis Using Evolutionary Techniques Based only on CMOS Transistors,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 50–58, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [9] G. Greenwood, E. Ramsden, and S. Ahmed, “An Empirical Comparison of Evolutionary Algorithms for Evolvable Hardware with Minimum Time-To-Reconfigure requirements,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 59–66, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [10] V. Aggarwal, “Evolving Sinusoidal Oscillators Using Genetic Algorithms,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 67–76, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).

- [11] J. Plante, H. Shaw, L. Mickens, and C. Johnson-Be, "Overview of Field Programmable Analog Arrays as Enabling Technology for Evolvable Hardware for High Reliability Systems," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 77–78, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [12] D. Gwaltney and M. I. Ferguson, "Intrinsic Hardware Evolution for the Design and Reconfiguration of Analog Speed Controllers for a DC Motor," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 81–90, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [13] A. H. Jackson, R. Canham, and A. M. Tyrrell, "Robot Fault-Tolerance Using and Embryonic Array," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 91–100, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [14] J. F. Amaral, C. Santini, R. Tanscheit, M. Vellasco, M. Pacheco, and A. Mesquita, "Evolvable Building Blocks for Analog Fuzzy Logic Controllers," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 101–110, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [15] E. Takahashi, M. Murakawa, Y. Kasai, and T. Higuchi, "Power Dissipation Reductions with Genetic Algorithms," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 111–116, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [16] L. Tian and T. Arslan, "An Evolutionary Power Management algorithm for SoC Based EHW Ststems," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 117–124, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [17] R. Thomson and T. Arslan, "The Evolutionary Design and Synthesis of Non-Linear Digital VLSI Systems," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 125–134, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [18] L. Sekanina and R. Ruzicka, "Easily Testable Image Operators: The Class of Circuits Where Evolution Beats Engineers," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 135–144, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [19] L. Zinchenko and S. Sorokin, "Fitness Estimations for Evolutionary Antenna Design," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 155–166, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [20] M. Garvie and A. Thompson, "Evolution of Combinational and Sequential On-Line Self-Diagnosing Hardware," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 167–173, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).

- [21] A. P. Shanthi and R. Parthasarathi, "Exploring FPGA Structures for Evolving Fault Tolerant Hardware," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 174–181, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [22] R. Zebulum, A. Stoica, X. Guo, D. Keymeulen, V. Duong, and M. I. Ferguson, "Experimental Results in Evolutionary Fault-Recovery for Field Programmable," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 182–188, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [23] D. Roggen, S. Hofmann, Y. Thoma, and D. Floreano, "Hardware Spiking Neural Network with Run-time Reconfigurable Connectivity in an Autonomous Robot," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 189–198, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [24] A. H. J. R. Canham and A. Tyrrell, "Robot Error Detection Using an Artificial Immune System," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 199–207, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [25] S. Kamio, H. Liu, H. Mitsuhashi, and H. Iba, "Researches on Ingeniously Behaving Agents," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 208–220, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. <http://ieeexplore.ieee.org/iel5/8637/27376/01217668.pdf?tp=&arnumber=1217668&isnumber=27376>.
- [26] S. Harding and J. F. Miller, "A Scalable Platform for Intrinsic Hardware and in materio Evolution," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 221–224, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [27] G. R. Kramer and J. Gallagher, "Improvements to the *CGA Enabling Online Intrinsic Evolution in Compact EH Devices," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 225–234, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [28] A. Stauffer and M. Sipper, "Data and Signals: A New Kind of Cellular Automation for Growing Systems," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 235–241, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [29] H. Sayama, "Self-Protection Maintains Diversity of Artificial Self-Replicators Evolving in Cellular Automata," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 242–254, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).
- [30] G. Tempesti, D. Mange, E. Petraglio, A. Stauffer, and Y. Thoma, "Developmental Processes in silicon: An Engineering Perspective," in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 255–264, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9–11 July, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).

- [31] J. Dinerstein, N. Dinerstein, and H. de Garis, “Automatic Multi-Module Neural Network Evolution in an Artificial Brain,” in *2003 NASA/DoD Conference on Evolvable Hardware*, J. Lohn, R. Zebulum, J. Steincamp, D. Keymeulen, A. Stoica, and M. I. Ferguson, eds., pp. 273–276, NASA Ames Research Center. IEEE Computer Society, Chicago, Illinois, 9-11 july, 2003. [EHWhttp://ehw.jpl.nasa.gov](http://ehw.jpl.nasa.gov).