Список литературы

- [Bennett III & Rieffel, 2000] Bennett III, F. H. & Rieffel, E. (2000). Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. The Second NASA/DoD workshop on Evolvable Hardware, 43–52
- [Bradley et al., 2000] Bradley, D., Ortega-Sanchez, C., & Tyrrell, A. (2000). Embryonics + immunotronics: A bio-inspired approach to fault tolerance. The Second NASA/DoD workshop on Evolvable Hardware, 205–224
- [Coello et al., 2000] Coello, C., Aguirre, A., & Buckles, B. (2000). Evolutionary multiobjective design of combinational logic circuits. The Second NASA/DoD workshop on Evolvable Hardware, 161–170
- [de Garis et al., 2000] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P., & Decesare, D. (2000). Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). The Second NASA/DoD workshop on Evolvable Hardware, 225–234
- [Flockton & Sheehan, 2000] Flockton, S. & Sheehan, K. (2000). Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. The Second NASA/DoD workshop on Evolvable Hardware, 117–124
- [Hollingworth et al., 2000] Hollingworth, G., Smith, S., & Tyrrell, A. (2000). Safe intrinsic evolution of virtex devices. The Second NASA/DoD workshop on Evolvable Hardware, 195–202
- [Imamura et al., 2000] Imamura, K., Foster, J., & Krings, A. (2000). Bidirectional incremental evolution in extrinsic evolvable hardware. The Second NASA/DoD workshop on Evolvable Hardware, 75–80
- [Jonathan et al., 2000] Jonathan, M., Zebulum, R., Pacheco, M., & Vellasco, M. (2000). Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. The Second NASA/DoD workshop on Evolvable Hardware, 133–140
- [Kalganova, 2000] Kalganova, T. (2000). Bidirectional incremental evolution in extrinsic evolvable hardware. The Second NASA/DoD workshop on Evolvable Hardware, 65–74
- [Korkin et al., 2000] Korkin, M., Fehr, G., & Jeffery, G. (2000). Evolving hardware on a large scale. The Second NASA/DoD workshop on Evolvable Hardware, 173–182
- [Koza et al., 2000] Koza, J. R., Yu, J., Keane, M. A., & Mydlowec, W. (2000). Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. The Second NASA/DoD workshop on Evolvable Hardware, 5–16
- [Lee et al., 2000] Lee, C., Hall, D., Perkowski, M., & Jun, D. (2000). Self-repairable eplds: Design, self-repair, and evaluation methodology. *The Second NASA/DoD workshop on Evolvable Hardware*, 183–194
- [Levi, 2000] Levi, D. (2000). Hereboy: A fast evolutionary algorithm. The Second NASA/DoD workshop on Evolvable Hardware, 17–24
- [Levy et al., 2000] Levy, R., Lepri, S., Sanchez, E., Ritter, G., & Sipper, M. (2000). Slate of the art: An evolving fpga-based board for handwritten-digit recognition. The Second NASA/DoD workshop on Evolvable Hardware, 237–244
- [Mange et al., 2000] Mange, D., Sipper, M., Stauffer, A., & Tempesti, G. (2000). Toward self-repairing and self-replicating hardware: The embryonics approach. The Second NASA/DoD workshop on Evolvable Hardware, 205–214
- [Marston et al., 2000] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K., & Higuchi, T. (2000). An evolutionary approach to ghz digital systems. *The Second NASA/DoD workshop on Evolvable Hardware*, 125–131

- [Masner et al., 2000] Masner, J., Cavalieri, J., Frenzel, J., & Foster, J. (2000). Size versus robustness in evolved sorting networks: Is bigger better? The Second NASA/DoD workshop on Evolvable Hardware, 81–87
- [Milano & Koumoutsakos, 2000] Milano, M. & Koumoutsakos, P. (2000). A clustering genetic algorithm for actuator optimization in flow control. The Second NASA/DoD workshop on Evolvable Hardware, 263–270
- [Pollack & Lipson, 2000] Pollack, J. & Lipson, H. (2000). The golem project: Evolving hardware bodies and brains. The Second NASA/DoD workshop on Evolvable Hardware, 37–42
- [Seok et al., 2000] Seok, H., Lee, K., Zhang, B., Lee, D., & Sim, K. (2000). Genetic programming of process decomposition strategies for evolvable hardware. The Second NASA/DoD workshop on Evolvable Hardware, 25–34
- [Stoica et al., 2000] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R., & Duong, V. (2000). Evolution of analog circuits on field programmable transistor arrays. The Second NASA/DoD workshop on Evolvable Hardware, 99–108
- [Thompson & Wasshuber, 2000] Thompson, A. & Wasshuber, C. (2000). Evolutionary design of single electron systems. The Second NASA/DoD workshop on Evolvable Hardware, 109–116
- [Torresen, 2000] Torresen, J. (2000). Scalable evolvable hardware applied to road image recognition. The Second NASA/DoD workshop on Evolvable Hardware, 245–252
- [Tufte & Haddow, 2000] Tufte, G. & Haddow, P. (2000). Evolving an adaptive digital filter. The Second NASA/DoD workshop on Evolvable Hardware, 143–150
- [Vassilev & Miller, 2000] Vassilev, V. & Miller, J. (2000). Scalability problems of digital circuit evolution: Evolvability and efficient designs. The Second NASA/DoD workshop on Evolvable Hardware, 55–64
- [Yasunaga et al., 2000] Yasunaga, M., Nakamura, T., Yoshihara, I., & Kim, J. (2000). Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. *The Second NASA/DoD workshop on Evolvable Hardware*, 253–262
- [Zebulum et al., 2000] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M., & Szwarcman, M. (2000). A reconfigurable platform for the automatic synthesis of analog circuits. The Second NASA/DoD workshop on Evolvable Hardware, 91–98