Список литературы

[Bennett III and Rieffel 2000]

F. H. Bennett III and E. Rieffel, Design of decentralized controllers for self-reconfigurable modular robots using genetic programming, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 43–52, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Bradley et al. 2000]

D. Bradley, C. Ortega-Sanchez, and A. Tyrrell, Embryonics + immunotronics: A bio-inspired approach to fault tolerance, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 205–224, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Coello et al. 2000]

C. Coello, A. Aguirre, and B. Buckles, Evolutionary multiobjective design of combinational logic circuits, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 161–170, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[de Garis et al. 2000]

H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, and D. Decesare, Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs), in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 225–234, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Flockton and Sheehan 2000]

S. Flockton and K. Sheehan, Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 117–124, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Hollingworth et al. 2000]

G. Hollingworth, S. Smith, and A. Tyrrell, Safe intrinsic evolution of virtex devices, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 195–202, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Imamura et al. 2000]

K. Imamura, J. Foster, and A. Krings, Bidirectional incremental evolution in extrinsic evolvable hardware, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 75–80, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Jonathan et al. 2000]

M. Jonathan, R. Zebulum, M. Pacheco, and M. Vellasco, Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 133–140, Palo Alto, California, 13-15 July 2000, Jet Propulsion

Laboratory, California Institute of Technology, IEEE Computer Society.

[Kalganova 2000]

T. Kalganova, Bidirectional incremental evolution in extrinsic evolvable hardware, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 65–74, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Korkin et al. 2000]

M. Korkin, G. Fehr, and G. Jeffery, Evolving hardware on a large scale, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 173–182, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Koza et al. 2000]

J. R. Koza, J. Yu, M. A. Keane, and W. Mydlowec, Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 5–16, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Lee et al. 2000]

C. Lee, D. Hall, M. Perkowski, and D. Jun, Self-repairable eplds: Design, self-repair, and evaluation methodology, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 183–194, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levi 2000]

D. Levi, Hereboy: A fast evolutionary algorithm, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 17–24, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Levy et al. 2000]

R. Levy, S. Lepri, E. Sanchez, G. Ritter, and M. Sipper, Slate of the art: An evolving fpga-based board for handwritten-digit recognition, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 237–244, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Mange et al. 2000]

D. Mange, M. Sipper, A. Stauffer, and G. Tempesti, Toward self-repairing and self-replicating hardware: The embryonics approach, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 205–214, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Marston et al. 2000]

N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, and T. Higuchi, An evolutionary approach to ghz digital systems, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 125–131, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Masner et al. 2000]

J. Masner, J. Cavalieri, J. Frenzel, and J. Foster, Size versus robustness in evolved sorting networks: Is bigger better?, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 81-87, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Milano and Koumoutsakos 2000] M. Milano and P. Koumoutsakos, A clustering genetic algorithm for actuator optimization in flow control, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 263–270, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Pollack and Lipson 2000]

J. Pollack and H. Lipson, The golem project: Evolving hardware bodies and brains, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 37-42, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Seok et al. 2000]

H. Seok, K. Lee, B. Zhang, D. Lee, and K. Sim, Genetic programming of process decomposition strategies for evolvable hardware, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 25–34, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Stoica et al. 2000]

A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, and V. Duong, Evolution of analog circuits on field programmable transistor arrays, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 99–108, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Thompson and Wasshuber 2000]

A. Thompson and C. Wasshuber, Evolutionary design of single electron systems, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 109–116, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Torresen 2000]

J. Torresen, Scalable evolvable hardware applied to road image recognition, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 245–252, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Tufte and Haddow 2000]

G. Tufte and P. Haddow, Evolving an adaptive digital filter, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 143–150, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Vassilev and Miller 2000]

V. Vassilev and J. Miller, Scalability problems of digital circuit evolution: Evolvability and efficient designs, in The Second NASA/DoD workshop on Evolvable Hardware, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 55–64, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Yasunaga et al. 2000]

M. Yasunaga, T. Nakamura, I. Yoshihara, and J. Kim, Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 253–262, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[Zebulum et al. 2000]

R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, and M. Szwarcman, A reconfigurable platform for the automatic synthesis of analog circuits, in *The Second NASA/DoD workshop on Evolvable Hardware*, edited by J. Lohn, A. Stoica, and D. Keymeulen, pp. 91–98, Palo Alto, California, 13-15 July 2000, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.