Список литературы

[Bennett III & Rieffel(2000)] Bennett III, F. H. & Rieffel, E. (2000). Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Bradley *et al.*(2000)Bradley, Ortega-Sanchez & Tyrrell] Bradley, D., Ortega-Sanchez, C. & Tyrrell, A. (2000). Embryonics + immunotronics: A bio-inspired approach to fault tolerance. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Coello *et al.*(2000)Coello, Aguirre & Buckles] Coello, C., Aguirre, A. & Buckles, B. (2000). Evolutionary multiobjective design of combinational logic circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[de Garis *et al.*(2000)de Garis, Buller, Dob, Honlet, Guttikonda & Decesare] de Garis, H., Buller, A., Dob, T., Honlet, J., Guttikonda, P. & Decesare, D. (2000). Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Flockton & Sheehan(2000)] Flockton, S. & Sheehan, K. (2000). Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Hollingworth *et al.*(2000)Hollingworth, Smith & Tyrrell] Hollingworth, G., Smith, S. & Tyrrell, A. (2000). Safe intrinsic evolution of virtex devices. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Imamura *et al.*(2000)Imamura, Foster & Krings] Imamura, K., Foster, J. & Krings, A. (2000). Bidirectional incremental evolution in extrinsic evolvable hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Jonathan *et al.*(2000)Jonathan, Zebulum, Pacheco & Vellasco] Jonathan, M., Zebulum, R., Pacheco, M. & Vellasco, M. (2000). Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Kalganova(2000)] Kalganova, T. (2000). Bidirectional incremental evolution in extrinsic evolvable hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Korkin *et al.*(2000)Korkin, Fehr & Jeffery] Korkin, M., Fehr, G. & Jeffery, G. (2000). Evolving hardware on a large scale. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Koza *et al.*(2000)Koza, Yu, Keane & Mydlowec] Koza, J. R., Yu, J., Keane, M. A. & Mydlowec, W. (2000). Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Lee *et al.*(2000)Lee, Hall, Perkowski & Jun] Lee, C., Hall, D., Perkowski, M. & Jun, D. (2000). Self-repairable eplds: Design, self-repair, and evaluation methodology. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Levi(2000)] Levi, D. (2000). Hereboy: A fast evolutionary algorithm. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Levy *et al.*(2000)Levy, Lepri, Sanchez, Ritter & Sipper] Levy, R., Lepri, S., Sanchez, E., Ritter, G. & Sipper, M. (2000). Slate of the art: An evolving fpga-based board for handwritten-digit recognition. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Mange *et al.*(2000)Mange, Sipper, Stauffer & Tempesti] Mange, D., Sipper, M., Stauffer, A. & Tempesti, G. (2000). Toward self-repairing and self-replicating hardware: The embryonics approach. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Marston *et al.*(2000)Marston, Takahashi, Murakawa, Kasai, Adachi, Takasuka & Higuchi] Marston, N., Takahashi, E., Murakawa, M., Kasai, Y., Adachi, T., Takasuka, K. & Higuchi, T. (2000). An evolutionary approach to ghz digital systems. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Masner *et al.*(2000)Masner, Cavalieri, Frenzel & Foster] Masner, J., Cavalieri, J., Frenzel, J. & Foster, J. (2000). Size versus robustness in evolved sorting networks: Is bigger better? In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Milano & Koumoutsakos(2000)] Milano, M. & Koumoutsakos, P. (2000). A clustering genetic algorithm for actuator optimization in flow control. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Pollack & Lipson(2000)] Pollack, J. & Lipson, H. (2000). The golem project: Evolving hardware bodies and brains. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Seok *et al.*(2000)Seok, Lee, Zhang, Lee & Sim] Seok, H., Lee, K., Zhang, B., Lee, D. & Sim, K. (2000). Genetic programming of process decomposition strategies for evolvable hardware. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Stoica *et al.*(2000)Stoica, Keymeulen, Zebulum, Thakoor, Daud, Klimeck, Jin, Tawel & Duong] Stoica, A., Keymeulen, D., Zebulum, R., Thakoor, A., Daud, T., Klimeck, G., Jin, Y., Tawel, R. & Duong, V. (2000). Evolution of analog circuits on field programmable transistor arrays. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Thompson & Wasshuber(2000)] Thompson, A. & Wasshuber, C. (2000). Evolutionary design of single electron systems. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Torresen(2000)] Torresen, J. (2000). Scalable evolvable hardware applied to road image recognition. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Tufte & Haddow(2000)] Tufte, G. & Haddow, P. (2000). Evolving an adaptive digital filter. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Vassilev & Miller(2000)] Vassilev, V. & Miller, J. (2000). Scalability problems of digital circuit evolution: Evolvability and efficient designs. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Yasunaga *et al.*(2000)Yasunaga, Nakamura, Yoshihara & Kim] Yasunaga, M., Nakamura, T., Yoshihara, I. & Kim, J. (2000). Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.

[Zebulum *et al.*(2000)Zebulum, Sinohara, Vellasco, Santini, Pacheco & Szwarcman] Zebulum, R., Sinohara, H., Vellasco, M., Santini, C., Pacheco, M. & Szwarcman, M. (2000). A reconfigurable platform for the automatic synthesis of analog circuits. In: *The Second NASA/DoD workshop on Evolvable Hardware* (Lohn, J., Stoica, A. & Keymeulen, D., eds.). Jet Propulsion Laboratory, California Institute of Technology, Palo Alto, California: IEEE Computer Society.