

## Список литературы

- [1] SEGOVIA-JUAREZ, J. L. and COLOMBANO, S., Mutation buffering capabilities of the hypernetwork model, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 7–13, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [2] PFAFFMANN, J. O. and ZAUNER, K. P., Scouting context-sensitive components, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 14–20, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [3] DOLIN, B., Bennett III, F. H., and RIEFFEL, E. G., Methods for evolving robust distributed robot control software: coevolutionary and single population techniques, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 21–29, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [4] STOICA, A., ZEBULUM, R., and KEYMEULEN, D., Progress and challenges in building evolvable devices, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 33–35, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [5] SANTINI, C. C., ZEBULUM, R., PACHECO, M. A. C., VELLASCO, M. M. R., and SZWARCMAN, M. H., Pama-programmable analog multiplexer array, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 36–43, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [6] SINOHARA, H. T., PACHECO, M. A. C., and VELLASCO, M. M. R., Repair of analog circuits: Extrinsic and intrinsic evolutionary techniques, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 44–47, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [7] GALLAGHER, J. C., A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 48–55, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [8] SALEH, J. H., HASTINGS, D. E., and NEWMAN, D. J., Extracting the essence of flexibility in system design, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 59–72, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [9] ABRAMOVICI, M., EMMERT, J. M., and STROUD, C. E., Roving stars: An integrated approach to on-line testing, diagnosis, and fault tolerance for fpgas in adaptive computing systems, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 73–92, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [10] TYRRELL, A. M., HOLLINGWORTH, G., and SMITH, S. L., Evolutionary strategies and intrinsic fault tolerance, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 98–106, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [11] HADDOW, P. C. and TUFTE, G., Bridging the genotype-phenotype mapping for digital fpgas, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 109–115, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [12] MILLER, J. F. and HARTMANN, M., Evolving messy gates for fault tolerance: Some preliminary findings, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 116–123, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [13] HOUNSELL, B. I. and ARSLAN, T., Evolutionary design and adaption of digital filters within an embedded fault, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [14] HOUNSELL, B. I. and ARSLAN, T., Evolutionary design and adaption of digital filters within an embedded fault, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [15] SCHINER, T., YAO, X., and LIU, P., Digital filter design using multiple pareto fronts, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 136–145, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [16] CASTILLO, O., MONTIEL, O., SEPULVEDA, R., and MELIN, P., Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 146–153, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [17] Moreno Arostegui, J. M., SANCHEZ, E., and CABESTANY, J., An in-system routing strategy for evolvable hardware programmable platforms, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [18] Moreno Arostegui, J. M., SANCHEZ, E., and CABESTANY, J., An in-system routing strategy for evolvable hardware programmable platforms, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [19] EDWARDS, R. T. and KIM, C. J., Breaking the resistivity barrier, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 167–171, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [20] LANGEHEINE, J., BECKER, J., FOILLING, S., MEIRE, K., and SCHEMMEL, J., A cmos fpta chip for intrinsic hardware evolution of analog electronic circuits, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 172–175, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [21] RAMSDEN, E., The isppac family of reconfigurable analog circuits, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 176–181, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [22] STAUFFER, A., MANGE, D., TEMPESTI, G., and TEUSCHER, C., Biowatch: A giant electronic bio-inspired watch, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 185–192, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [23] BRADLEY, D. W. and TYRELL, A. M., The architecture for a hardware immune system, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 193–200, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [24] JACKSON, A. H. and TYRRELL, A. M., Asynchronous embryonics, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 201–210, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [25] de Garis, H., de Penning, L., BULLNER, A., and DECESARE, D., Early experiments on the cam-brain machine (cbm), in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 211–219, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [26] KAZADI, S., QI, Y., PARK, I., et al., Insufficiency of piecewise evolution, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 223–231, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [27] HERNANDEZ-AGUIRRE, A., BUCKLES, B. P., and COELLO, C. A. C., On learning knnf boolean formulas, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 240–246, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [28] LINDEN, D. S., A system for evolving antennas in-situ, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 249–255, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [29] DARREN, A. G., CONDE, R., CHERN, B., et al., Adaptive instrument module: Space instrument controller "brain" through programmable logic devices, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 256–260, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [30] PORTER, R., GOKHALE, M., HARVEY, N., PERKINS, S., and YOUNG, C., Evolving network architectures with custom computers for multi-spectral feature identification, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 261–270, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [31] LOCKWOOD, J. W., Evolvable internet hardware platforms, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 271–279, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [32] GRAHAM, R. I. and ARSLAN, T., Rule evolution in order based diagnostic systems, in KEYMEULEN, D., STOICA, A., LOHN, J., and ZEBULUM, R. S., editors, *The Third NASA/DoD workshop on Evolvable Hardware*, pp. 280–286, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.