# Список литературы

[1] Segovia-Juarez, J. L. and Colombano, S., Mutation buffering capabilities of the hypernetwork model, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 7–13, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[2] Pfaffmann, J. O. and Zauner, K. P., Scouting context-sensitive components, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 14–20, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[3] Dolin, B., Bennett III, F. H., and Rieffel, E. G., Methods for evolving robust distributed robot control software: coevolutionary and single population techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 21–29, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[4] Stoica, A., Zebulum, R., and Keymeulen, D., Progress and challenges in building evolvable devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 33–35, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[5] Santini, C. C., Zebulum, R., Pacheco, M. A. C., Vellasco, M. M. R., and Szwarcman, M. H., Pama-programmable analog multiplexter array, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 36–43, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[6] Sinohara, H. T., Pacheco, M. A. C., and Vellasco, M. M. R., Repair of analog circuits: Extrinsic and instrinsic evolutionary techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 44–47, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[7] Gallagher, J. C., A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 48–55, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[8] Saleh, J. H., Hastings, D. E., and Newman, D. J., Extracting the essence of flexibility in system design, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 59–72, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[9] Abramovici, M., Emmert, J. M., and Stroud, C. E., Roving stars: An integrated approach to on-line testing, diagnosis,and fault tolerance for fpgas in adaptive computing systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 73–92, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[10] Tyrrell, A. M., Hollingworth, G., and Smith, S. L., Evolutionary strategies and intrinsic fault tolerance, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 98–106, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[11] Haddow, P. C. and Tufte, G., Bridging the genotype-phenotype mapping for digital fpgas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 109–115, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[12] Miller, J. F. and Hartmann, M., Evolving messy gates for fault tolerance: Some preliminary findings, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 116–123, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[13] Hounsell, B. I. and Arslan, T., Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[14] Hounsell, B. I. and Arslan, T., Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[15] Schiner, T., Yao, X., and Liu, P., Digital filter design using multiple pareto fronts, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 136–145, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[16] Castillo, O., Montiel, O., Sepulveda, R., and Melin, P., Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 146–153, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[17] Moreno Arostegui, J. M., Sanchez, E., and Cabestany, J., An in-system routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[18] Moreno Arostegui, J. M., Sanchez, E., and Cabestany, J., An in-system routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[19] Edwards, R. T. and Kim, C. J., Breaking the resistivity barrier, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 167–171, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[20] Langeheine, J., Becker, J., Foilling, S., Meire, K., and Schemmel, J., A cmos fpta chip for intrinsic hardware evolution of analong electronic circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 172–175, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[21] Ramsden, E., The isppac family of reconfigurable analog circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 176–181, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[22] Stauffer, A., Mange, D., Tempesti, G., and Teuscher, C., Biowatch: A giant electronic bio-inspired watch, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 185–192, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[23] Bradley, D. W. and Tyrell, A. M., The architecture for a hardware immune system, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 193–200, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[24] Jackson, A. H. and Tyrrell, A. M., Asynchronous embryonics, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 201–210, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[25] de Garis, H., de Penning, L., Bullner, A., and Decesare, D., Early experiments on the cam-brain machine (cbm), in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 211–219, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[26] Kazadi, S. et al., Insufficiency of piecewise evolution, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 223–231, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[27] Hernandez-Aguirre, A., Buckles, B. P., and Coello, C. A. C., On learning kdnf boolean formulas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 240–246, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[28] Linden, D. S., A system for evolving antennas in-situ, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 249–255, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[29] Darren, A. G. et al., Adaptive instrument module: Space instrument controller "brain"through progammable logic devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 256–260, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[30] Porter, R., Gokhale, M., Harvey, N., Perkins, S., and Young, C., Evolving network architectures with custom computers for multi-spectral feature identification, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 261–270, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[31] Lockwood, J. W., Evovable internet hardware platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 271–279, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[32] Graham, R. I. and Arslan, T., Rule evolution in order based diagnostic systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen, D., Stoica, A., Lohn, J., and Zebulum, R. S., pages 280–286, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.