

Список литературы

- [1] Segovia-Juarez JL, Colombano S. Mutation Buffering Capabilities of the Hypernetwork Model. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 7-13.
- [2] Pfaffmann JO, Zauner KP. Scouting COnText-Sensitive Components. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 14-20.
- [3] Dolin B, Bennett III FH, Rieffel EG. Methods for evolving robust distributed robot control software: coevolutionary and single population techniques. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 21-9.
- [4] Stoica A, Zebulum R, Keymeulen D. Progress and Challenges in Building Evolvable Devices. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 33-5.
- [5] Santini CC, Zebulum R, Pacheco MAC, Vellasco MMR, Szwarcman MH. PAMA-Programmable Analog Multiplexer Array. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 36-43.
- [6] Sinohara HT, Pacheco MAC, Vellasco MMR. Repair of Analog Circuits: Extrinsic and Intrinsic Evolutionary Techniques. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 44-7.
- [7] Gallagher JC. A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 48-55.
- [8] Saleh JH, Hastings DE, Newman DJ. Extracting the Essence of Flexibility in System Design. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 59-72.
- [9] Abramovici M, Emmert JM, Stroud CE. Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault Tolerance for FPGAs in Adaptive Computing Systems. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 73-92.
- [10] Tyrrell AM, Hollingworth G, Smith SL. Evolutionary Strategies and Intrinsic Fault Tolerance. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 98-106.
- [11] Haddow PC, Tufte G. Bridging the Genotype-Phenotype Mapping for Digital FPGAs. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 109-15.

- [12] Miller JF, Hartmann M. Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 116-23.
- [13] Hounsell BI, Arslan T. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 127-35.
- [14] Hounsell BI, Arslan T. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 127-35.
- [15] Schiner T, Yao X, Liu P. Digital filter Design Using Multiple Pareto Fronts. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 136-45.
- [16] Castillo O, Montiel O, Sepulveda R, Melin P. Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 146-53.
- [17] Moreno Arostegui JM, Sanchez E, Cabestany J. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 157-66.
- [18] Moreno Arostegui JM, Sanchez E, Cabestany J. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 157-66.
- [19] Edwards RT, Kim CJ. Breaking the Resistivity Barrier. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 167-71.
- [20] Langeheine J, Becker J, Foilling S, Meire K, Schemmel J. A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analog Electronic Circuits. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 172-5.
- [21] Ramsden E. The ispPAC Family of Reconfigurable Analog Circuits. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 176-81.
- [22] Stauffer A, Mange D, Tempesti G, Teuscher C. BioWatch: A Giant Electronic Bio-Inspired Watch. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 185-92.
- [23] Bradley DW, Tyrell AM. The Architecture for a Hardware Immune System. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 193-200.

- [24] Jackson AH, Tyrrell AM. Asynchronous Embryonics. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 201-10.
- [25] de Garis H, de Penning L, Bullner A, Decesare D. Early Experiments on the CAM-Brain Machine (CBM). In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 211-9.
- [26] Kazadi S, Qi Y, Park I, Huang N, Hwu P, Kwan B, et al. Insufficiency of Piecewise Evolution. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 223-31.
- [27] Hernandez-Aguirre A, Buckles BP, Coello CAC. On Learning KDNF Boolean Formulas. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 240-6.
- [28] Linden DS. A System for Evolving Antennas In-Situ. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 249-55.
- [29] Darren AG, Conde R, Chern B, Luers P, Jurczyk S, Mills C. Adaptive Instrument Module: Space Instrument Controller "Brain"through Programmable Logic Devices. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 256-60.
- [30] Porter R, Gokhale M, Harvey N, Perkins S, Young C. Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 261-70.
- [31] Lockwood JW. Evolvable Internet Hardware Platforms. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 271-9.
- [32] Graham RI, Arslan T. Rule Evolution in Order Based Diagnostic Systems. In: Keymeulen D, Stoica A, Lohn J, Zebulum RS, editors. The Third NASA/DoD workshop on Evolvable Hardware. Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society; 2001. p. 280-6.