Список литературы

[1] J. L. Segovia-Juarez and S. Colombano, Mutation buffering capabilities of the hypernetwork model, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 7–13, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[2] J. O. Pfaffmann and K. P. Zauner, Scouting context-sensitive components, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 14–20, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[3] B. Dolin, F. H. Bennett III, and E. G. Rieffel, Methods for evolving robust distributed robot control software: coevolutionary and single population techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 21–29, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[4] A. Stoica, R. Zebulum, and D. Keymeulen, Progress and challenges in building evolvable devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 33–35, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[5] C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco, and M. H. Szwarcman, Pama-programmable analog multiplexter array, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 36–43, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[6] H. T. Sinohara, M. A. C. Pacheco, and M. M. R. Vellasco, Repair of analog circuits: Extrinsic and instrinsic evolutionary techniques, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 44–47, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[7] J. C. Gallagher, A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 48–55, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[8] J. H. Saleh, D. E. Hastings, and D. J. Newman, Extracting the essence of flexibility in system design, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 59–72, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[9] M. Abramovici, J. M. Emmert, and C. E. Stroud, Roving stars: An integrated approach to on-line testing, diagnosis,and fault tolerance for fpgas in adaptive computing systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 73–92, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[10] A. M. Tyrrell, G. Hollingworth, and S. L. Smith, Evolutionary strategies and intrinsic fault tolerance, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 98–106, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[11] P. C. Haddow and G. Tufte, Bridging the genotype-phenotype mapping for digital fpgas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 109–115, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[12] J. F. Miller and M. Hartmann, Evolving messy gates for fault tolerance: Some preliminary findings, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 116–123, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[13] B. I. Hounsell and T. Arslan, Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[14] B. I. Hounsell and T. Arslan, Evolutionary design and adaption of digital filters within an embedded fault, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 127–135, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[15] T. Schiner, X. Yao, and P. Liu, Digital filter design using multiple pareto fronts, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 136–145, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[16] O. Castillo, O. Montiel, R. Sepulveda, and P. Melin, Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 146–153, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[17] J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany, An in-system routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[18] J. M. Moreno Arostegui, E. Sanchez, and J. Cabestany, An in-system routing strategy for evolvable hardware programmable platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 157–166, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[19] R. T. Edwards and C. J. Kim, Breaking the resistivity barrier, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 167–171, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[20] J. Langeheine, J. Becker, S. Foilling, K. Meire, and J. Schemmel, A cmos fpta chip for intrinsic hardware evolution of analong electronic circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 172–175, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[21] E. Ramsden, The isppac family of reconfigurable analog circuits, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 176–181, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[22] A. Stauffer, D. Mange, G. Tempesti, and C. Teuscher, Biowatch: A giant electronic bio-inspired watch, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 185–192, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[23] D. W. Bradley and A. M. Tyrell, The architecture for a hardware immune system, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 193–200, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[24] A. H. Jackson and A. M. Tyrrell, Asynchronous embryonics, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 201–210, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[25] H. de Garis, L. de Penning, A. Bullner, and D. Decesare, Early experiments on the cambrain machine (cbm), in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 211–219, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[26] S. Kazadi et al., Insufficiency of piecewise evolution, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 223–231, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[27] A. Hernandez-Aguirre, B. P. Buckles, and C. A. C. Coello, On learning kdnf boolean formulas, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 240–246, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[28] D. S. Linden, A system for evolving antennas in-situ, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 249–255, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[29] A. G. Darren et al., Adaptive instrument module: Space instrument controller "brain"through progammable logic devices, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 256–260, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[30] R. Porter, M. Gokhale, N. Harvey, S. Perkins, and C. Young, Evolving network architectures with custom computers for multi-spectral feature identification, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 261–270, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[31] J. W. Lockwood, Evovable internet hardware platforms, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 271–279, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

[32] R. I. Graham and T. Arslan, Rule evolution in order based diagnostic systems, in *The Third NASA/DoD workshop on Evolvable Hardware*, edited by D. Keymeulen, A. Stoica, J. Lohn, and R. S. Zebulum, pages 280–286, Long Beach, California, 2001, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.