

## Список литературы

- [1] F. H Bennett III ja E. Rieffel. Design of decentralized controllers for self-reconfigurable modular robots using genetic programming. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 43–52, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [2] D. Bradley, C. Ortega-Sanchez, ja A. Tyrrell. Embryonics + immunotronics: A bio-inspired approach to fault tolerance. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 205–224, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [3] C. Coello, A. Aguirre, ja B. Buckles. Evolutionary multiobjective design of combinational logic circuits. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 161–170, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [4] H. de Garis, A. Buller, T. Dob, J. Honlet, P. Guttikonda, ja D. Decesare. Building multimodule systems with unlimited evolvable capacities from modules with limited evolvable capacities (mecs). Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 225–234, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [5] S. Flockton ja K. Sheehan. Behavior of a building block for intrinsic evolution of analogue signal shaping and filtering circuits. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 117–124, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [6] G. Hollingworth, S. Smith, ja A. Tyrrell. Safe intrinsic evolution of virtex devices. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 195–202, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [7] K. Imamura, J. Foster, ja A. Krings. Bidirectional incremental evolution in extrinsic evolvable hardware. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 75–80, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [8] M. Jonathan, R. Zebulum, M. Pacheco, ja M. Vellasco. Multiobjective optimization techniques: A study of the energy minimization method and its application to the synthesis of ota amplifiers. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 133–140, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [9] T. Kalganova. Bidirectional incremental evolution in extrinsic evolvable hardware. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 65–74, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [10] M. Korkin, G. Fehr, ja G. Jeffery. Evolving hardware on a large scale. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 173–182, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [11] John R. Koza, Jessen Yu, Martin A. Keane, ja William Mydlowec. Use of conditional developmental operators and free variables in automatically synthesizing generalized circuits using genetic programming. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second*

- NASA/DoD workshop on Evolvable Hardware*, ss. 5–16, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [12] C. Lee, D. Hall, M. Perkowski, ja D. Jun. Self-repairable eplds: Design, self-repair, and evaluation methodology. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 183–194, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [13] D. Levi. Hereboy: A fast evolutionary algorithm. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 17–24, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [14] R. Levy, S. Lepri, E. Sanchez, G. Ritter, ja M. Sipper. Slate of the art: An evolving fpga-based board for handwritten-digit recognition. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 237–244, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [15] D. Mange, M. Sipper, A. Stauffer, ja G. Tempesti. Toward self-repairing and self-replicating hardware: The embryonics approach. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 205–214, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [16] N. Marston, E. Takahashi, M. Murakawa, Y. Kasai, T. Adachi, K. Takasuka, ja T. Higuchi. An evolutionary approach to ghz digital systems. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 125–131, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [17] J. Masner, J. Cavalieri, J. Frenzel, ja J. Foster. Size versus robustness in evolved sorting networks: Is bigger better? Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 81–87, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [18] M. Milano ja P. Koumoutsakos. A clustering genetic algorithm for actuator optimization in flow control. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 263–270, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [19] J. Pollack ja H. Lipson. The golem project: Evolving hardware bodies and brains. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 37–42, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [20] H. Seok, K. Lee, B. Zhang, D. Lee, ja K. Sim. Genetic programming of process decomposition strategies for evolvable hardware. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 25–34, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [21] A. Stoica, D. Keymeulen, R. Zebulum, A. Thakoor, T. Daud, G. Klimeck, Y. Jin, R. Tawel, ja V. Duong. Evolution of analog circuits on field programmable transistor arrays. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 99–108, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
  - [22] A. Thompson ja C. Wasshuber. Evolutionary design of single electron systems. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 109–116, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.

- [23] J. Torresen. Scalable evolvable hardware applied to road image recognition. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 245–252, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [24] G. Tufte ja P. Haddow. Evolving an adaptive digital filter. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 143–150, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [25] V. Vassilev ja J. Miller. Scalability problems of digital circuit evolution: Evolvability and efficient designs. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 55–64, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [26] M. Yasunaga, T. Nakamura, I. Yoshihara, ja J. Kim. Kernel-based pattern recognition hardware: Its design methodology using evolved truth tables. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 253–262, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.
- [27] R. Zebulum, H. Sinohara, M. Vellasco, C. Santini, M. Pacheco, ja M. Szwarcman. A reconfigurable platform for the automatic synthesis of analog circuits. Kirjassa Jason Lohn, Adrian Stoica, ja Didier Keymeulen, toim., *The Second NASA/DoD workshop on Evolvable Hardware*, ss. 91–98, Palo Alto, California, 13-15 July 2000. Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society.