

Список литературы

- [1] J. L. Segovia-Juarez and S. Colombano, *Mutation buffering capabilities of the hypernetwork model*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 7–13, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [2] J. O. Pfaffmann and K. P. Zauner, *Scouting context-sensitive components*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 14–20, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [3] B. Dolin, F. H. Bennett III and E. G. Rieffel, *Methods for evolving robust distributed robot control software: coevolutionary and single population techniques*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 21–29, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [4] A. Stoica, R. Zebulum and D. Keymeulen, *Progress and challenges in building evolvable devices*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 33–35, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [5] C. C. Santini, R. Zebulum, M. A. C. Pacheco, M. M. R. Vellasco and M. H. Szwarcman, *Pama-programmable analog multiplexer array*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 36–43, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [6] H. T. Sinohara, M. A. C. Pacheco and M. M. R. Vellasco, *Repair of analog circuits: Extrinsic and intrinsic evolutionary techniques*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 44–47, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [7] J. C. Gallagher, *A neuromorphic paradigm for extrinsically evolved hybrid analog/digital device controllers: Initial explorations*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 48–55, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [8] J. H. Saleh, D. E. Hastings and D. J. Newman, *Extracting the essence of flexibility in system design*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 59–72, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [9] M. Abramovici, J. M. Emmert and C. E. Stroud, *Roving stars: An integrated approach to on-line testing, diagnosis, and fault tolerance for fpgas in adaptive computing systems*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 73–92, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [10] A. M. Tyrrell, G. Hollingworth and S. L. Smith, *Evolutionary strategies and intrinsic fault tolerance*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 98–106, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [11] P. C. Haddow and G. Tufte, *Bridging the genotype-phenotype mapping for digital fpgas*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 109–115, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.

- [12] J. F. Miller and M. Hartmann, *Evolving messy gates for fault tolerance: Some preliminary findings*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 116–123, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [13] B. I. Hounsell and T. Arslan, *Evolutionary design and adaption of digital filters within an embedded fault*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 127–135, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [14] B. I. Hounsell and T. Arslan, *Evolutionary design and adaption of digital filters within an embedded fault*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 127–135, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [15] T. Schiner, X. Yao and P. Liu, *Digital filter design using multiple pareto fronts*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 136–145, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [16] O. Castillo, O. Montiel, R. Sepulveda and P. Melin, *Application of a breeder genetic algorithm for system identification in an adaptive finite impulse response filter*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 146–153, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [17] J. M. Moreno Arostegui, E. Sanchez and J. Cabestany, *An in-system routing strategy for evolvable hardware programmable platforms*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 157–166, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [18] J. M. Moreno Arostegui, E. Sanchez and J. Cabestany, *An in-system routing strategy for evolvable hardware programmable platforms*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 157–166, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [19] R. T. Edwards and C. J. Kim, *Breaking the resistivity barrier*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 167–171, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [20] J. Langeheine, J. Becker, S. Foilling, K. Meire and J. Schemmel, *A cmos fpta chip for intrinsic hardware evolution of analog electronic circuits*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 172–175, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [21] E. Ramsden, *The isppac family of reconfigurable analog circuits*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 176–181, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [22] A. Stauffer, D. Mange, G. Tempesti and C. Teuscher, *Biowatch: A giant electronic bio-inspired watch*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 185–192, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.

- [23] D. W. Bradley and A. M. Tyrell, *The architecture for a hardware immune system*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 193–200, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [24] A. H. Jackson and A. M. Tyrrell, *Asynchronous embryonics*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 201–210, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [25] H. de Garis, L. de Penning, A. Bullner and D. Decesare, *Early experiments on the cam-brain machine (cbm)*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 211–219, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [26] S. Kazadi, Y. Qi, I. Park, N. Huang, P. Hwu, B. Kwan, W. Lue and H. Li, *Insufficiency of piecewise evolution*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 223–231, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [27] A. Hernandez-Aguirre, B. P. Buckles and C. A. C. Coello, *On learning kdnf boolean formulas*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 240–246, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [28] D. S. Linden, *A system for evolving antennas in-situ*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 249–255, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [29] A. G. Darren, R. Conde, B. Chern, P. Luers, S. Jurczyk and C. Mills, *Adaptive instrument module: Space instrument controller "brain" through programmable logic devices*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 256–260, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [30] R. Porter, M. Gokhale, N. Harvey, S. Perkins and C. Young, *Evolving network architectures with custom computers for multi-spectral feature identification*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 261–270, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [31] J. W. Lockwood, *Evolvable internet hardware platforms*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 271–279, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.
- [32] R. I. Graham and T. Arslan, *Rule evolution in order based diagnostic systems*, in *The Third NASA/DoD workshop on Evolvable Hardware* (D. Keymeulen, A. Stoica, J. Lohn and R. S. Zebulum, eds.), (Long Beach, California), pp. 280–286, Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, 12-14 July, 2001.