Список литературы

- [1] Abramovici, M. Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault TOlerance for FPGAs in Adaptive Computing Systems / M. Abramovici, J. M. Emmert, C. E. Stroud // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 73–92.
- [2] Adaptive Instrument Module: Space Instrument Controller "Brain"through Progammable Logic Devices / A. G. Darren, R. Conde, B. Chern et al. // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 256–260.
- [3] Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter / O. Castillo, O. Montiel, R. Sepulveda, P. Melin // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 146—153.
- [4] BioWatch: A Giant Electronic Bio-Inspired Watch / A. Stauffer, D. Mange, G. Tempesti,
 C. Teuscher // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 185–192.
- [5] Bradley, D. W. The Architecture for a Hardware Immune System / D. W. Bradley, A. M. Tyrell // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. — Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 193–200.
- [6] A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analong Electronic Circuits / J. Langeheine, J. Becker, S. Foilling et al. // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 172–175.
- [7] Dolin, B. Methods for evolving robust distributed robot control software: coevolutionary and single population techniques / B. Dolin, F. H. Bennett III, E. G. Rieffel // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 21–29.
- [8] Early Experiments on the CAM-Brain Machine (CBM) / H. de Garis, L. de Penning, A. Bullner,
 D. Decesare // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July.
 P. 211–219.
- [9] Edwards, R. T. Breaking the Resistivity Barrier / R. T. Edwards, C. J. Kim // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. — Long Beach, California: IEEE Computer Society, 2001. — 12-14 July. — P. 167–171.
- [10] Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification / R. Porter, M. Gokhale, N. Harvey et al. // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 261–270.
- [11] Gallagher, J. C. A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations / J. C. Gallagher // The Third NASA/DoD workshop on

- Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum ; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California : IEEE Computer Society, 2001.-12-14 July. P. 48-55.
- [12] Graham, R. I. Rule Evolution in Order Based Diagnostic Systems / R. I. Graham, T. Arslan // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. — Long Beach, California: IEEE Computer Society, 2001. — 12-14 July. — P. 280–286.
- [13] Haddow, P. C. Bridging the Genotype-Phenotype Mapping for Digital FPGAs / P. C. Haddow,
 G. Tufte // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 109–115.
- [14] Hernandez-Aguirre, A. On Learning KDNF Boolean Formulas / A. Hernandez-Aguirre, B. P. Buckles, C. A. C. Coello // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 240–246.
- [15] Hounsell, B. I. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault / B. I. Hounsell, T. Arslan // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 127–135.
- [16] Hounsell, B. I. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault / B. I. Hounsell, T. Arslan // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 127–135.
- [17] Insufficiency of Piecewise Evolution / S. Kazadi, Y. Qi, I. Park et al. // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 223–231.
- [18] Jackson, A. H. Asynchronous Embryonics / A. H. Jackson, A. M. Tyrrell // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 201–210.
- [19] Linden, D. S. A System for Evolving Antennas In-Situ / D. S. Linden // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 249–255.
- [20] Lockwood, J. W. Evovable Internet Hardware Platforms / J. W. Lockwood // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. — Long Beach, California: IEEE Computer Society, 2001. — 12-14 July. — P. 271—279.
- [21] Miller, J. F. Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings / J. F. Miller,
 M. Hartmann // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July.
- [22] Moreno Arostegui, J. M. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms / J. M. Moreno Arostegui, E. Sanchez, J. Cabestany // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 157–166.

- [23] Moreno Arostegui, J. M. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms / J. M. Moreno Arostegui, E. Sanchez, J. Cabestany // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 157–166.
- [24] PAMA-Programmable Analog Multiplexter Array / C. C. Santini, R. Zebulum, M. A. C. Pacheco et al. // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 36–43.
- [25] Pfaffmann, J. O. Scouting COntext-Sensitive Components / J. O. Pfaffmann, K. P. Zauner // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 14–20.
- [26] Ramsden, E. The ispPAC Family of Reconfigurable Analog Circuits / E. Ramsden // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 176—181.
- [27] Saleh, J. H. Extracting the Essence of Flexibility in System Design / J. H. Saleh, D. E. Hastings,
 D. J. Newman // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 59-72.
- [28] Schiner, T. Digital filter Design Using Multiple Pareto Fronts / T. Schiner, X. Yao, P. Liu // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 136–145.
- [29] Segovia-Juarez, J. L. Mutation Buffering Capabilities of the Hypernetwork Model / J. L. Segovia-Juarez, S. Colombano // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 7—13.
- [30] Sinohara, H. T. Repair of Analog Circuits: Extrinsic and Instrinsic Evolutionary Techniques / H. T. Sinohara, M. A. C. Pacheco, M. M. R. Vellasco // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 44-47.
- [31] Stoica, A. Progress and Challenges in Building Evolvable Devices / A. Stoica, R. Zebulum,
 D. Keymeulen // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen,
 A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology.
 Long Beach, California: IEEE Computer Society, 2001. 12-14 July. P. 33-35.
- [32] Tyrrell, A. M. Evolutionary Strategies and Intrinsic Fault Tolerance / A. M. Tyrrell, G. Hollingworth, S. L. Smith // The Third NASA/DoD workshop on Evolvable Hardware / ed. by D. Keymeulen, A. Stoica, J. Lohn, R. S. Zebulum; Jet Propulsion Laboratory, California Institute of Technology. Long Beach, California: IEEE Computer Society, 2001.—12-14 July.— P. 98-106.