Список литературы

[1] Segovia-Juarez, J. L & Colombano, S. (2001) *Mutation Buffering Capabilities of the Hypernetwork Model* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 7–13.

[2] Pfaffmann, J. O & Zauner, K. P. (2001) *Scouting COntext-Sensitive Components* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 14–20.

[3] Dolin, B, Bennett III, F. H, & Rieffel, E. G. (2001) *Methods for evolving robust distributed robot control software: coevolutionary and single population techniques* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 21–29.

[4] Stoica, A, Zebulum, R, & Keymeulen, D. (2001) *Progress and Challenges in Building Evolvable Devices* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 33–35.

[5] Santini, C. C, Zebulum, R, Pacheco, M. A. C, Vellasco, M. M. R, & Szwarcman, M. H. (2001) *PAMA-Programmable Analog Multiplexter Array* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 36–43.

[6] Sinohara, H. T, Pacheco, M. A. C, & Vellasco, M. M. R. (2001) *Repair of Analog Circuits: Extrinsic and Instrinsic Evolutionary Techniques* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 44–47.

[7] Gallagher, J. C. (2001) *A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 48–55.

[8] Saleh, J. H, Hastings, D. E, & Newman, D. J. (2001) *Extracting the Essence of Flexibility in System Design* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 59–72.

[9] Abramovici, M, Emmert, J. M, & Stroud, C. E. (2001) *Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis,and Fault TOlerance for FPGAs in Adaptive Computing Systems* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 73–92.

[10] Tyrrell, A. M, Hollingworth, G, & Smith, S. L. (2001) *Evolutionary Strategies and Intrinsic Fault Tolerance* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 98–106.

[11] Haddow, P. C & Tufte, G. (2001) *Bridging the Genotype-Phenotype Mapping for Digital FPGAs* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 109–115.

[12] Miller, J. F & Hartmann, M. (2001) *Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 116–123.

[13] Hounsell, B. I & Arslan, T. (2001) *Evolutionary Design and Adaption of Digital Filters within an Embedded Fault* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 127–135.

[14] Hounsell, B. I & Arslan, T. (2001) *Evolutionary Design and Adaption of Digital Filters within an Embedded Fault* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 127–135.

[15] Schiner, T, Yao, X, & Liu, P. (2001) *Digital filter Design Using Multiple Pareto Fronts* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 136–145.

[16] Castillo, O, Montiel, O, Sepulveda, R, & Melin, P. (2001) *Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 146–153.

[17] Moreno Arostegui, J. M, Sanchez, E, & Cabestany, J. (2001) *An In-System Routing Strategy for Evolvable Hardware Programmable Platforms* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 157–166.

[18] Moreno Arostegui, J. M, Sanchez, E, & Cabestany, J. (2001) *An In-System Routing Strategy for Evolvable Hardware Programmable Platforms* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 157–166.

[19] Edwards, R. T & Kim, C. J. (2001) *Breaking the Resistivity Barrier* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 167–171.

[20] Langeheine, J, Becker, J, Foilling, S, Meire, K, & Schemmel, J. (2001) *A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analong Electronic Circuits* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 172–175.

[21] Ramsden, E. (2001) *The ispPAC Family of Reconfigurable Analog Circuits* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 176–181.

[22] Stauffer, A, Mange, D, Tempesti, G, & Teuscher, C. (2001) *BioWatch: A Giant Electronic Bio-Inspired Watch* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 185–192.

[23] Bradley, D. W & Tyrell, A. M. (2001) *The Architecture for a Hardware Immune System* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 193–200.

[24] Jackson, A. H & Tyrrell, A. M. (2001) *Asynchronous Embryonics* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 201–210.

[25] de Garis, H, de Penning, L, Bullner, A, & Decesare, D. (2001) *Early Experiments on the CAM-Brain Machine (CBM)* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 211–219.

[26] Kazadi, S, Qi, Y, Park, I, Huang, N, Hwu, P, Kwan, B, Lue, W, & Li, H. (2001) *Insufficiency of Piecewise Evolution* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 223–231.

[27] Hernandez-Aguirre, A, Buckles, B. P, & Coello, C. A. C. (2001) *On Learning KDNF Boolean Formulas* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 240–246.

[28] Linden, D. S. (2001) *A System for Evolving Antennas In-Situ* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 249–255.

[29] Darren, A. G, Conde, R, Chern, B, Luers, P, Jurczyk, S, & Mills, C. (2001) *Adaptive Instrument Module: Space Instrument Controller "Brain"through Progammable Logic Devices* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 256–260.

[30] Porter, R, Gokhale, M, Harvey, N, Perkins, S, & Young, C. (2001) *Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 261–270.

[31] Lockwood, J. W. (2001) *Evovable Internet Hardware Platforms* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 271–279.

[32] Graham, R. I & Arslan, T. (2001) *Rule Evolution in Order Based Diagnostic Systems* eds. Keymeulen, D, Stoica, A, Lohn, J, & Zebulum, R. S. (Jet Propulsion Laboratory, California Institute of Technology, IEEE Computer Society, Long Beach, California), pp. 280–286.