

Список литературы

- [1] Segovia-Juarez JL, Colombano S. Mutation Buffering Capabilities of the Hypernetwork Model. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 7–13.
- [2] Pfaffmann JO, Zauner KP. Scouting COntext-Sensitive Components. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 14–20.
- [3] Dolin B, Bennett III FH, Rieffel EG. Methods for evolving robust distributed robot control software: coevolutionary and single population techniques. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 21–29.
- [4] Stoica A, Zebulum R, Keymeulen D. Progress and Challenges in Building Evolvable Devices. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 33–35.
- [5] Santini CC, Zebulum R, Pacheco MAC, Vellasco MMR, Szwarcman MH. PAMA-Programmable Analog Multiplexer Array. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 36–43.
- [6] Sinohara HT, Pacheco MAC, Vellasco MMR. Repair of Analog Circuits: Extrinsic and Intrinsic Evolutionary Techniques. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 44–47.
- [7] Gallagher JC. A Neuromorphic Paradigm for Extrinsically Evolved Hybrid Analog/Digital Device Controllers: Initial Explorations. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 48–55.
- [8] Saleh JH, Hastings DE, Newman DJ. Extracting the Essence of Flexibility in System Design. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 59–72.
- [9] Abramovici M, Emmert JM, Stroud CE. Roving STARS: An Integrated Approach to On-Line Testing, Diagnosis, and Fault Tolerance for FPGAs in Adaptive Computing Systems. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 73–92.
- [10] Tyrrell AM, Hollingworth G, Smith SL. Evolutionary Strategies and Intrinsic Fault Tolerance. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 98–106.
- [11] Haddow PC, Tufte G. Bridging the Genotype-Phenotype Mapping for Digital FPGAs. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 109–115.

- [12] Miller JF, Hartmann M. Evolving Messy Gates for Fault Tolerance: Some Preliminary Findings. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 116–123.
- [13] Hounsell BI, Arslan T. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 127–135.
- [14] Hounsell BI, Arslan T. Evolutionary Design and Adaption of Digital Filters within an Embedded Fault. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 127–135.
- [15] Schiner T, Yao X, Liu P. Digital filter Design Using Multiple Pareto Fronts. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 136–145.
- [16] Castillo O, Montiel O, Sepulveda R, Melin P. Application of a Breeder Genetic Algorithm for System Identification in an Adaptive Finite Impulse Response Filter. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 146–153.
- [17] Moreno Arostegui JM, Sanchez E, Cabestany J. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 157–166.
- [18] Moreno Arostegui JM, Sanchez E, Cabestany J. An In-System Routing Strategy for Evolvable Hardware Programmable Platforms. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 157–166.
- [19] Edwards RT, Kim CJ. Breaking the Resistivity Barrier. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 167–171.
- [20] Langeheine J, Becker J, Foilling S, Meire K, Schemmel J. A CMOS FPTA Chip for Intrinsic Hardware Evolution of Analog Electronic Circuits. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 172–175.
- [21] Ramsden E. The ispPAC Family of Reconfigurable Analog Circuits. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 176–181.
- [22] Stauffer A, Mange D, Tempesti G, Teuscher C. BioWatch: A Giant Electronic Bio-Inspired Watch. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 185–192.
- [23] Bradley DW, Tyrell AM. The Architecture for a Hardware Immune System. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 193–200.

- [24] Jackson AH, Tyrrell AM. Asynchronous Embryonics. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 201–210.
- [25] de Garis H, de Penning L, Bullner A, Decesare D. Early Experiments on the CAM-Brain Machine (CBM). In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 211–219.
- [26] Kazadi S, Qi Y, Park I, Huang N, Hwu P, Kwan B, Lue W, Li H. Insufficiency of Piecewise Evolution. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 223–231.
- [27] Hernandez-Aguirre A, Buckles BP, Coello CAC. On Learning KDNF Boolean Formulas. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 240–246.
- [28] Linden DS. A System for Evolving Antennas In-Situ. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 249–255.
- [29] Darren AG, Conde R, Chern B, Luers P, Jurczyk S, Mills C. Adaptive Instrument Module: Space Instrument Controller "Brain" through Programmable Logic Devices. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 256–260.
- [30] Porter R, Gokhale M, Harvey N, Perkins S, Young C. Evolving Network Architectures with Custom Computers for Multi-Spectral feature Identification. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 261–270.
- [31] Lockwood JW. Evolvable Internet Hardware Platforms. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 271–279.
- [32] Graham RI, Arslan T. Rule Evolution in Order Based Diagnostic Systems. In: *The Third NASA/DoD workshop on Evolvable Hardware*, edited by Keymeulen D, Stoica A, Lohn J, Zebulum RS. Jet Propulsion Laboratory, California Institute of Technology, Long Beach, California: IEEE Computer Society. 2001; pp. 280–286.