

UJUN



CRYPTOGRAPHY USING SYSTEMVERILOG LEVERAGING AES

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Objective

- **Problem Statement:** Cryptography is one of the key elements in data transmission it prevents third parties from accessing data. encryption methods such as AES, DOS are 3DOS are time taking processes and are mostly preferred for large amount of data. When it comes down to smaller systems implementation of these encryption might not be necessary due to its complexity and requirements. So providing a adaptable and scalable tool is nessecery for smaller devices which do not require a very complex encryption. This brings us the need for simple and quick cipher for short data which can also be scaled for large data by replication.
- **Points of focus:** Secure Data Encryption and Decryption, Flexibility and Adaptability, ensuring encryption standards and high utilization and efficieny.

About the Tool

- Our tool operates on 128-bit data using an 8-bit key, enabling secure encryption and decryption processes.
- **Input:** 128bit data,Key,mode.
- **Output:** Encrypted Data.
- **Key Combinations:** With a total of 256 possible combinations for the 8-bit key, our tool ensures a wide range of encryption possibilities.
- **User-Controlled Mode:** The tool's functionality is dictated by a user-input mode, providing flexibility for encryption or decryption operations.
- The Tool uses 5 modules with multiple components to cipher data.

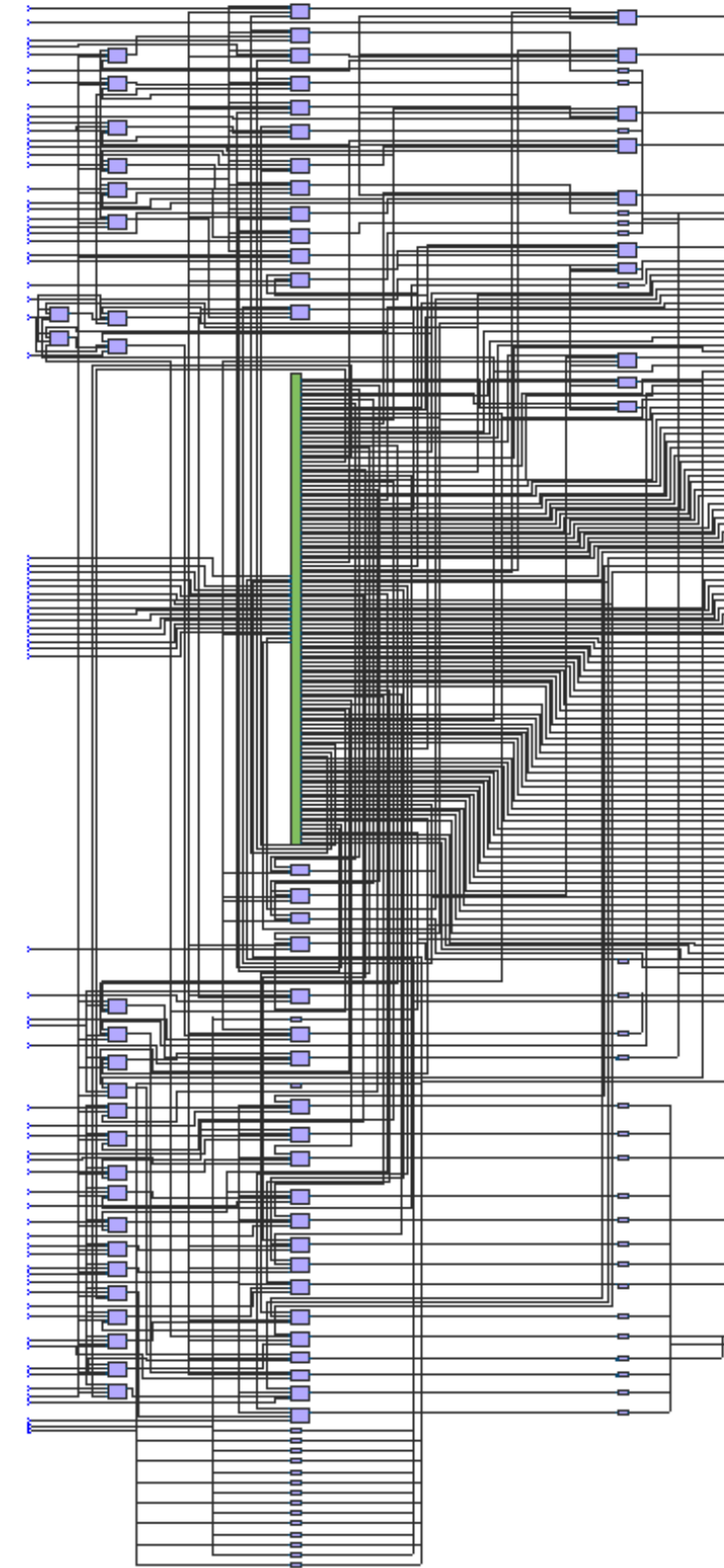
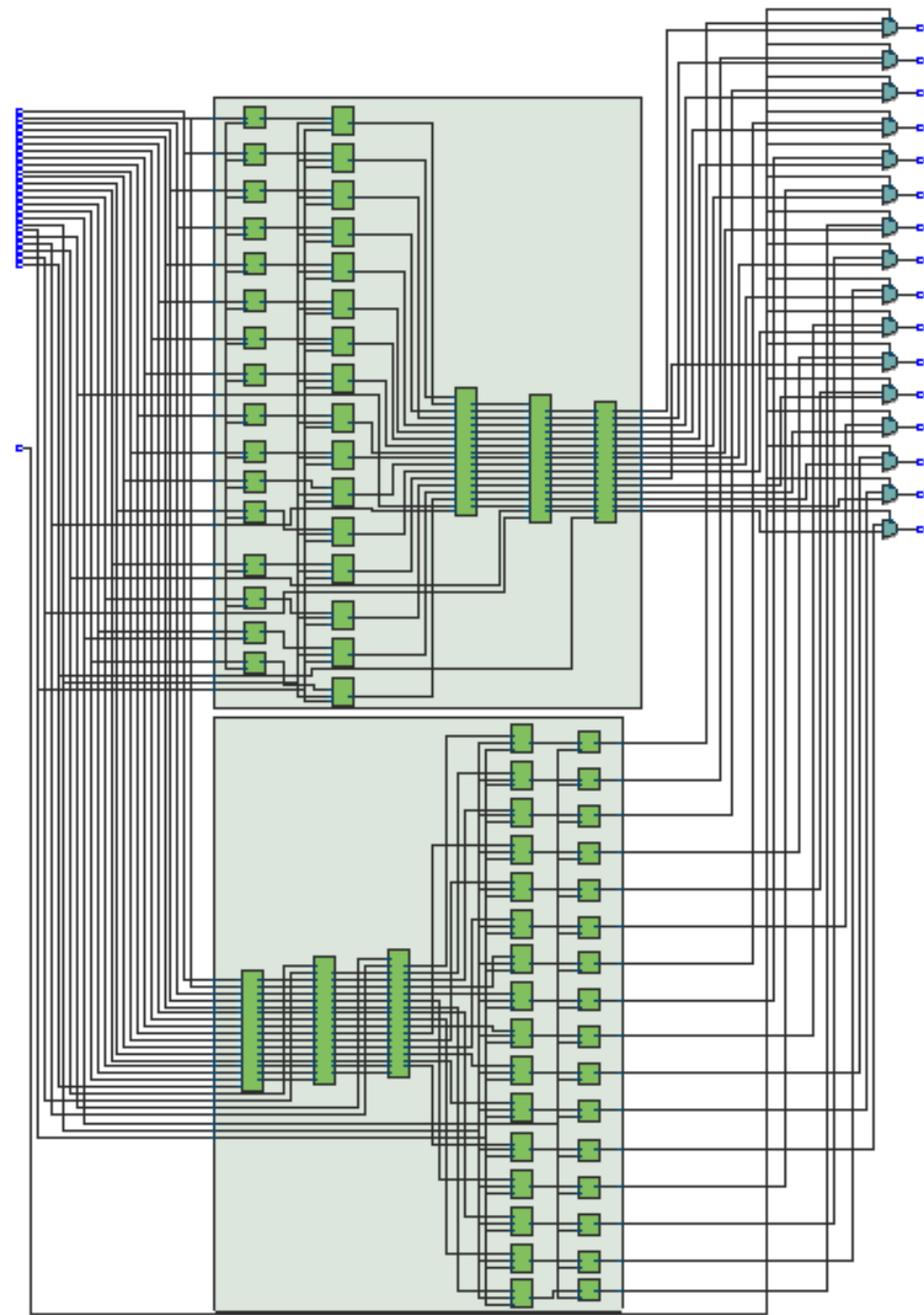


Compilation Report

- The tool uses 2166 Logic Elements & 265 I/O pins.
- Compilation time: 3 mins
- Additionally, it consists of 20 module files.

Top-level Entity Name	Enigma
Family	MAX 10
Device	10M50DAF672C7G
Timing Models	Final
Total logic elements	2,166 / 49,760 (4 %)
Total registers	0
Total pins	265 / 500 (53 %)
Total virtual pins	0
Total memory bits	0 / 1,677,312 (0 %)
Embedded Multiplier 9-bit elements	0 / 288 (0 %)
Total PLLs	0 / 4 (0 %)
UFM blocks	0 / 1 (0 %)
ADC blocks	0 / 2 (0 %)

RTL & Technology Schematic



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Code Snippets

Main Module:

```
1 module Enigma(input logic [7:0]a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,
2   input logic s0,s1,s2,s3,s4,s5,s6,s7,
3   output logic [7:0]w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3,
4   input logic mode
5 );
6 logic [7:0]n0,n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15;
7 logic [7:0]m0,m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15;
8
9 Encryption En1(a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,s0,s1,s2,s3,s4,s5,s6,s7);
10 Decryption De1(a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,s0,s1,s2,s3,s4,s5,s6,s7);
11
12 always@(mode)
13 begin
14   if (mode ==0)
15   begin
16     w0 = n0;w1 = n1;w2 = n2;w3 = n3;
17     x0 = n4;x1 = n5;x2 = n6;x3 = n7;
18     y0 = n8;y1 = n9;y2 = n10;y3 = n11;
19     z0 = n12;z1 = n13;z2 = n14;z3 = n15;
20   end
21   else if(mode ==1)
22   begin
23     w0 = m0;w1 = m1;w2 = m2;w3 = m3;
24     x0 = m4;x1 = m5;x2 = m6;x3 = m7;
25     y0 = m8;y1 = m9;y2 = m10;y3 = m11;
26     z0 = m12;z1 = m13;z2 = m14;z3 = m15;
27   end
28 end
29 endmodule
```

Encryption Module:

```
1 module Encryption(
2   input logic [7:0]a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,
3   input logic s0,s1,s2,s3,s4,s5,s6,s7,
4   output logic [7:0]w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3
5 );
6 logic [7:0]l1,l2,l3,l4,l5,l6,l7,l8,l9,l10,l11,l12,l13,l14,l15,l16;
7
8 DataInv data0(a0,s0,l1);
9 DataInv data1(a1,s0,l2);
10 DataInv data2(a2,s0,l3);
11 DataInv data3(a3,s0,l4);
12 DataInv data4(b0,s0,l5);
13 DataInv data5(b1,s0,l6);
14 DataInv data6(b2,s0,l7);
15 DataInv data7(b3,s0,l8);
16 DataInv data8(c0,s0,l9);
17 DataInv data9(c1,s0,l10);
18 DataInv data10(c2,s0,l11);
19 DataInv data11(c3,s0,l12);
20 DataInv data12(d0,s0,l13);
21 DataInv data13(d1,s0,l14);
22 DataInv data14(d2,s0,l15);
23 DataInv data15(d3,s0,l16);
24
25 logic [7:0]m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15,m16;
26
27 barrel bar0(l1,s1,s2,m1);
28 barrel bar1(l2,s1,s2,m2);
29 barrel bar2(l3,s1,s2,m3);
30 barrel bar3(l4,s1,s2,m4);
31 barrel bar4(l5,s1,s2,m5);
32 barrel bar5(l6,s1,s2,m6);
33 barrel bar6(l7,s1,s2,m7);
34 barrel bar7(l8,s1,s2,m8);
35 barrel bar8(l9,s1,s2,m9);
36 barrel bar9(l10,s1,s2,m10);
37 barrel bar10(l11,s1,s2,m11);
38 barrel bar11(l12,s1,s2,m12);
39 barrel bar12(l13,s1,s2,m13);
40 barrel bar13(l14,s1,s2,m14);
41 barrel bar14(l15,s1,s2,m15);
42 barrel bar15(l16,s1,s2,m16);
43
44 logic [7:0]n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15,n16;
45 Column col1(m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15,m16,s3,s4,n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15,n16);
46
47 logic [7:0]o1,o2,o3,o4,o5,o6,o7,o8,o9,o10,o11,o12,o13,o14,o15,o16;
48
49 Rows r1(n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15,n16,s5,s6,o1,o2,o3,o4,o5,o6,o7,o8,o9,o10,o11,o12,o13,o14,o15,o16);
50
51 Rotate rr1(o1,o2,o3,o4,o5,o6,o7,o8,o9,o10,o11,o12,o13,o14,o15,o16,s7,w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3);
52
53
54 endmodule
```

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Code Snippets

Decryption:

```
1 module Decryption(  
2     input logic [7:0] a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,  
3     input logic s0,s1,s2,s3,s4,s5,s6,s7,  
4     output logic [7:0] w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3  
5 );  
6  
7     logic [7:0] l1,l2,l3,l4,l5,l6,l7,l8,l9,l10,l11,l12,l13,l14,l15,l16;  
8  
9     ReRotate rel(a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,s7,l1,l2,l3,l4,l5,l6,l7,l8,l9,l10,l11,l12,l13,l14,l15,l16);  
10  
11     logic [7:0] m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15,m16;  
12  
13     ReRows rrl(l1,l2,l3,l4,l5,l6,l7,l8,l9,l10,l11,l12,l13,l14,l15,l16,s5,s6,m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15,m16);  
14  
15     logic [7:0] n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15,n16;  
16  
17     ReColumn rcl(m1,m2,m3,m4,m5,m6,m7,m8,m9,m10,m11,m12,m13,m14,m15,m16,s3,s4,n1,n2,n3,n4,n5,n6,n7,n8,n9,n10,n11,n12,n13,n14,n15,n16);  
18  
19     logic [7:0] o1,o2,o3,o4,o5,o6,o7,o8,o9,o10,o11,o12,o13,o14,o15,o16;  
20  
21     Rebarrel bar0(n1,s1,s2,o1);  
22     Rebarrel bar1(n2,s1,s2,o2);  
23     Rebarrel bar2(n3,s1,s2,o3);  
24     Rebarrel bar3(n4,s1,s2,o4);  
25     Rebarrel bar4(n5,s1,s2,o5);  
26     Rebarrel bar5(n6,s1,s2,o6);  
27     Rebarrel bar6(n7,s1,s2,o7);  
28     Rebarrel bar7(n8,s1,s2,o8);  
29     Rebarrel bar8(n9,s1,s2,o9);  
30     Rebarrel bar9(n10,s1,s2,o10);  
31     Rebarrel bar10(n11,s1,s2,o11);  
32     Rebarrel bar11(n12,s1,s2,o12);  
33     Rebarrel bar12(n13,s1,s2,o13);  
34     Rebarrel bar13(n14,s1,s2,o14);  
35     Rebarrel bar14(n15,s1,s2,o15);  
36     Rebarrel bar15(n16,s1,s2,o16);  
37  
38     ReDataInv data0(o1,s0,w0);  
39     ReDataInv data1(o2,s0,w1);  
40     ReDataInv data2(o3,s0,w2);  
41     ReDataInv data3(o4,s0,w3);  
42     ReDataInv data4(o5,s0,x0);  
43     ReDataInv data5(o6,s0,x1);  
44     ReDataInv data6(o7,s0,x2);  
45     ReDataInv data7(o8,s0,x3);  
46     ReDataInv data8(o9,s0,y0);  
47     ReDataInv data9(o10,s0,y1);  
48     ReDataInv data10(o11,s0,y2);  
49     ReDataInv data11(o12,s0,y3);  
50     ReDataInv data12(o13,s0,z0);  
51     ReDataInv data13(o14,s0,z1);  
52     ReDataInv data14(o15,s0,z2);  
53     ReDataInv data15(o16,s0,z3);  
54  
55     endmodule
```

Data Encryption1:

```
1 module DataInv(  
2     input logic [7:0] a,  
3     input logic s0,  
4     output logic [7:0] w  
5 );  
6  
7     logic [7:0] out;  
8     Subtractor s1(a, out);  
9  
10    always @(s0)  
11    begin  
12        if (s0 == 0)  
13        begin  
14            w <= ~a;  
15        end  
16        else if (s0 == 1)  
17        begin  
18            w <= out;  
19        end  
20    end  
21  
22    endmodule  
23
```

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Code Snippets

Data Encryption 2:

```
1 module barrel(  
2   input logic [0:7] a,  
3   input logic s1,s2,  
4   output [0:7] d  
5 );  
6  
7   mux4(a[6],a[2],a[2],a[4],s1,s2,d[0]);  
8   mux4(a[7],a[3],a[3],a[5],s1,s2,d[1]);  
9   mux4(a[0],a[4],a[0],a[6],s1,s2,d[2]);  
10  mux4(a[1],a[5],a[1],a[7],s1,s2,d[3]);  
11  mux4(a[2],a[6],a[6],a[0],s1,s2,d[4]);  
12  mux4(a[3],a[7],a[7],a[1],s1,s2,d[5]);  
13  mux4(a[4],a[0],a[4],a[2],s1,s2,d[6]);  
14  mux4(a[5],a[1],a[5],a[3],s1,s2,d[7]);  
15  
16 endmodule
```

Level 1: Rearrangement Module:

```
1 module Column(  
2   input logic [7:0] a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,  
3   input logic s3,s4,  
4   output logic [7:0]w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3  
5 );  
6  
7   always @(s3 or s4)  
8   begin  
9     if(s3 == 0)  
10    begin  
11      if(s4 == 0)  
12      begin  
13        w0 = a2;  
14        w1 = a1;  
15        w2 = a0;  
16        w3 = a3;  
17  
18        x0 = b2;  
19        x1 = b1;  
20        x3 = b0;  
21        x2 = b3;  
22  
23        y0 = c2;  
24        y1 = c1;  
25        y2 = c0;  
26        y3 = c3;  
27  
28        z0 = d2;  
29        z1 = d1;  
30        z2 = d0;  
31        z3 = d3;  
32      end  
33    else if(s4 == 1)  
34    begin  
35      w0 = a1;  
36      w1 = a0;
```


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Code Snippets

Level 2: Rearrangement Module:

```
1 module Rows(  
2   input logic [7:0] a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,  
3   input logic s3,s4,  
4   output logic [7:0] w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3  
5 );  
6  
7 always @(s3 or s4)  
8   begin  
9     if(s3 == 0)  
10      begin  
11        if(s4 == 0)  
12          begin  
13            w0 = c0;  
14            x0 = b0;  
15            y0 = a0;  
16            z0 = d0;  
17  
18            w1 = c1;  
19            x1 = b1;  
20            y1 = a1;  
21            z1 = d1;  
22  
23            w2 = c2;  
24            x2 = b2;  
25            y2 = a2;  
26            z2 = d2;  
27  
28            w3 = c3;  
29            x3 = b3;  
30            y3 = a3;  
31            z3 = d3;  
32          end  
33        else if(s4 == 1)  
34          begin  
35            w0 = a0;  
36            x0 = d0;  
37            y0 = c0;  
38            z0 = b0;  
39  
40            w1 = a1;  
41            x1 = d1;  
42            y1 = c1;  
43            z1 = b1;  
44  
45            w2 = a2;  
46            x2 = d2;  
47            y2 = c2;  
48            z2 = b2;  
49  
50            w3 = a3;  
51            x3 = d3;  
52            y3 = c3;  
53            z3 = b3;  
54          end  
55        end  
56      end  
57    end  
58  end
```

Level 3: Rearrangement Module:

```
1 module Rows(  
2   input logic [7:0] a0,a1,a2,a3,b0,b1,b2,b3,c0,c1,c2,c3,d0,d1,d2,d3,  
3   input logic s7,  
4   output logic [7:0] w0,w1,w2,w3,x0,x1,x2,x3,y0,y1,y2,y3,z0,z1,z2,z3  
5 );  
6  
7 always @(s7)  
8   begin  
9     if(s7 == 0)  
10      begin  
11        w0 = a3;  
12        w1 = b3;  
13        w2 = c3;  
14        w3 = d3;  
15  
16        x0 = a2;  
17        x1 = b1;  
18        x2 = b2;  
19        x3 = d2;  
20  
21        y0 = a1;  
22        y1 = c1;  
23        y2 = c2;  
24        y3 = d1;  
25  
26        z0 = a0;  
27        z1 = b0;  
28        z2 = c0;  
29        z3 = d0;  
30      end  
31    else if (s7 == 1)  
32      begin//rotate right  
33        w0 = d0;  
34        w1 = c0;  
35        w2 = b0;  
36        w3 = a0;  
37  
38        x0 = d1;  
39        x1 = b1;  
40        x2 = b2;  
41        x3 = a1;  
42  
43        y0 = d2;  
44        y1 = c1;  
45        y2 = c2;  
46        y3 = a2;  
47  
48        z0 = d3;  
49        z1 = c3;  
50        z2 = b3;  
51        z3 = a3;  
52      end  
53    end  
54  end
```

Encryption

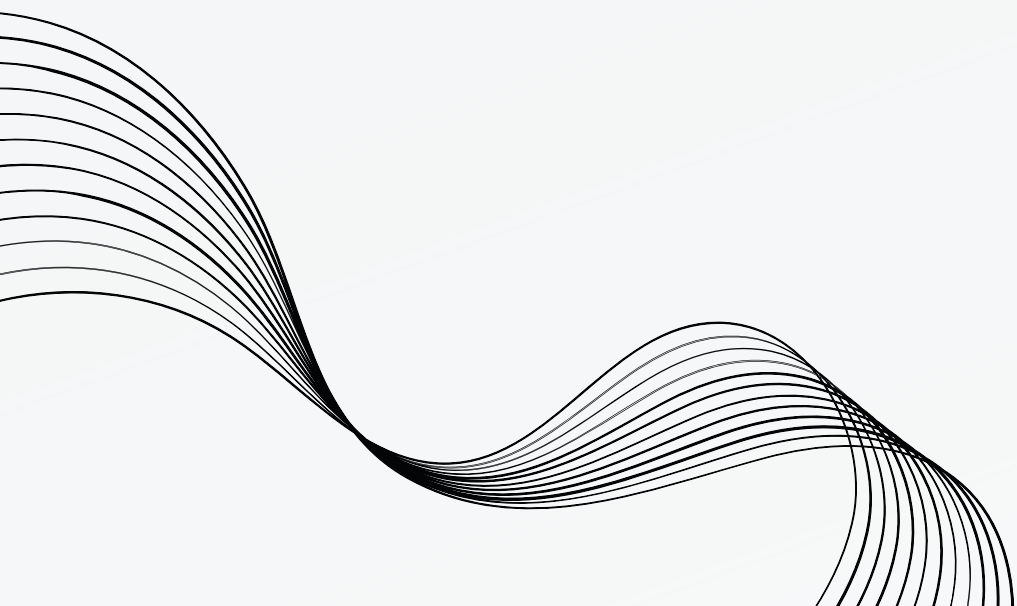
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Data Encryption: The Data consist of 16 bytes. Where each byte carries a character. Based on the first bit of key the given 8 bit data is either inverted or decremented.

Data Shifter: The 8 bit data after inverting or decrementing is rotated to the right or left to further encrypt the data based on 2nd & 3rd bit of key.

Data Visualization: Given 16 byte data is visualized in the form of 4x4 matrix for shuffling the order.

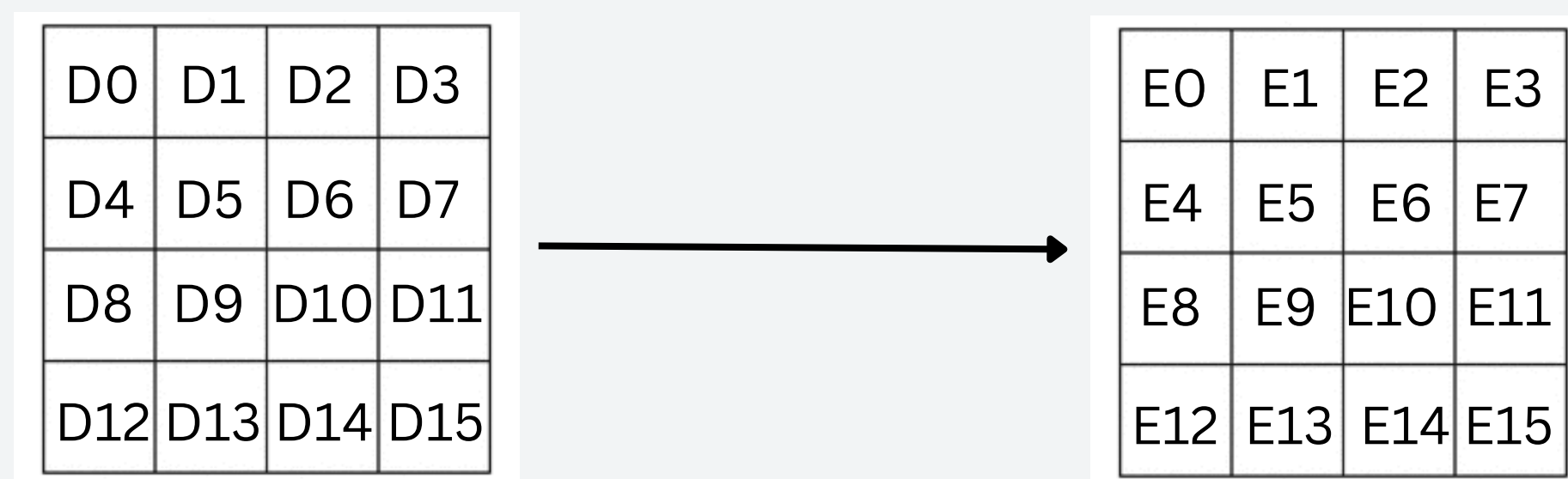
D0	D1	D2	D3
D4	D5	D6	D7
D8	D9	D10	D11
D12	D13	D14	D15



Rearrangement

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- Level 1: In 4x4 Visualization, the columns of the matrix are rearranged in multiple patterns based on 4th and 5th bit of key.
- Level 2: The Rows of the matrix are rearranged. based on 6th and 7th bit of the key.
- Level 3: The center 4 elements hold their position and the outermost layer is rotated to the right or left based on 8th bit of the key.
- The Data after Level 3 is given to output.
- The Encryption Process contains 5 Levels including Data Manipulation.



Decryption

CLASSIFIED

- Decryption Process is the Inversion of Encryption Process.
- The Decryption Process starts with the Level 3 which is Rotating the matrix.
- Then Data's rows are shifted to the initial state.
- Then Columns are shifted based on the same key sequence.
- The 8-bit element is rotated as an inverse operation of encryption.
- Then as final Level the byte is either inverted again or incremented based on the 1st bit key.
- After final bit operation the Decrypted data is fed as output



Project Dynamics: Encryption

Testbench Input data “CRYPTOGRAPHICSYS”

```
23 begin
24     #10//CRYPTOGRAPHICSYS
25     A0  = 8'b01000011;
26     A1  = 8'b01010010;
27     A2  = 8'b01011001;
28     A3  = 8'b01010000;
29     A4  = 8'b01010100;
30     A5  = 8'b01001111;
31     A6  = 8'b01000111;
32     A7  = 8'b01010010;
33     A8  = 8'b01000001;
34     A9  = 8'b01010000;
35     A10 = 8'b01001000;
36     A11 = 8'b01001001;
37     A12 = 8'b01000011;
38     A13 = 8'b01010011;
39     A14 = 8'b01011001;
40     A15 = 8'b01010011;
41
42     mode = 1'b0;
43     S0  = 1'b1;
44     S1  = 1'b1;
45     S2  = 1'b1;
46     S3  = 1'b1;
47     S4  = 1'b0;
48     S5  = 1'b0;
49     S6  = 1'b0;
50     S7  = 1'b0;
```

Output: \$\$5%?d? ?t%???

```
GIVEN DATA : 01000011 01010010 01011001 01010000 01010100 01001111 01000111 01010010 01000001 01010000 01001000 01001001 01000011 01010011 01011001 01010011
ENCRYPTED DATA : 00100100 00100100 00110101 00000100 00100101 00010101 01100100 10000100 10000101 11110100 10000101 01110100 00100101 00010101 11100100 11110100
```

Project Dynamics: Decryption

Testbench Input “\$\$5%?d? ?t%???”

```
54      A0 = D0;
55      A1 = D1;
56      A2 = D2;
57      A3 = D3;
58      A4 = D4;
59      A5 = D5;
60      A6 = D6;
61      A7 = D7;
62      A8 = D8;
63      A9 = D9;
64      A10 = D10;
65      A11 = D11;
66      A12 = D12;
67      A13 = D13;
68      A14 = D14;
69      A15 = D15;
```

```
70      mode = 1'b1;
```

Output: “CRYPTOGRAPHICSYS”

```
# GIVEN DATA : 00100100 00100100 00110101 00000100 00100101 00010101 01100100 10000100 10000101 11110100 10000101 01110100 00100101 00010101 11100100 11110100
# DECRYPTED DATA : 01000011 01010010 01011001 01010000 01010100 01001111 01000111 01010010 01000001 01010000 01001000 01001001 01000011 01010011 01011001 01010011
```

Project Dynamics: Encryption

Testbench Input data “CRYPTOGRAPHICSYS”

```
23 begin
24     #10//CRYPTOGRAPHICSYS
25     A0 = 8'b01000011;
26     A1 = 8'b01010010;
27     A2 = 8'b01011001;
28     A3 = 8'b01010000;
29     A4 = 8'b01010100;
30     A5 = 8'b01001111;
31     A6 = 8'b01000111;
32     A7 = 8'b01010010;
33     A8 = 8'b01000001;
34     A9 = 8'b01010000;
35     A10 = 8'b01001000;
36     A11 = 8'b01001001;
37     A12 = 8'b01000011;
38     A13 = 8'b01010011;
39     A14 = 8'b01011001;
40     A15 = 8'b01010011;
```

```
92     mode = 1'b0;
93     S0 = 1'b0;
94     S1 = 1'b1;
95     S2 = 1'b1;
96     S3 = 1'b1;
97     S4 = 1'b0;
98     S5 = 1'b0;
99     S6 = 1'b0;
100    S7 = 1'b0;
```

Output: ËËºëÊÚ;:ú:{

```
# GIVEN DATA : 01000011 01010010 01011001 01010000 01010100 01001111 01000111 01010010 01000001 01010000 01001000 01001001 01000011 01010011 01011001 01010011
# ENCRYPTED DATA : 11001011 11001011 10111010 11101011 11001010 11011010 10001011 01101011 01101010 11111010 01101010 01111011 11001010 11011010 00001011 11111010
```

Project Dynamics: Decryption

Testbench Input “ËËºëÊÚ;:ú:{”

```
54      A0 = D0;
55      A1 = D1;
56      A2 = D2;
57      A3 = D3;
58      A4 = D4;
59      A5 = D5;
60      A6 = D6;
61      A7 = D7;
62      A8 = D8;
63      A9 = D9;
64      A10 = D10;
65      A11 = D11;
66      A12 = D12;
67      A13 = D13;
68      A14 = D14;
69      A15 = D15;

70      mode = 1'b1;
```

Output: “CRYPTOGRAPHICSYS”

```
# GIVEN DATA : 11001011 11001011 10111010 11101011 11001010 11011010 10001011 01101011 01101010 11111010 01101010 01111011 11001010 11011010 00001011 11111010
# DECRYPTED DATA : 01000011 01010010 01011001 01010000 01010100 01001111 01000111 01010010 01000001 01010000 01001000 01001001 01000011 01010011 01011001 01010011
```


Conclusion

- **Challenges Faced:** Debugging was more complex compared to building the module. Decrypted and Encrypted data are not in a fixed state, therefore making it hard to find the exact point of error. Therefore to debug we had to manually go through each module and verify.
- **Future Expansion:** Looking ahead, we will be focusing on better efficiency and utilization of the system. We are trying to use the same module for encryption and decryption of data, which would bring the logic elements below 1000.
- **Learnings & Growth:** The project journey provided invaluable insights into cryptography, SystemVerilog, and project management.

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THANK YOU

*Transforming vulnerability into
invincibility*

