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SYLLING STRUCTURES

Seat No.: 4AIOSEE038

F of 056452

BE Eighth Semester (Electrical & Electronics Engineering) KARNATAKA, INDIA STATEMENT OF MARKS

Name of the Student SANJEEV KUMAR

Father's / Mother's Name RAJENDRA SINGH

Name of the College ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY, CHICKMAGALUR

_	0	(C)	4	ω	N	-	No.	SI.
	EE86	EE85	EE847	EE832	EE82	EE81	Code	
	Seminar on Project Work	Project Work	Electromagnetic Compatibility	Al & ANN Applications in Power System	Industrial Drives & Application	Ind. Mgmt, Electrical Estimation & Economics	Title	Subject
		100	8	8	1 00	100	Max	Exam
	,	8	33	35	쓩	쓩	Min	Examination Marks
		8	2	8	5	8	Obtained	Marks
	8	100	8	Bi	33	R	Max	Internal
	8	8			•		Min	Assessm
	49	88	2	23	8	8	Obtained	Internal Assessment Marks
	8	200	3	3	23	23	Max	
	8	8	8	8	8	8	Min	Total Marks
-	\$	188	8	2	88	83	Obtained	rks
	80 mr	60 INF	80 mr	90 mr	60 Inf	60 ITT	Passing	Month &

*First Attempt Total (in words) : FIVE HUNDRED AND NINETY ONLY

GRAND TOTAL

750

310

Result of the Semester FIRST CLASS WITH DISTINCTION

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Semesters	<	≤	≦	≅	VTOVII
Marks at First Attempt	473	510	515	590	2088
Total Marks (Max.)	900	900	776	750	3325
Result	SC	SC	낁	FCD	ਨ

First Attempt Total (V to VIII Sem) in words : TWO THOUSAND EIGHTY EIGHT ONLY

Result of the Degree

Date AUG 31,2009

FIRST CLASS



Contrary

Registrar (Evaluation)