



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET

B.Tech. COMPUTER SCIENCE & ENGINEERING

CMM. No. : **c 0701554**

Serial No. : 21180012941

Name : **SHIBANGI NANDA**

Hall Ticket No. : **12D01A05F7**



Name of the College : **D0-S M C E T, DESHMUKHI**

Month & Year of Final Exam : **May, 2016**

Class Awarded : **FIRST CLASS**

Year of Admission : **2012-2013**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	22	40	62	4	2	MATHEMATICS - I	22	26	48	6
3	MATHEMATICAL METHODS	23	26	49	6	4	ENGINEERING PHYSICS	22	30	52	4
5	ENGINEERING CHEMISTRY	24	26	50	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	23	51	74	6
7	ENGINEERING DRAWING	23	26	49	4	8	COMPUTER PROGRAMMING LAB	24	46	70	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	24	49	73	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	24	45	69	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	24	49	73	4						
I SEMESTER											
II YEAR											
I SEMESTER											
1	PROBABILITY AND STATISTICS	21	29	50	3	1	COMPUTER ORGANIZATION	14	30	44*	4
2	MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE	20	36	56	3	2	DATA BASE MANAGEMENT SYSTEMS	18	45	63	4
3	DATA STRUCTURES THROUGH C++	23	26	49	4	3	OBJECT ORIENTED PROGRAMMING	19	26	45	4
4	DIGITAL LOGIC DESIGN	22	47	69	4	4	ENVIRONMENTAL STUDIES	18	45	63	3
5	ELECTRONIC DEVICES & CIRCUITS	21	27	48	4	5	FORMAL LANGUAGES & AUTOMATA THEORY	20	41	61	3
6	BASIC ELECTRICAL ENGG	21	26	47	3	6	DESIGN & ANALYSIS OF ALGORITHMS	15	51	66	3
7	ELECTRICAL AND ELECTRONICS LAB	21	48	69	2	7	OBJECT ORIENTED PROGRAMMING LAB	18	42	60	2
8	DATA STRUCTURES LAB THROUGH C++	22	47	69	2	8	DATA BASE MANAGEMENT SYSTEMS LAB	21	45	66	2
II SEMESTER											
III YEAR											
I SEMESTER											
1	PRINCIPLES OF PROGRAMMING LANGUAGES	21	38	59	4	1	OBJECT ORIENTED ANALYSIS AND DESIGN	18	36	54	4
2	OPERATIONS RESEARCH	23	26	49	3	2	VLSI DESIGN	22	31	53	3
3	SOFTWARE ENGINEERING	17	26	43*	3	3	NETWORK SECURITY	20	32	52	3
4	MICROPROCESSORS & INTERFACING	15	32	47	3	4	COMPILER DESIGN	14	35	49	4
5	OPERATING SYSTEMS	21	48	69	4	5	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	17	42	59	4
6	DATA COMMUNICATION & COMPUTER NETWORKS	17	28	45	4	6	WEB TECHNOLOGIES	17	32	49	3
7	MICROPROCESSORS & INTERFACING (LAB)	18	46	64	2	7	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	22	48	70	2
8	COMPUTER NETWORKS & OPERATING SYSTEMS (LAB)	21	49	70	2	8	WEB TECHNOLOGIES & COMPILER DESIGN (LAB)	23	47	70	2
II SEMESTER											
IV YEAR											
I SEMESTER											
1	LINUX PROGRAMMING	16	35	51	4	1	MANAGEMENT SCIENCE	21	26	47	3
2	SOFTWARE TESTING METHODOLOGIES	15	33	48	4	2	WEB SERVICES	20	34	54	3
3	DATA WAREHOUSING AND DATA MINING	20	42	62	4	3	DATABASE SECURITY	22	29	51	3
4	COMPUTER GRAPHICS	15	39	54	3	4	INDUSTRY ORIENTED MINI PROJECT	-	40	40	2
5	CLOUD COMPUTING	19	51	70	3	5	SEMINAR	47	-	47	2
6	DESIGN PATTERNS	15	33	48	3	6	PROJECT WORK	47	130	177	10
7	LINUX PROGRAMMING AND DATA MINING LAB	23	47	70	2	7	COMPREHENSIVE VIVA	-	90	90	2
8	CASE TOOLS AND SOFTWARE TESTING LAB	24	45	69	2						
II SEMESTER											
(# Project Internal= 50 , External=150)											

Number of Credits registered for : 200 Aggregate Marks Secured for best: 193

Aggregate Marks Secured **3387 OUT OF 5250 (64.51%)**
June 22, 2016

Date of Issue :

(see overleaf for Rules concerned to award of class)
A indicates 'ABSENT'



(*Courses registered but not counted for calculation of aggregate)

Aravind Singh

CONTROLLER OF EXAMINATIONS