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1. One Flip Flop can store **one** bits of digital information.
2. A decoder has n outputs, so number of inputs will be **$\sqrt{n}$**.
3. 3-bit Johnson counter has modulous = **6**
4. -20 in 1's complement form can be represented in binary as **$(101011)_2$**.
5. 98 in Excess -3 code can be represented as **$(1100\ 1011)_2$**.
6. A six-bit asynchronous counter has modulous = **64**.
7. Number of clock pulses required to write 4-bit data synchronously into PISO register is **4**.
8. The frequency of clock applied at the input of Mod-16 asynchronous counter is 32KHz. The frequency of waveform obtained at MSB output stage is **2KHz**.

9. 16V is to be represented in digital word of 4-bits. The step of each interval is **1** and resolution of ADC is **1/16**.
10. The minimum no. of NAND gates required to realise XOR gate is **four**.