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· The design of a CPU is optimized for sequential

ode performance.

The makes use of sophisticated control logic to allow instructions from a single thread of execution to execute in parallel.

· The large cache memories are provided to rective reduce the instruction and data access latencies of large complex applications - latency - oriented design

· Memory bandwidth limit the speed of applications by limiting the rate at which data can be delivered from the memory system to processors.

whereas Crpv design principle!—

The design philosophy of the apus is shaped by
the fast growing video game industry, which
requires the ability to perform a massive number
of floating-point calculations per video frame.

· apu performs well by executing massive numbers of threads.

· all hardware takes advantage of a large number of execution threads to find work to do when some of them are waiting for long-latery memory accesses.

· Small cache memories are provided to help the bandwidth requirements of applications, so multiple threads that access the same memory

data need not always access the DRAM-> throughput - oriented design most apps will use both cpus & apus executing the sequential parts on the CPU & numerically intensive parts on the GPUs. 2) #include < stallib h> # include < stdio h> #include < mpi.h> int main (int arge, char *argu [3) int size, rank, mat [4][4]; int *arr; int n, cor [43] mel_init (2 argc, Largy) mf1_Comm rank (mpl comm world, brank); mpl_comm_size(mpl_comm_world, & size); if (rank = =0) print("Enter size of 1D array: "). scanf ("%d", 2n); arr = malloc(n * sized(int)); printf(" Enter value for ID array: \n"); for (intizo) (an; itt) scart ("%d9, LarrEiz); printf (" Enter value for 2D matrix: 'h"); Por (int 1 = 0; 1<4; 1+4) for Lint (1=0) (<4) (th) scant ("%d", smot [i3G]); n=n/4°, int brr[4]; Int pain

```
MPI Scatter Carr, n, MPLINT, bro, n, MPLINT, o
          MPI_COMM_WORLD);
int min = box [0];
for (int int; ixn; ita)
    if (min > bor [i])
         min = bro [1];
MPL Scatter ( mat, 4, mPLINT, crr, 4, MPLINT,
         O, MPI_COMM_COORLD);
int max = correct;
for (int (=1; (24; itt)
   if Lmax 4 corti2)
       max = cor[1];
printf ("Rank = %d, minimum = %d", rank, min)
print [ " minimum in array + maximum in
       column %d in rank %d = %d",
        rank, rank, (min + max));
MPI-finalize();
return o;
```