## TarangAI — Technical Slides

## Responsibilities

- Transport and delivery of memory bundles.
- Routing and buffering.
- Delivery guarantees.

## **Components**

- Stream router.
- Buffer manager.
- Audit hooks for FluxMind.

## **Architecture**

flowchart LR

Tools --> Buf[Buffer] --> Router[TarangAl]

Router --> NV[Ninaivalaigal+eM]

Router --> SM[SmritiOS]

Router --> PR[Pragna]

Router --> FM[FluxMind]