**ECE 212 Lab - Introduction to Microprocessors**

**Department of Electrical and Computer Engineering**

**University of Alberta**

**Lab 1: Introduction to Assembly Language.**

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| --- | --- |
| **Student Name** | **Student ID** |
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|  |  |

**Submission Date: 12 February, 2018 (For Section H11)**

**13 February, 2018 (For Section H21)**

**14 February, 2018 (For Section H31)**

**15 February, 2018 (For Section H40)**

**15 February, 2018 (For Section H41)**

**Please submit to assignment box (Before 4.00 pm)**

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**Introduction**

<Write an introduction >

< describe about the lab1 in brief.>

<>

**Design**

Part A

<Explain how did you design your program – Part A>

<Flow diagram: need not to be precise, show your work, you can write on a paper and take a picture and paste here or attach to appendix >

<A sample calculation of conversion, explain here or in appendix>

Part B

<Explain how did you design your program – Part B>

<Flow diagram: need not to be precise, show your work, you can write on a paper and take a picture and paste here or attach to appendix >

<A sample calculation of conversion, explain here or in appendix>

**Testing**

Part A

<Explain how did you test your program – Part A>

<Screenshot of your MTTY , put here or attach to appendix>

Part B

<Explain how did you test your program – Part B>

<Screenshot of your MTTY, put here or attach to appendix >

**Questions (10 points)**

1. *What happens when there is no exit code ‘0x0D’ provided in the initialization process? Would it cause a problem? Why or why not?*

**Answer: <Write your answer >**

1. *How can our code be modified to provide a variable address range? For example, what if I only wanted to convert the first 10 data entires?*

**Answer: <Write your answer >**

# Conclusion (10 points)

**<Write a conclusion >**

**<Did you find any problem? If so how did you solve it. >**

# Appendix

Part A Assembler Code

**<paste text from your lab1a.s>**

**<each line of code should have comment>**

Part B Assembler Code

**<paste text from your lab1b.s>**

**<each line of code should have comment>**