

ECE 315 Assignment 1

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The system will enter any of the low power modes below when a **STOP** instruction is executed. Depending on the **WCR[LPMD]** bits A wake-up event can be any type or reset, or any valid, enabled interrupt request

1.0.1 wait

Wait mode saves power by stopping the CPU and memory clocks until a wake-up event is detected. Peripherals can be programmed to continue operating, and can also generate interrupts which will cause the CPU to exit from wait mode. The wait mode is entered by the **STOP** inst

1.0.2 doze

Doze mode saves power by
The doze mode is entered by
To exit doze mode,

1.0.3 stop

Stop mode saves power by
The stop mode is entered by
To exit stop mode,

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