Information about EECE.4800/5520 Exam 2 (Final)
Check SiS for exam time and location

120-minute exam (6:30pm-8:30pm).

Similar format as Exam 1
- Multiple Choices (Type A): ~5 questions, 4 points each
- Multiple Choices (Type B): ~5 questions, 4 points each
- Short answer questions: ~5 questions, 60 points

Coverage (For review only. Not an exact or complete list)
- DMA: Signals involved in DMA, purpose/benefits of DMA
  Pages 4-7 in DMAbrief.pdf

- PCI/PCIe: features, configuration space, PCI bus in a computer system, identification of a PCI device
  Pages 3-6, 11-15 in PCIbus.pdf

- I^2C bus: hardware architecture, addressing, read/write transactions,
  Pages 1-12, 28-35 in I2C.pdf and DS1307 datasheet

- Parallel and multithreaded programming: multithreaded programming, design methodology, Amdahl's law of speedup,
  Pages 1-9, 22-26, 29-33 in MultiCoreProgramming.pdf

- Linux: Linux features
  Pages 3-7 in LinuxkernelslidesFreeElectrons.pdf

- Embedded Bootloader
  Pages 8-11 in Introduction to Embedded Bootloader.pdf

- Emerging technologies
  EmergingTech.pdf