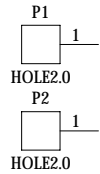
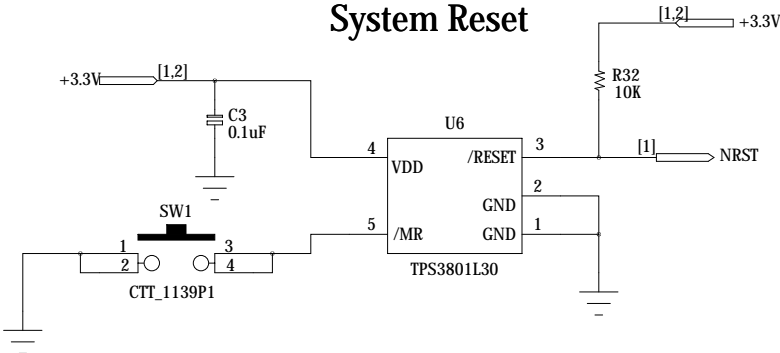


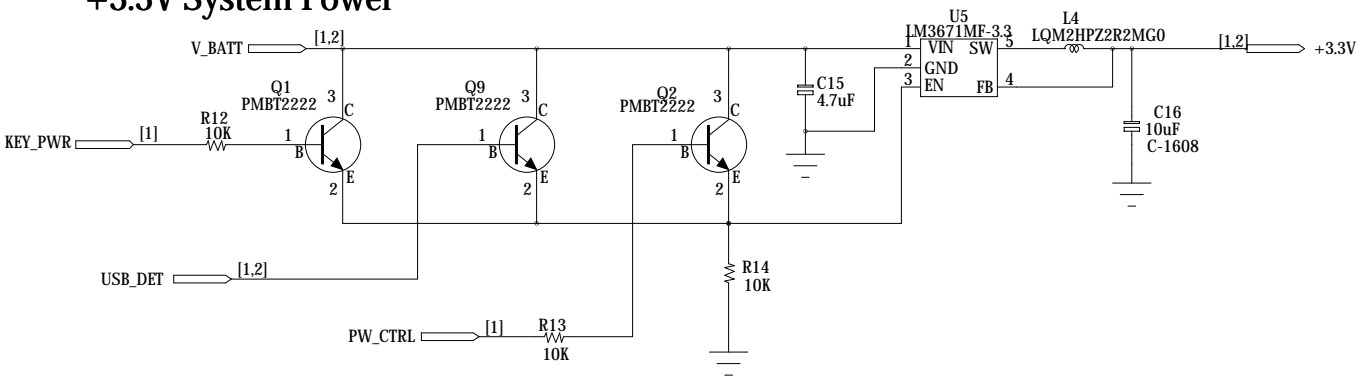
PCB HOLE 4ea



System Reset

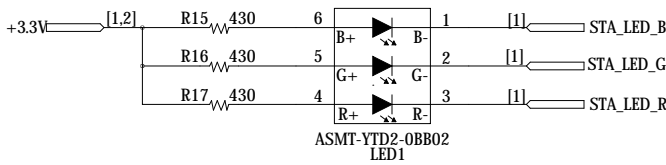
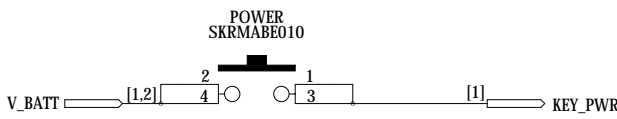


+3.3V System Power

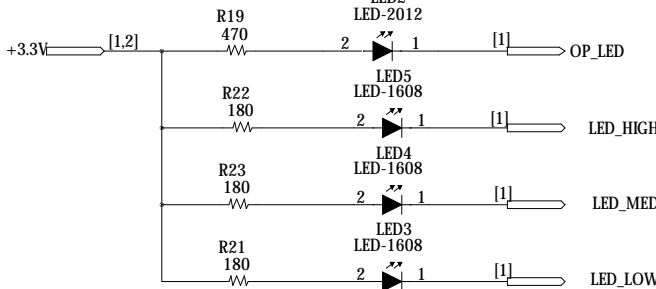
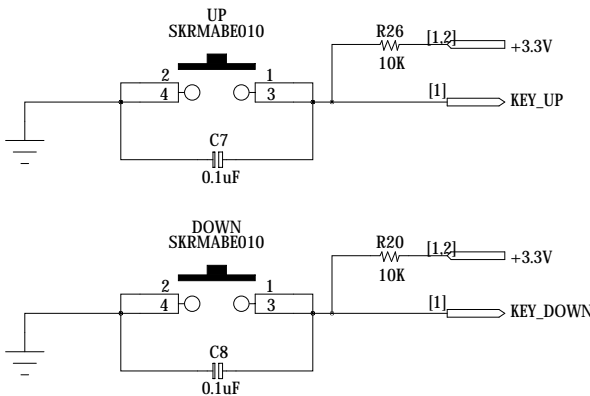


Status LED

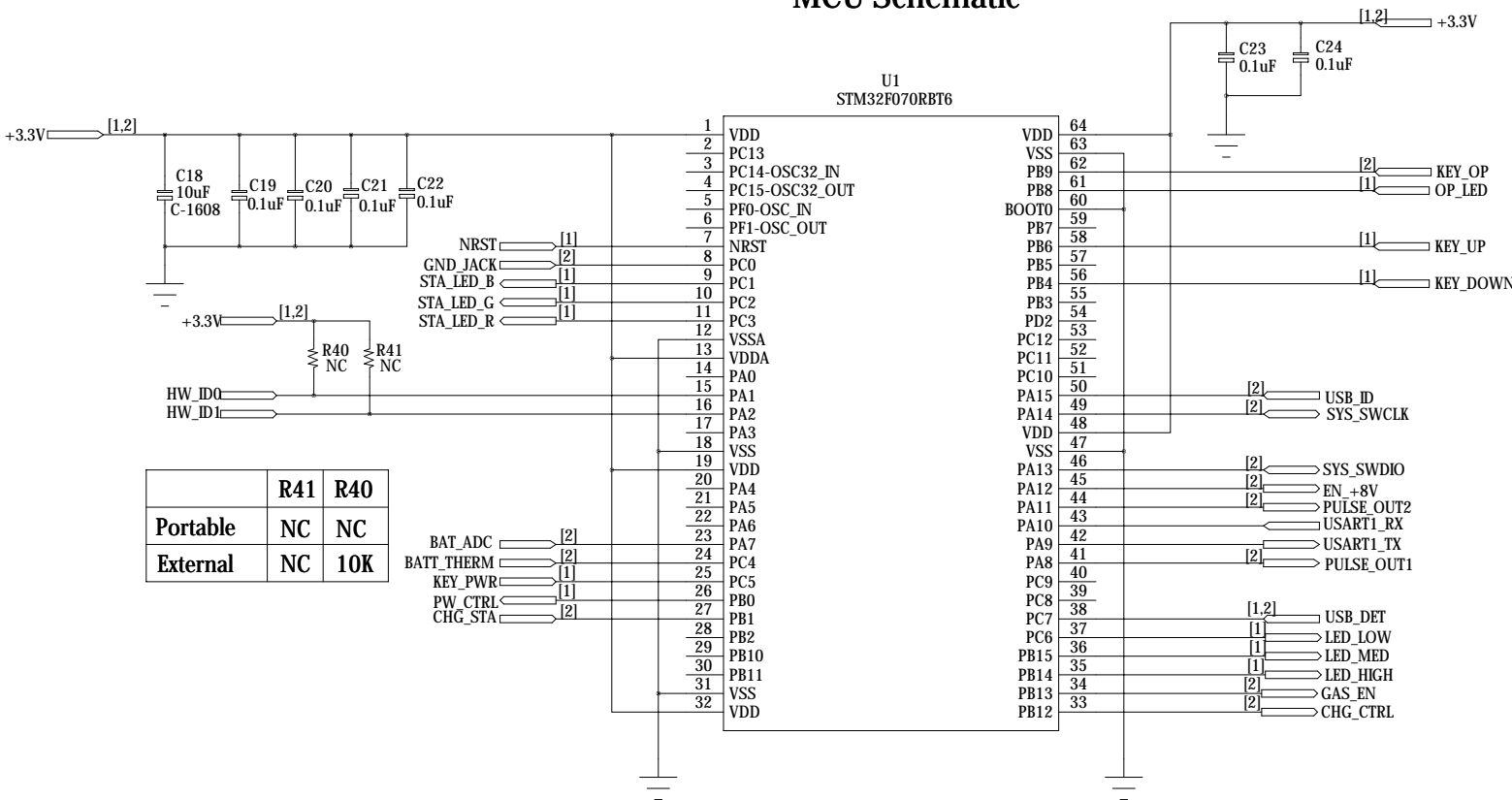
Power Key



Plasma On LED

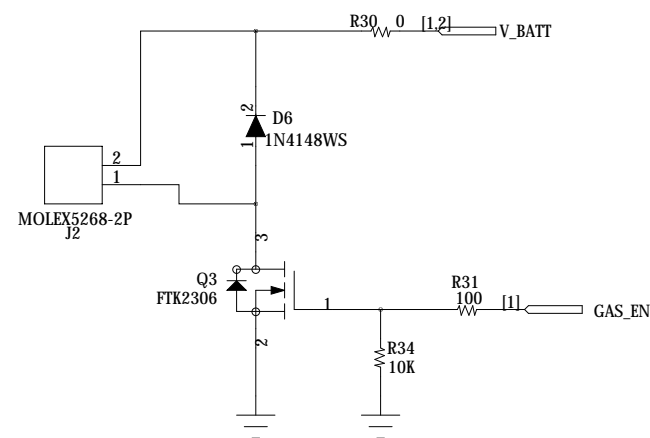
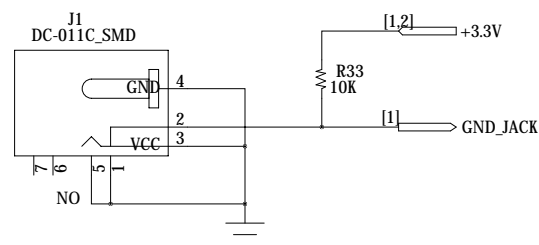


MCU Schematic



DRAWN By:	Company Name: Femto Science Inc.	
CHECKED By:	Model Name: Plasma Pipette Main	
DATE: 2019-06-10	Sheet Name: MCU	
Revision: V5.1	TEAM: HW Team	PAGE: 1 / 2

The schematic diagram shows the internal circuitry of the BATT module. It features a common ground line connected to pins 1 and 2 of the module. Pin 2 is also connected to a 10uF capacitor (C1, C-1608) to ground. A 10K resistor (R1) is connected in series with a 4.7uF capacitor (C2) to ground. A 1.2V voltage source (V_BATT) is connected in series with a 1.2V voltage source (BATT_THERM). The output of the 1.2V source is labeled +3.3V.



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