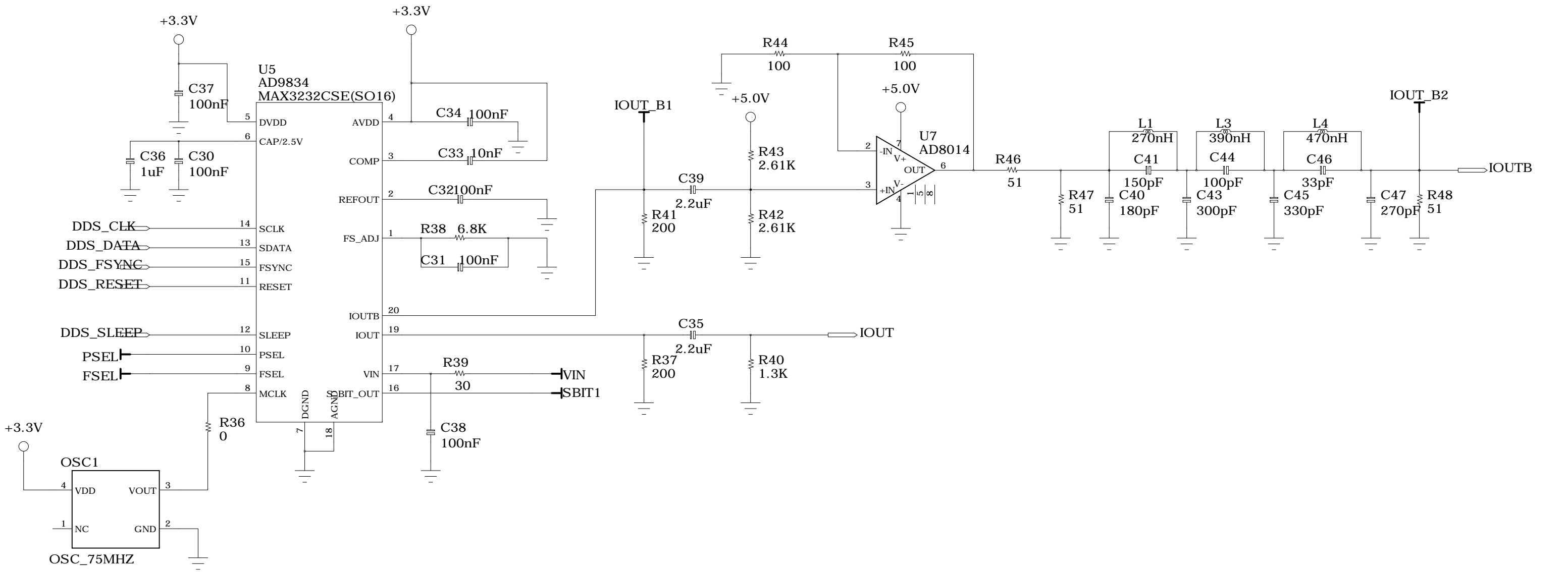
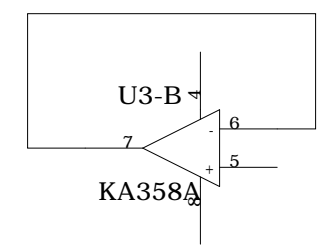
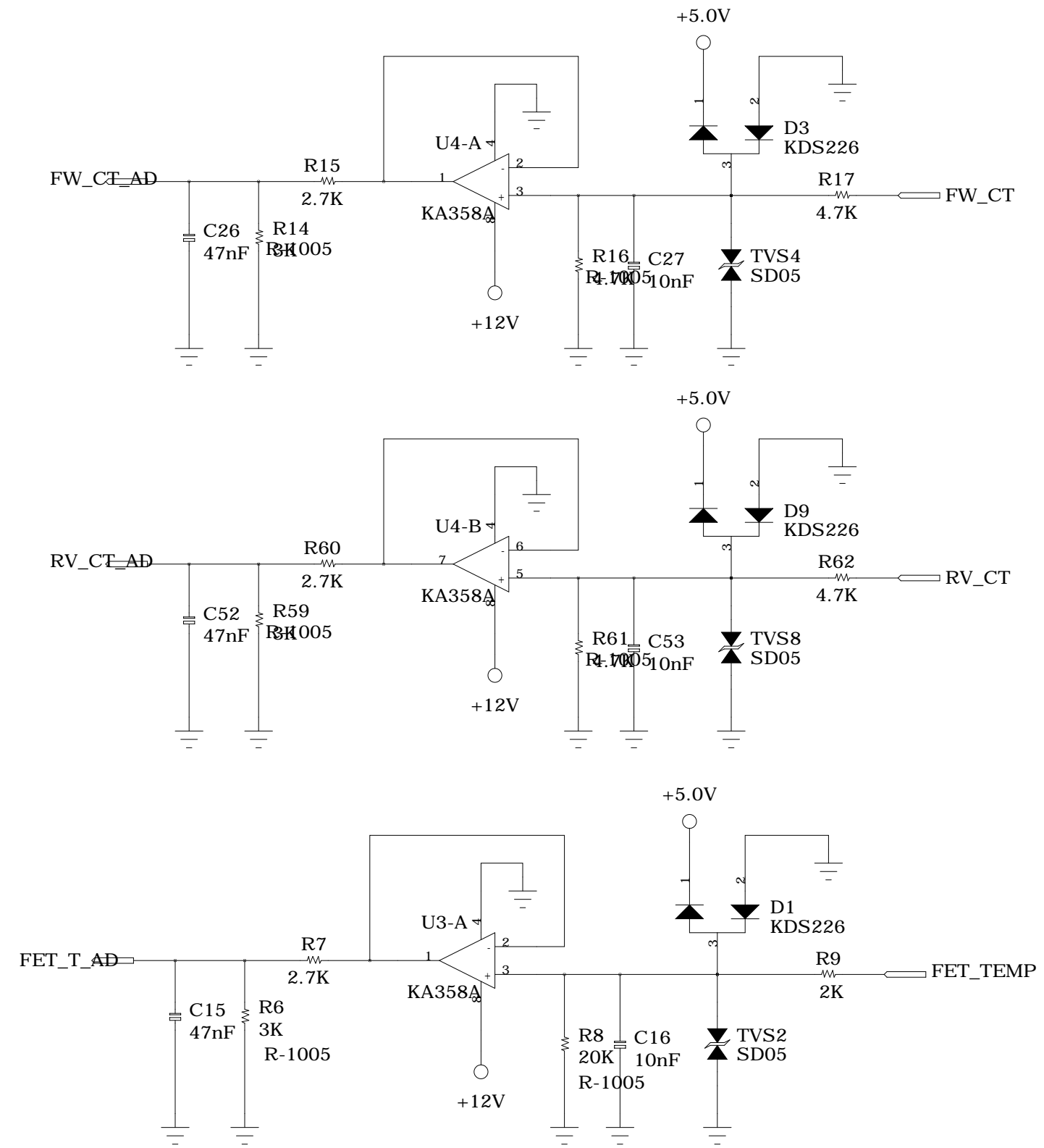
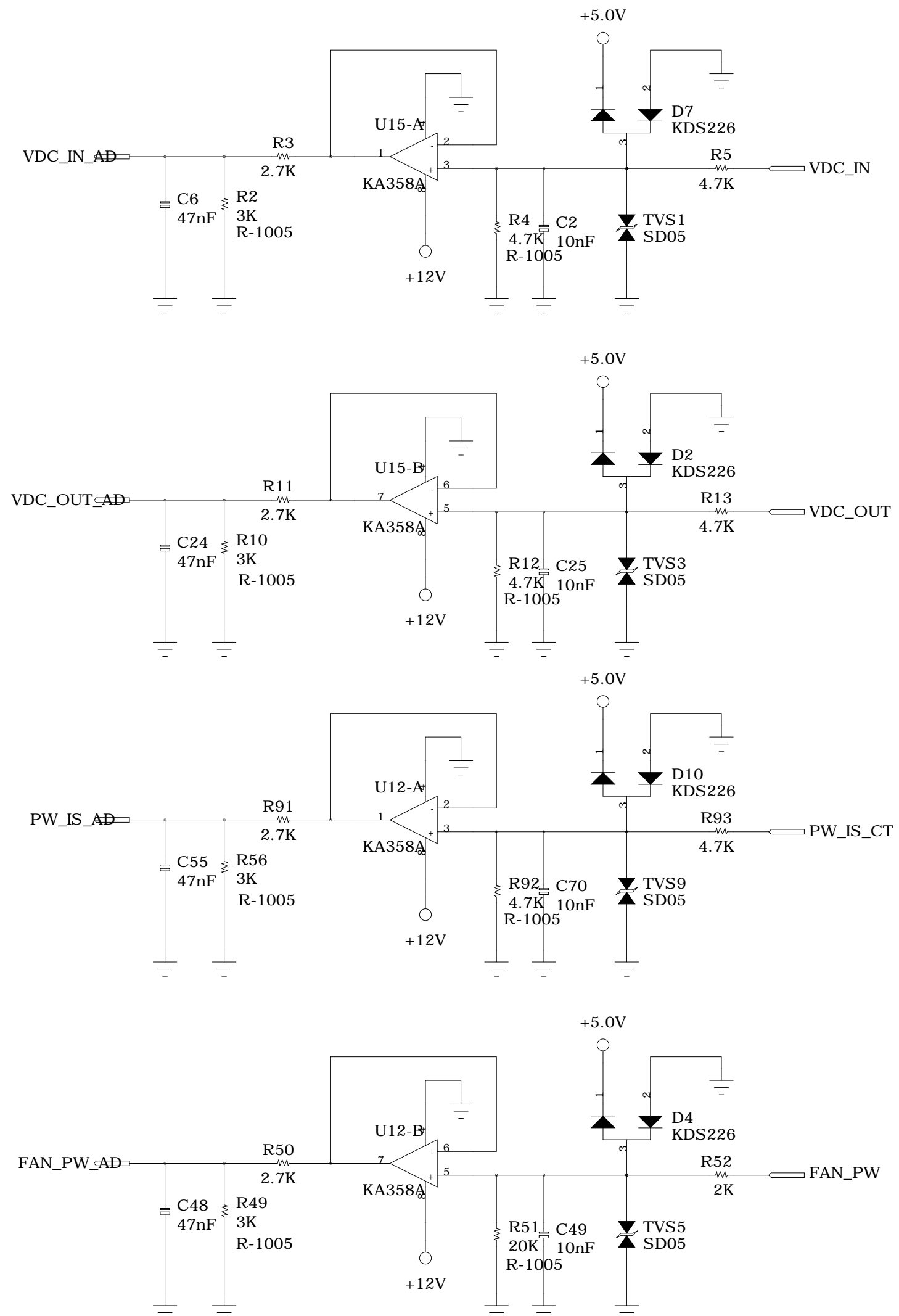


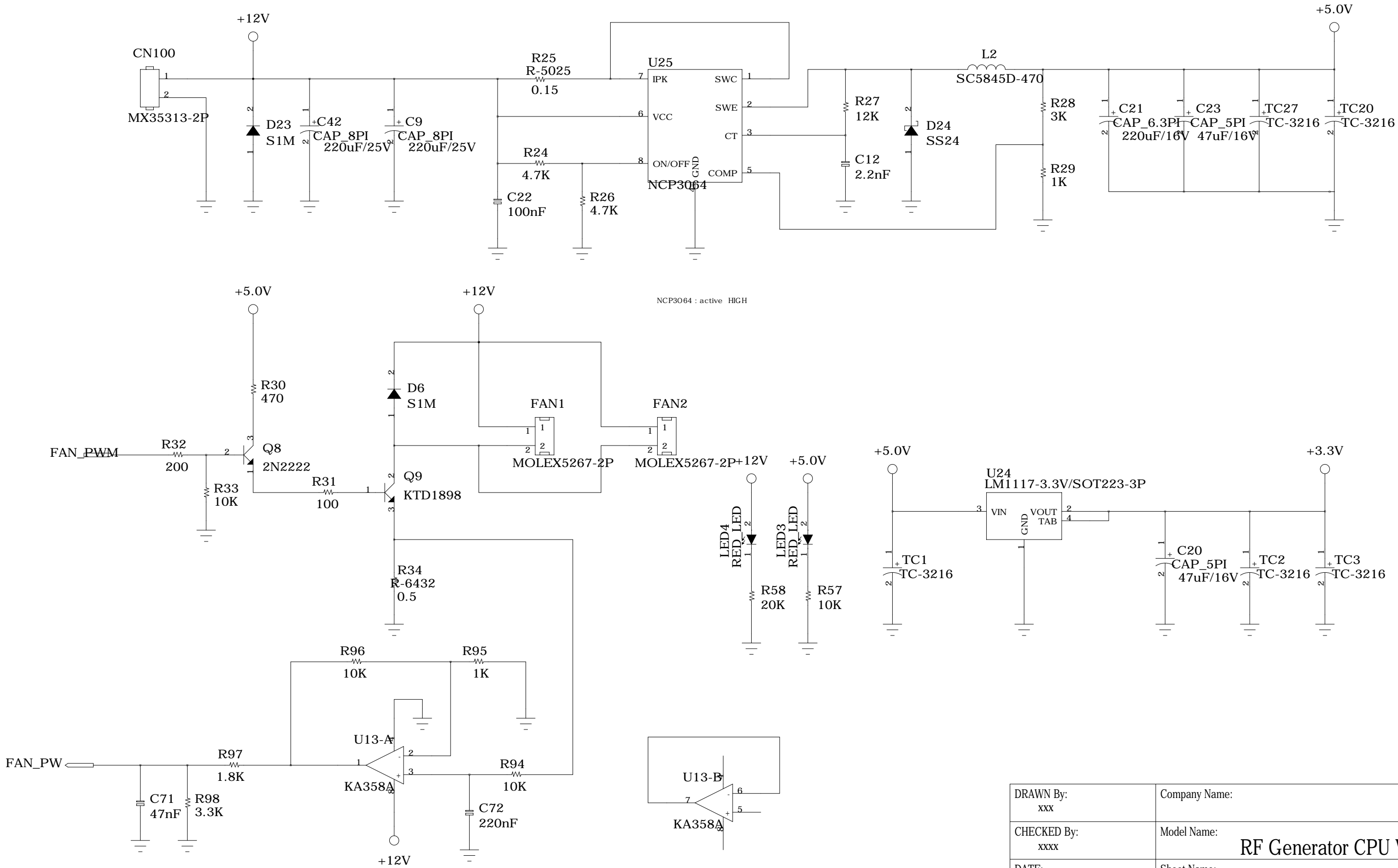
DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-08-26	Sheet Name: 01.MCU	
Revision:	TEAM: HW Team	PAGE: 1 / 7



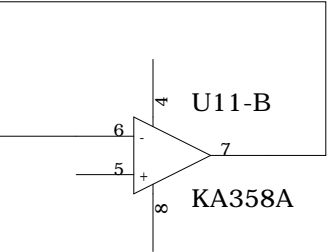
DRAWN By: xxx	Company Name:		
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0		
DATE: 2019-08-26	Sheet Name: 02.DDS		
Revision:	TEAM: HW Team	PAGE: 2 / 7	



DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-08-26	Sheet Name: 03.OPAMP	
Revision:	TEAM: HW Team	PAGE: 3 / 7



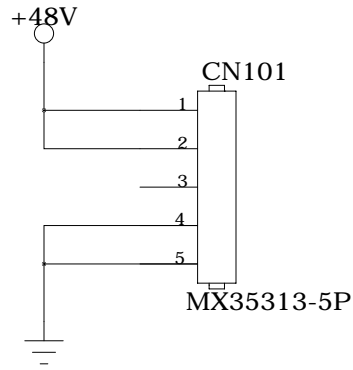
DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-08-26	Sheet Name: 05.DCDC	
Revision:	TEAM: HW Team	PAGE: 5 / 7

RF Generator CPU V1.0

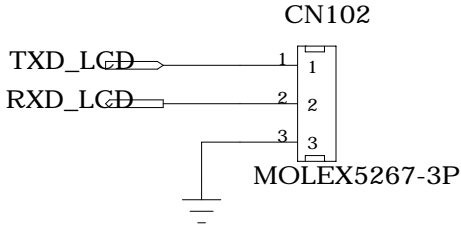
06.POWER

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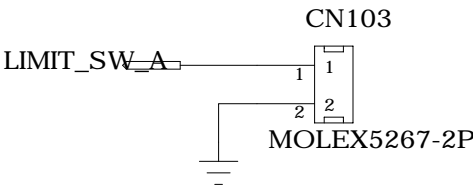
+48V SMPS IN



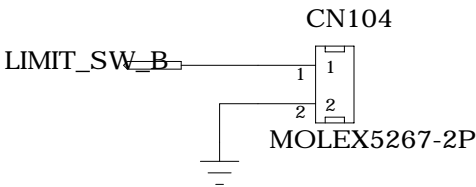
LCD A ½ A



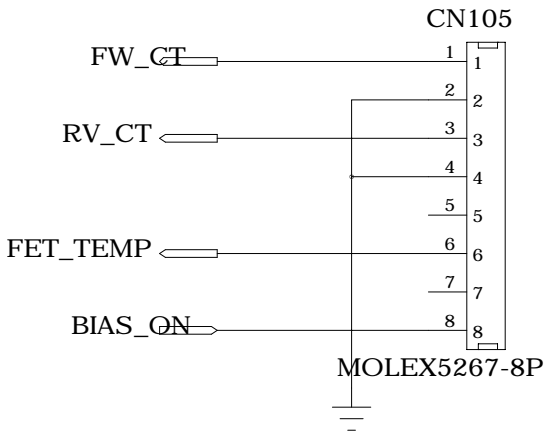
RF CON Limit S/W



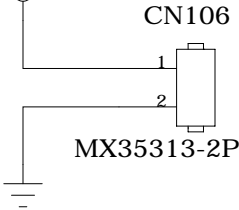
Case Limit S/W



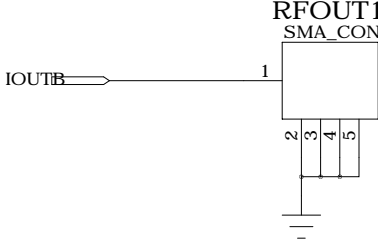
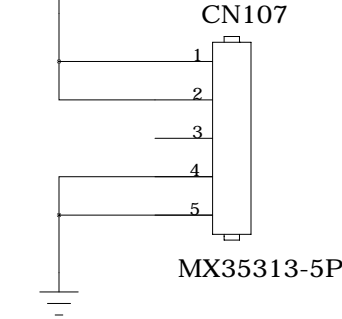
PWR PCB INTERFACE



+12V



OUT48V



DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-08-26	Sheet Name: 07.CON	
Revision:	TEAM: HW Team	PAGE: 7 / 7