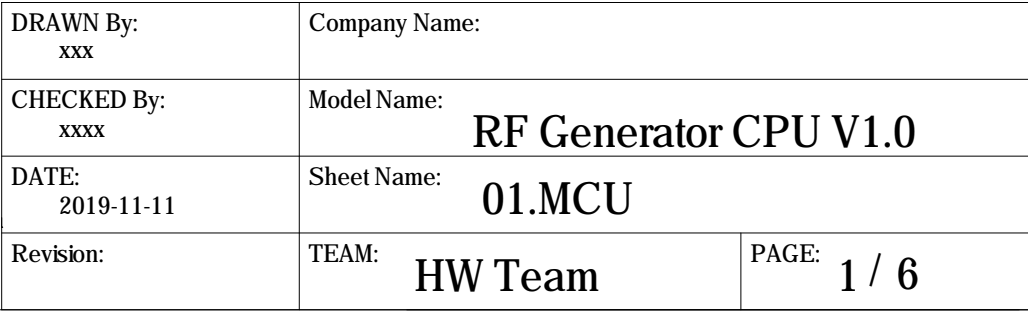
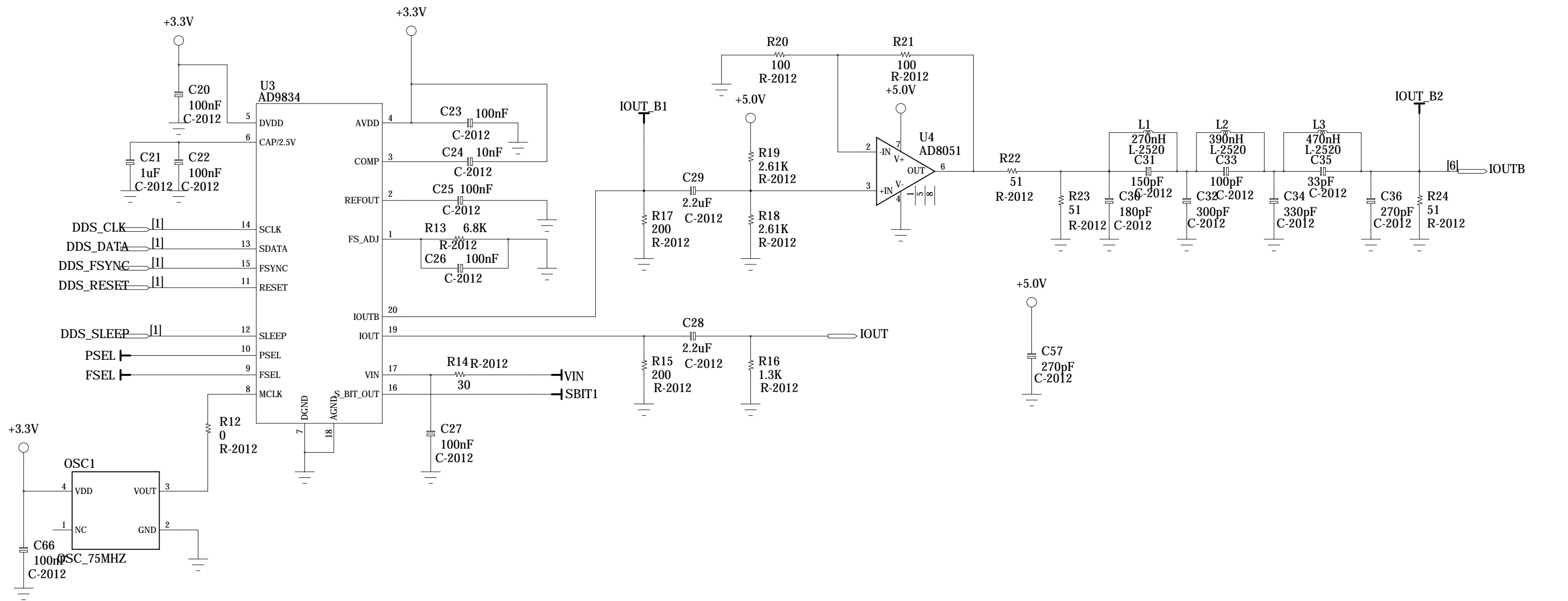
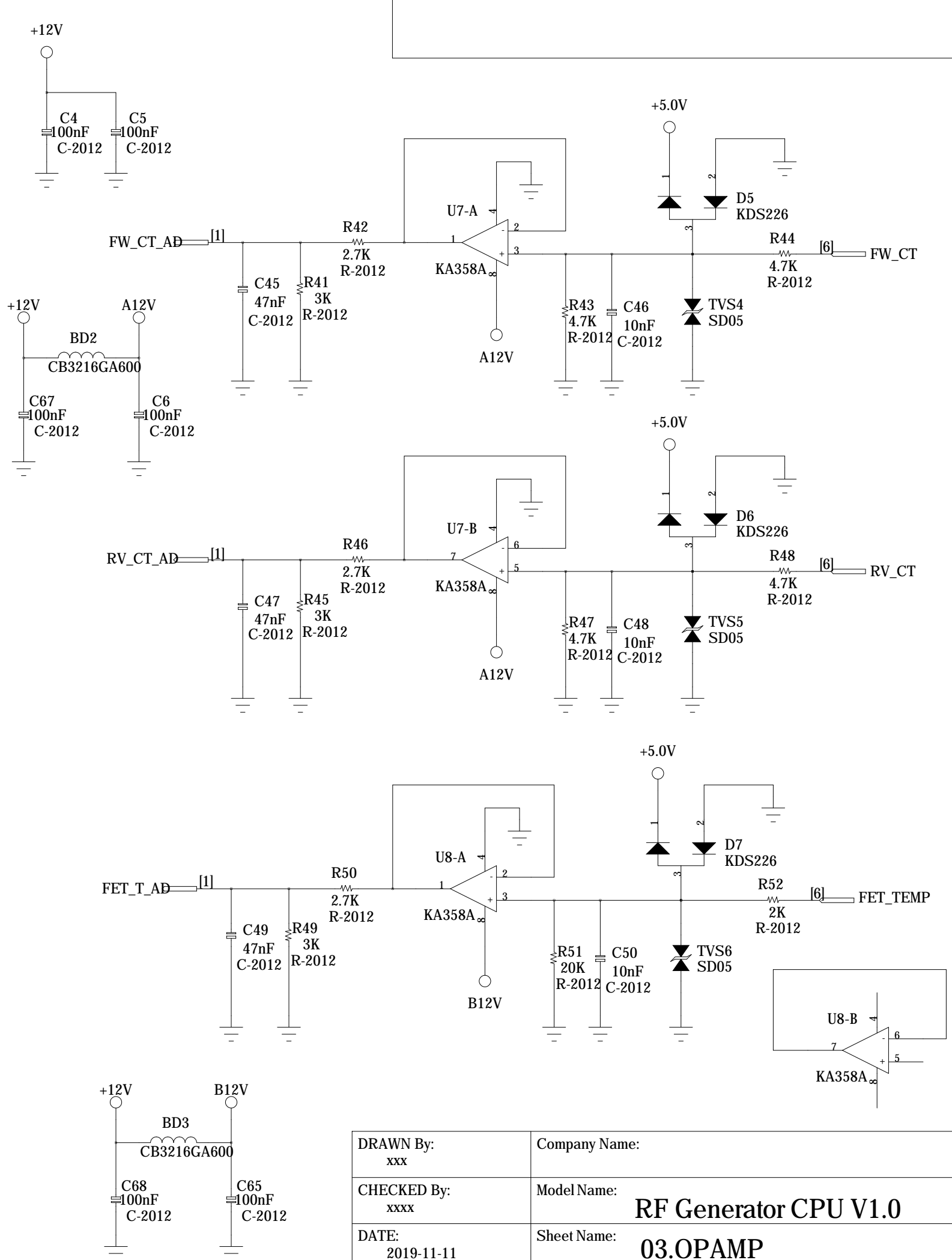
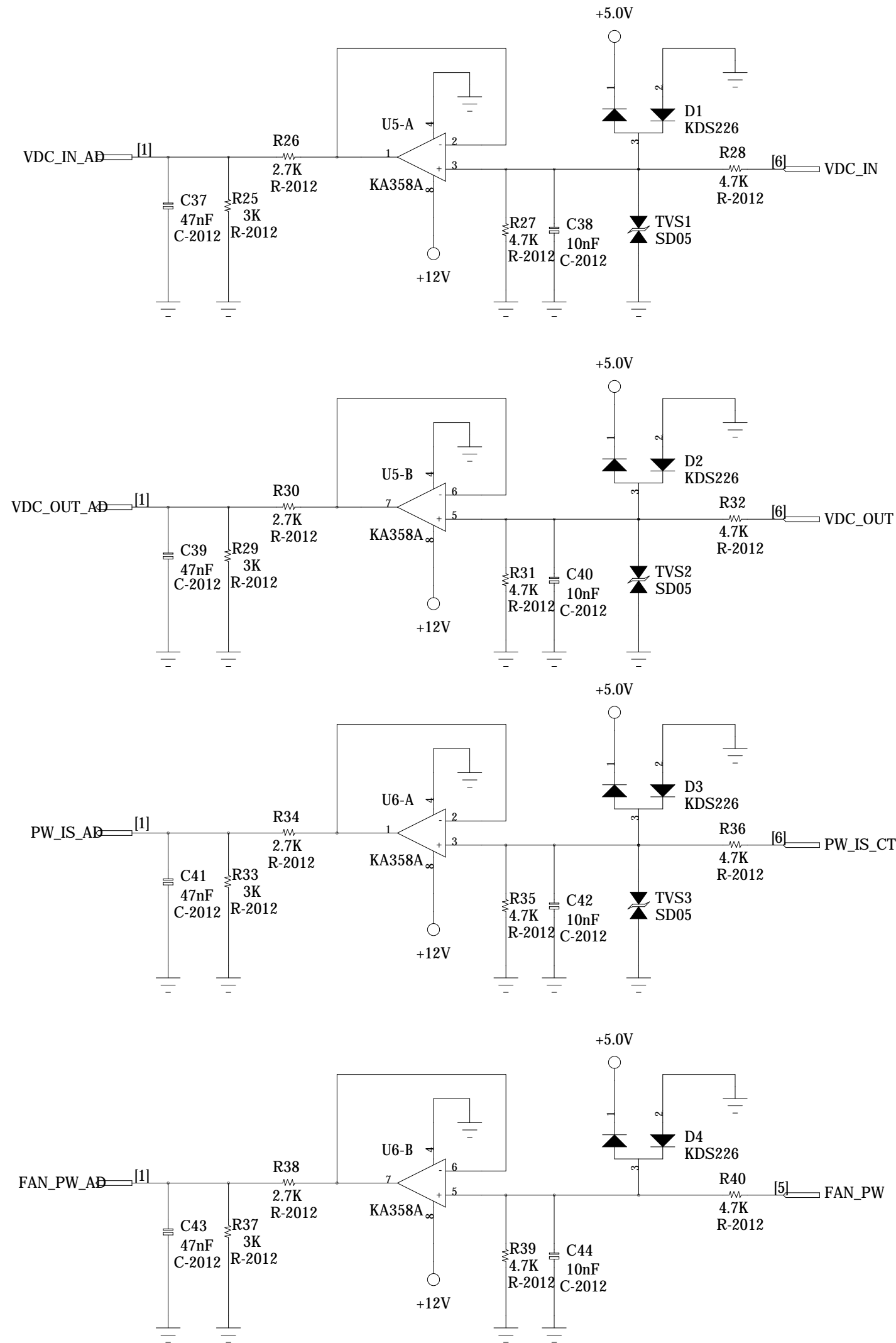


Femto

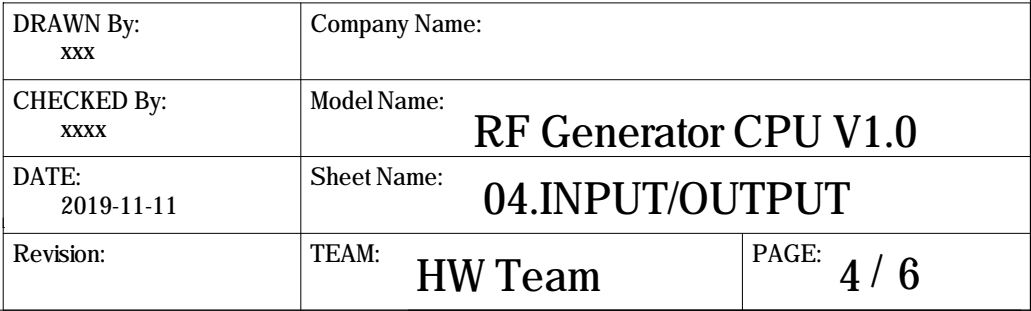




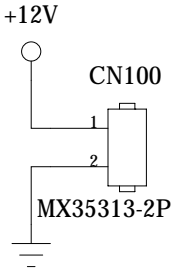
DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-11-11	Sheet Name: 02.DDS	
Revision:	TEAM: HW Team	PAGE: 2 / 6



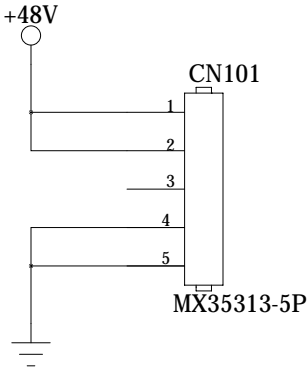
DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-11-11	Sheet Name: 03.OPAMP	
Revision:	TEAM: HW Team	PAGE: 3 / 6



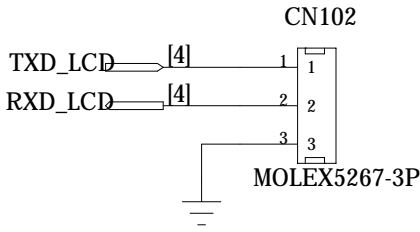
+12V SMPS INPUT



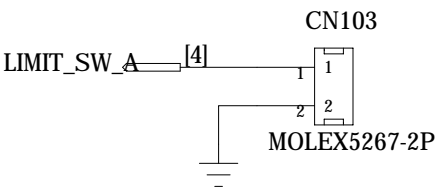
+48V SMPS IN



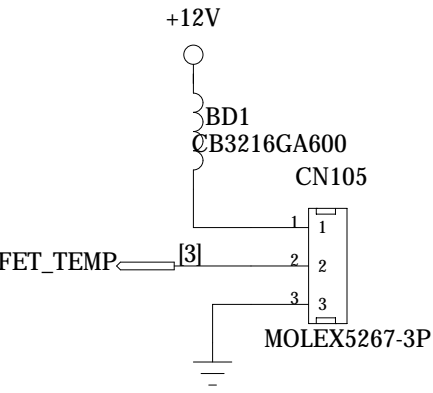
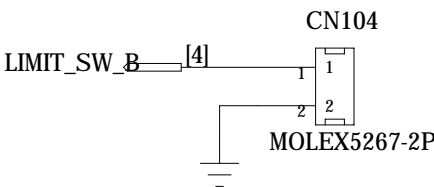
LCD Åö ½Å



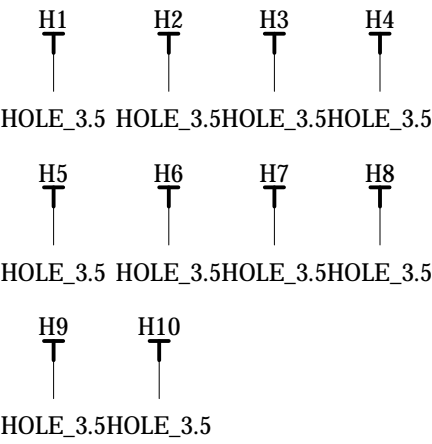
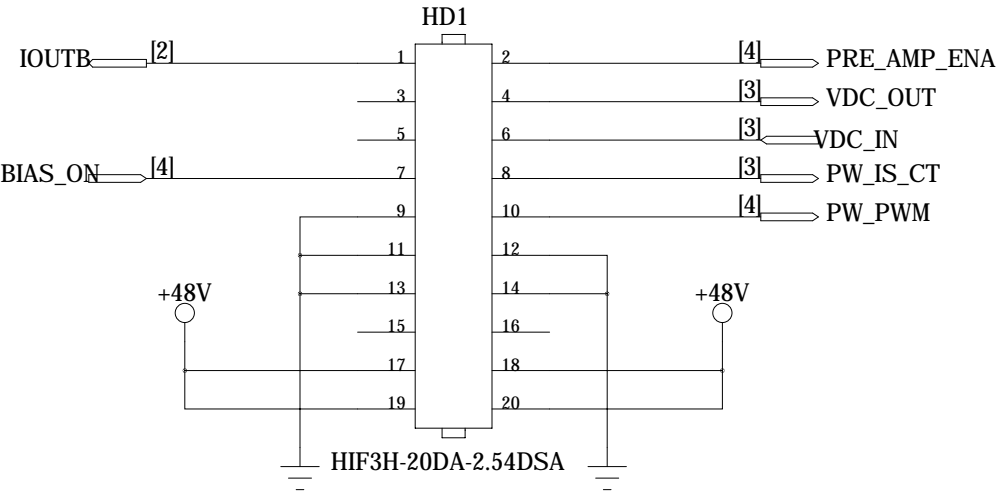
RF CON Limit S/W



Case Limit S/W



PWR PCB INTERFACE



DRAWN By: xxx	Company Name:	
CHECKED By: xxxx	Model Name: RF Generator CPU V1.0	
DATE: 2019-11-11	Sheet Name: 06.CON	
Revision:	TEAM: HW Team	PAGE: 6 / 6