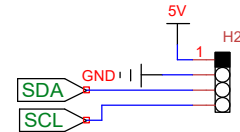
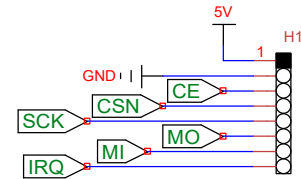
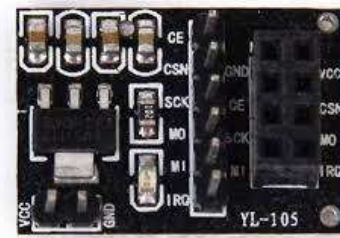
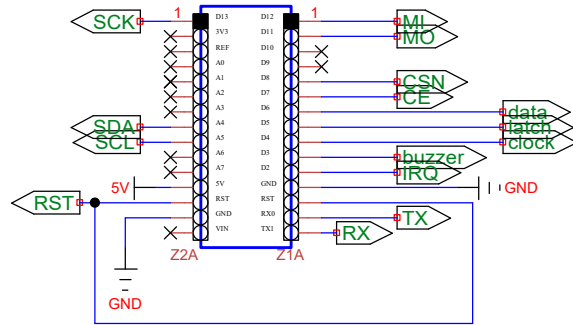
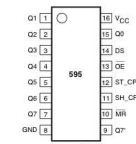
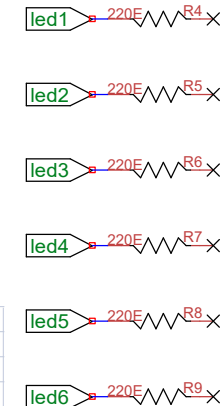
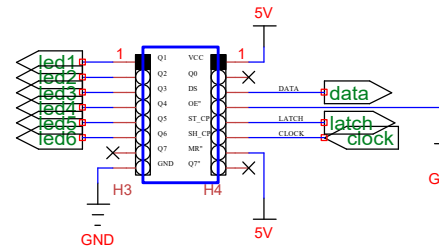
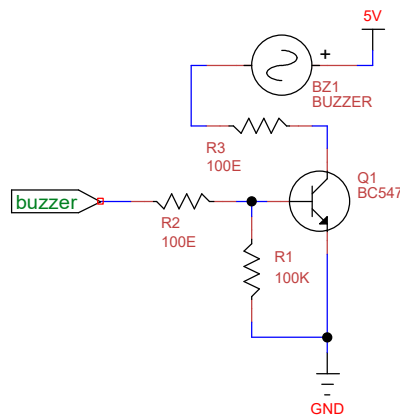
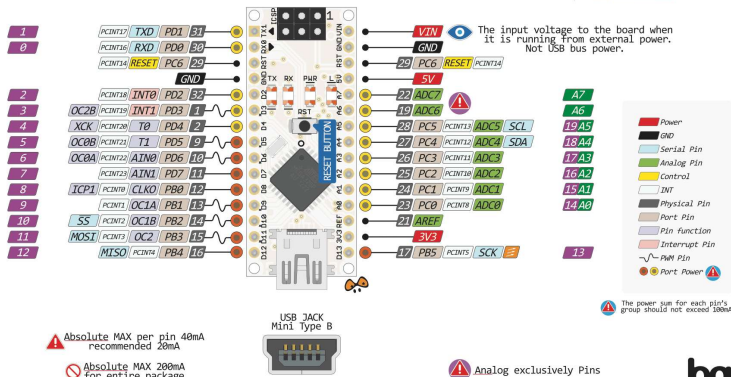


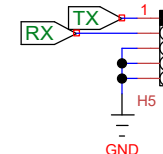
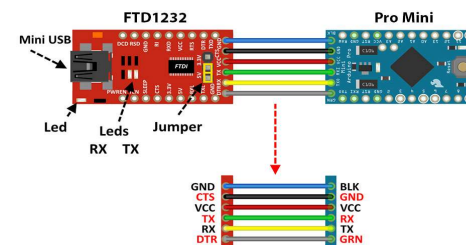
GND	GND
VCC	3.3V
CE	digIO 7
CSN	digIO 8
SCK	digIO 13
MOSI	digIO 11
MISO	digIO 12
IRQ	digIO 2



NANO PINOUT



//Pin connected to ST_CP
latchPin = 4;
//Pin connected to SH_CP
clockPin = 5;
//Pin connected to DS
dataPin = 6;



Title RX Board		
Author Arvind SKS		
File Z:\Users\aravi\OneDrive\ ... RXWITHOUTLED.dsn		Document
Revision 1.0	Date 21-01-2024	Sheets 2