

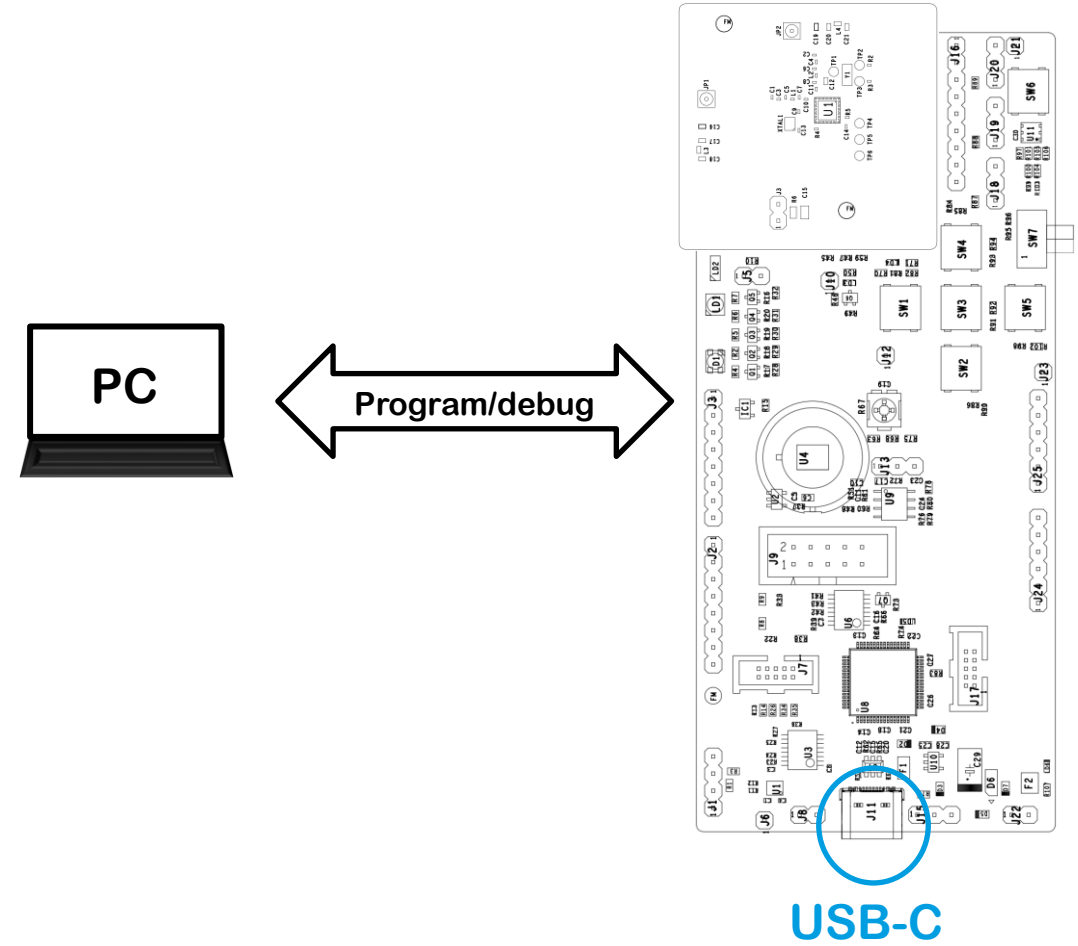


Get started with **QPG6105 PTC Development Kit**



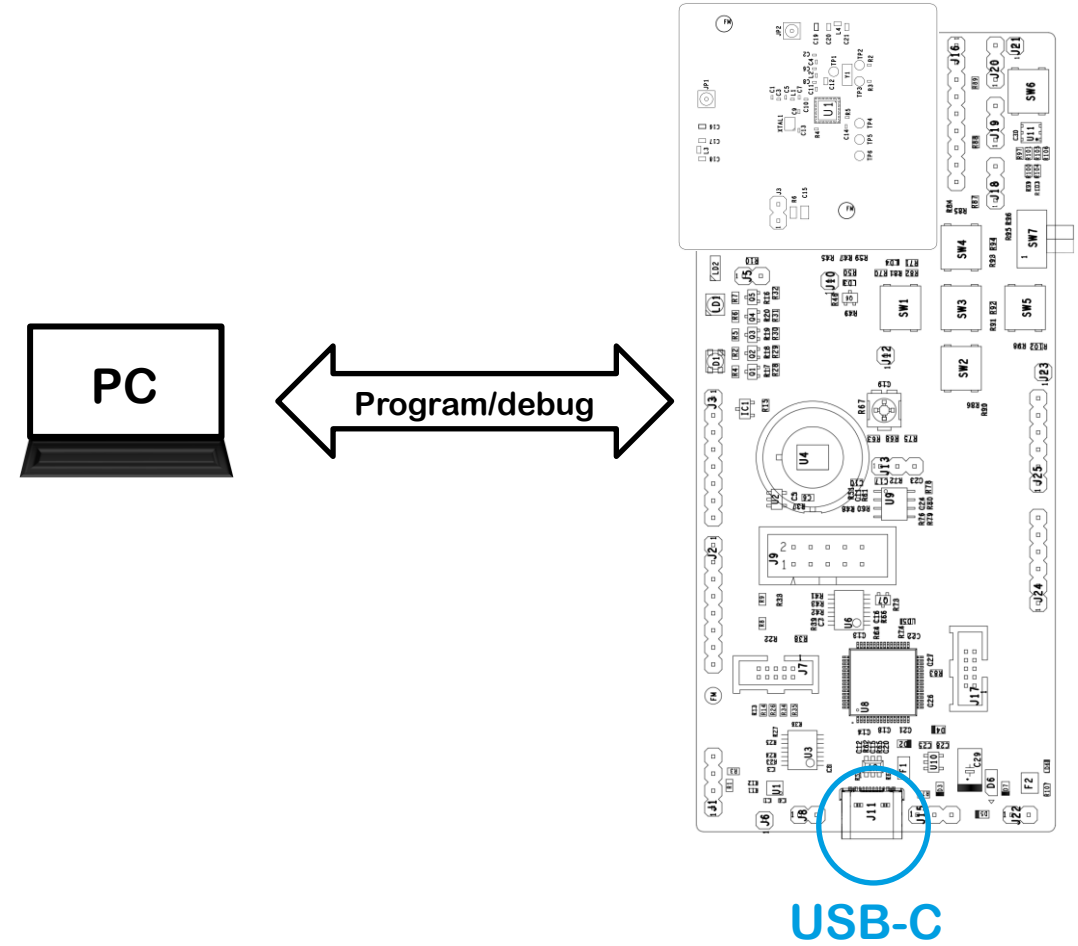
QPG6105 PTC DK Content

- Hardware
 - QPG6105 Radio Development Board
 - Smart Home and Lighting Carrier Board
 - USB-C cable
- Documents
 - Application user manuals
 - Schematics
- Tools
 - Logging



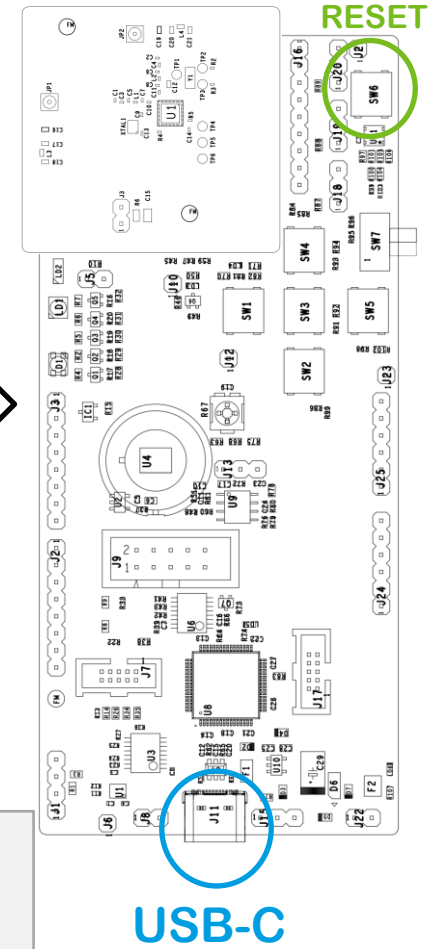
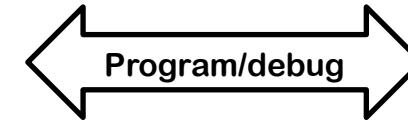
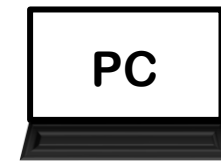
Development hardware

- PC
- QPG6105 DK board
- Test hardware
 - 802.15.4 sniffer / target



Logging

- Connect PC to the serial interface (USB-C)
- Run Jadelogger.exe in terminal
 - Available in *Tools* directory
- Reset the chip



Reset chip

1. Press **RESET** button once to reset the Qorvo chip

```
C:\> jadelogger.exe COM3:57600
== Connecting COM3:57600 ==
2020-01-14-10:27:26.752  23  0.000480  NRT  ROM  v1
```



Application usage

- Get started with the PTC architecture
UserManuals/PTC/GP_P864_UM_16481_TestComponentIntroductionManual
- The *RadioControlConsole tool* send commands from a PC to the DUT
Find out which commands at
UserManuals/PTC/GP_P864_UM_16380_PTC_Overview

