Name: Ranchal Thaman ROUND: 16010120110 Batch: B2 7 5 2021. ganchal. Assignment No. 6 PCB WOHRShop To understand Design and Manufacturing of Printed when t Board [PUB] Describe the function of double-sided UV Exposure Unit. 01) A double - sided UV vaccum exposure unit is an asential test for the A) phoduction of parts such as double -sided PCBs. The vaccum makes these by infosure units suitable few processing us sensitive flexible materials For the ultimate in reproduction definition, a vaccuum unit has to be the choice. It uses a vaccuum to ensure the art work and material to be exposed are held in perject contact. When fine tracks are required in a PCB or detailed definition on a graphic reproduction a vaccuum UV enchosuru unit should be chosen. Double sided can be carried out by switching mode on the unit. All units feature the number frame and glass plate saccuum system for the ultimate infosure resolution. An adjustable selectable 0-600 seconds on 0-100 mins timer is fitted to give the operator greater flucibility. A vaccuum gauge is also fitted in the machine enabling the oferator to check the level of vaccuum before activating the UV Tubes A vaccuum down to 0.2 bar can be created. All units come complete with an IEC socket and moulded 13 amp plug. The fluoroscent lamps emit a wavelength of 350-400 nm. list the softwares used for PCB layout design and explain how to design layout using DIPTRACE or EAGLE software. some softwares used for PCB layent design include Altium designer,

Fusion 360, NI Muttisim, Kilad EDA, Autoduk FAGLE, Dip Trace, CAM 350,

Ultiboard, PCB ortist by Advanced circuits, solid works PCB, x circuit itc.

Name: Hounchal Thaman ROU NO: 16010120110 Batch: B2 DATE # 5 2021 Aanchal. busting a PCB layout wing EAGLE software: -> Getting software in the system -> Planning phase and setup. -> library setup. - Part placement (1) - Placing resistors → Part placement (2) -> Placing LEDS → Part placement (3) → Placing switches → Part placement (4) → Placing Connectors -> Adding resistor to values -> Ranaming connectors -> Adding LED grounds -> flipping connectors - connecting the parts -> Adding a few entres and finishing connections -> Adding power to the schematic -> Error checking -> Board creation → Wherit builder setup - Precision part placement -> Finishing touches and DRC.

A) PCB pabrication is the process or procedure that transporms a circuit board design into a physical structure based upon the specifications provided in the design frackage. This physical manifestation is achieved through the following actions or techniques.

→ Imaging desired layout on copper than inner layous to reveal traces and bads.

→ Creating the PCB layer stack up by laminating (neating and brusing)

board materials at high temperatures.

Rall no: 160101 20110 Batch: B2 DATE / 7 5 2021 Aanuhal Drilling hales for mounting holes, through hale pine and vias. > Etching or removing excess copper from the surface layer(s) to neveal traces and pade - Plating bin holes and via holes. - Adding pratective coating to surface or solder masking -> Silks irun printing reference and polarity indicators, logos or other markings on the surface. -> optionally, a finish may be added to copper areas of surface 84) Explain PCB in detail. A printed circuit board (PCB) mechanically supports and electrically connects electrical er electronic components using conductive tracks, pools and other features etched from one en more sheet layers of copper laminated onto and or between sheet layers of a non-conductive subrate components are generally soldered onto the PCB to both electrically connect and mechanically faster them to it. Printed circuit boards are used in all but the simplest dectronic boards. They're also used in some electrical bushuets, such as passive switch barres PCBs can be single-sided (one capper layer), double-sided (two capper layers on both sides of one substrate layer), or multi-layer Couter and inner layers of copper, alternating with layers of substrate). Mutti-layer PCBs allow for much higher component density, because circuit traces on the inner layers would otherwise take up surface space between components. The rise in popularity of multilayer PCBs with more than two, and especially with more than four, copper planes was concurrent with the adoption of surface mount technology. However, multilayer PCBs make repair, analysis, and field modification of circuits much more difficult and usually impractical.

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ROUNO: 160101 20110 Batch: B2 7 5 2021 fancial. 95) Draw any one of the following schematic diagrams with its PCB layout. Plan drawing by hand -Plan drawing in software -

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Question 5)

