



SYMBIOSIS INSTITUTE OF TECHNOLOGY, NAGPUR

Symbiosis International (Deemed University)

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Founder: Prof. Dr. S. B. Mujumdar, M. Sc., Ph. D. (Awarded Padma Bhushan and Padma Shri by President of India)

MES CA-1

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SECTION: D

SEMESTER: 4th

1. Write an 8051 Assembly Language Program (ALP) to generate the last four digits of your PRN using arithmetic instructions only. The final result should be available in the Accumulator (A).

The screenshot shows the HDSIM5 software interface. On the left, there's a memory dump window titled 'Data Memory' showing a 16x16 grid of hex values. In the center, assembly code is displayed in green font:

```
RST Step Pause Run Stop Break CPY Paste BP
Time: 8lus - Instructions: 45
0000| MOV A, #06H ; A = 06
0002| ADD A, #06H ; A = 12
0004| DA A ; A = 12 (B6)
0005| MOV B, A ; B = 12

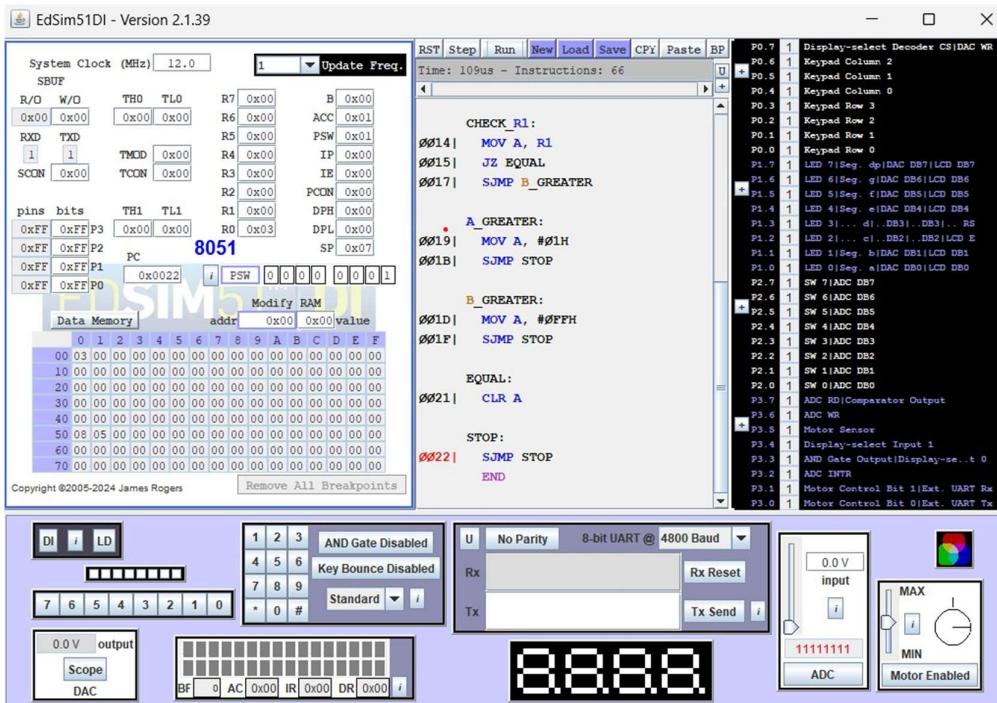
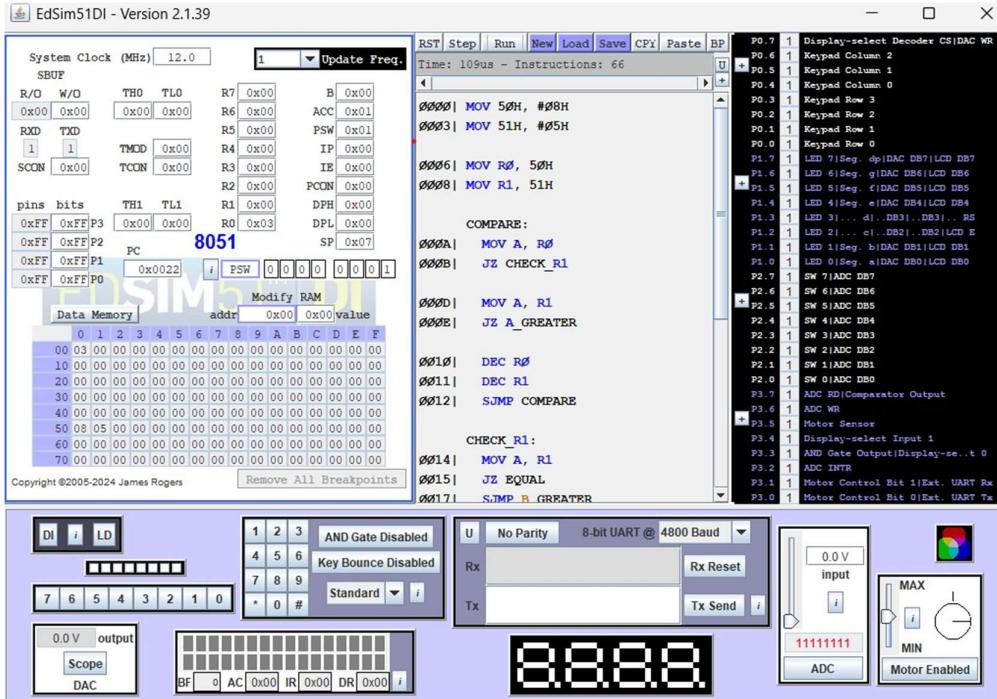
; ----- Generate 19 -----
0007| MOV A, #09H ; A = 09
0009| ADD A, #09H ; A = 12H (12)
000B| DA A ; A = 18 (B6)
000C| ADD A, #01H ; A = 19
000E| DA A ; A = 19 (B6)

; ----- Final Result -----
; B = 12
; A = 19
; AX = 1219

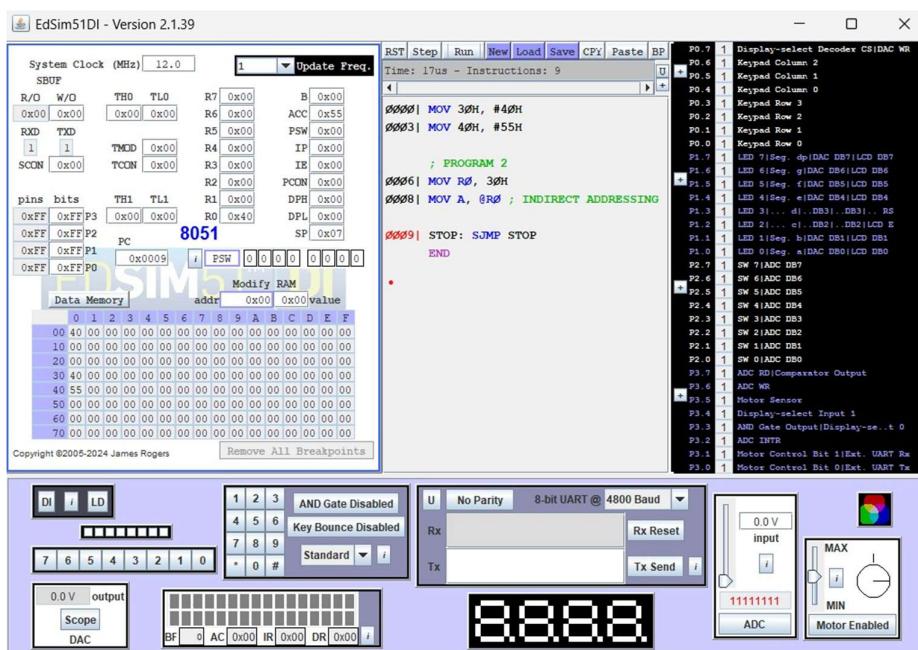
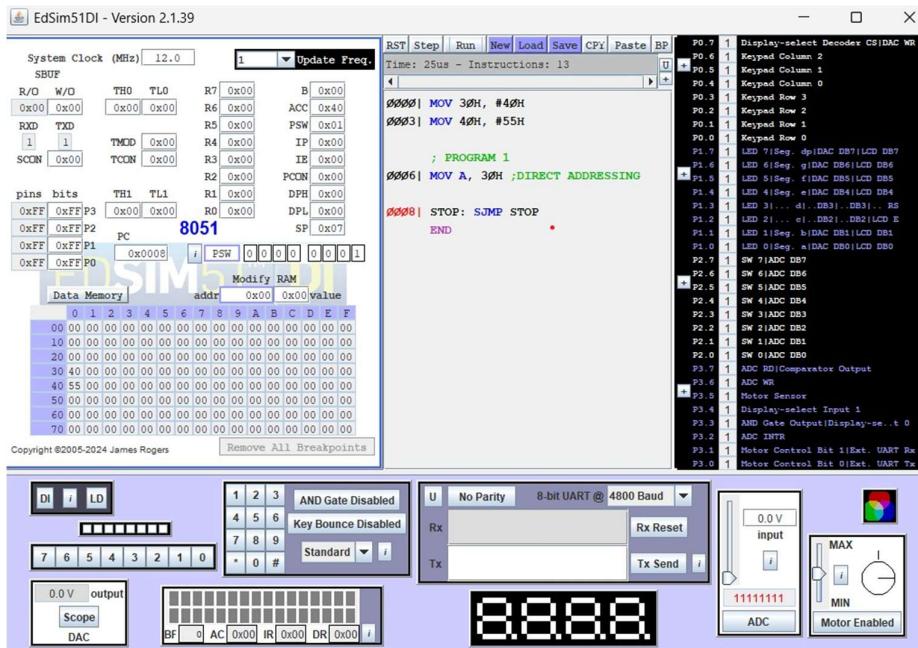
000F| HERE: SJMP HERE
END
```

On the right, a pin configuration window lists pins P0.0 through P3.0 with their corresponding functions. Below the assembly code, a digital logic simulation window shows various components like AND gates, a DAC, and a motor driver. A digital scope window at the bottom displays the value '8.88'.

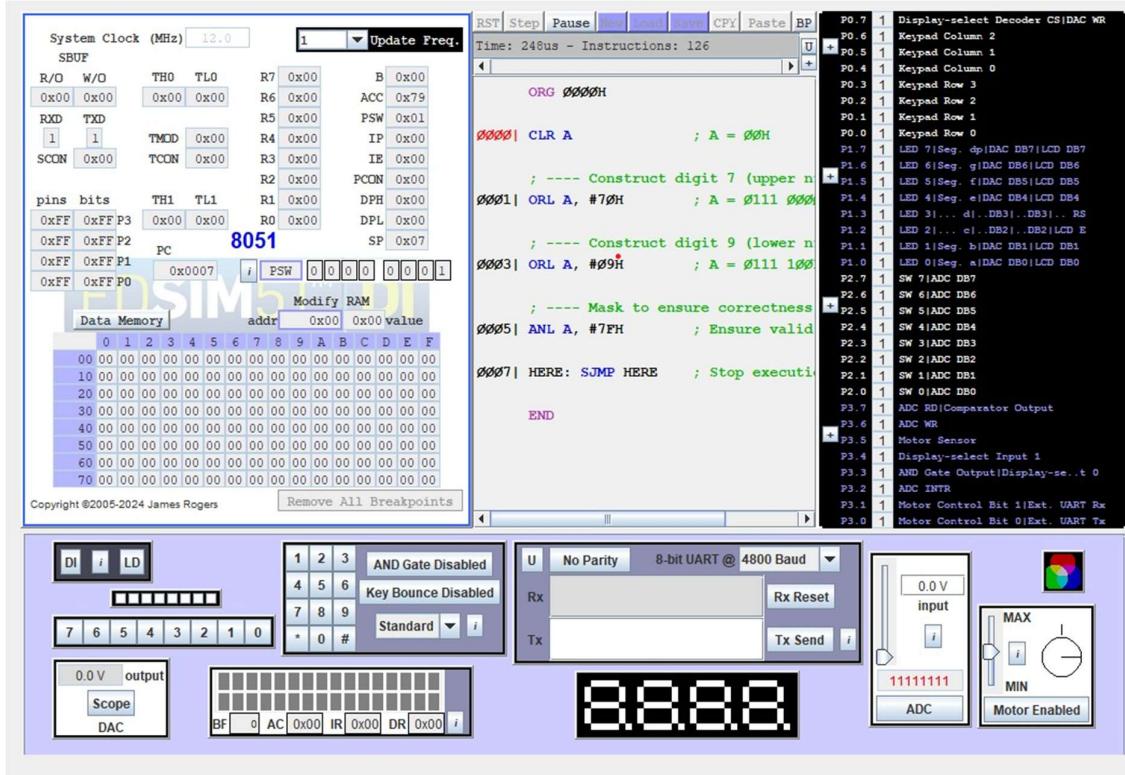
2. Write an 8051 Assembly Language Program to compare two unsigned numbers stored at internal RAM locations 50H and 51H without using CJNE, DJNZ, or SUBB. Only MOV, INC,DEC, JZ, JNZ, CLR, SETB, ANL, and ORL instructions are allowed. Store the result such that 01H indicates 50H > 51H, 00H indicates equality, and FFH indicates 50H < 51H.



3. A student claims that two assembly programs are equivalent because they access the same RAM address. Using one program with direct addressing and another with indirect addressing, demonstrate why this claim is incorrect by showing a case where both reference the same base address but produce different outputs. Explain how the difference in addressing modes causes this behavior.



4. Write an 8051 Assembly Language Program to generate the last four digits of your mobile number using logical instructions (ANL, ORL, CLR). Do not directly load the complete four-digit number. The final result must be available in the Accumulator (A).



5. Write an 8051 Assembly Language Program to compact data stored in internal RAM locations 40H–5FH using only indirect addressing. Remove all occurrences of FFH, shift the remaining data left to eliminate gaps, and fill the unused locations at the end with 00H, without using additional RAM or the stack.

