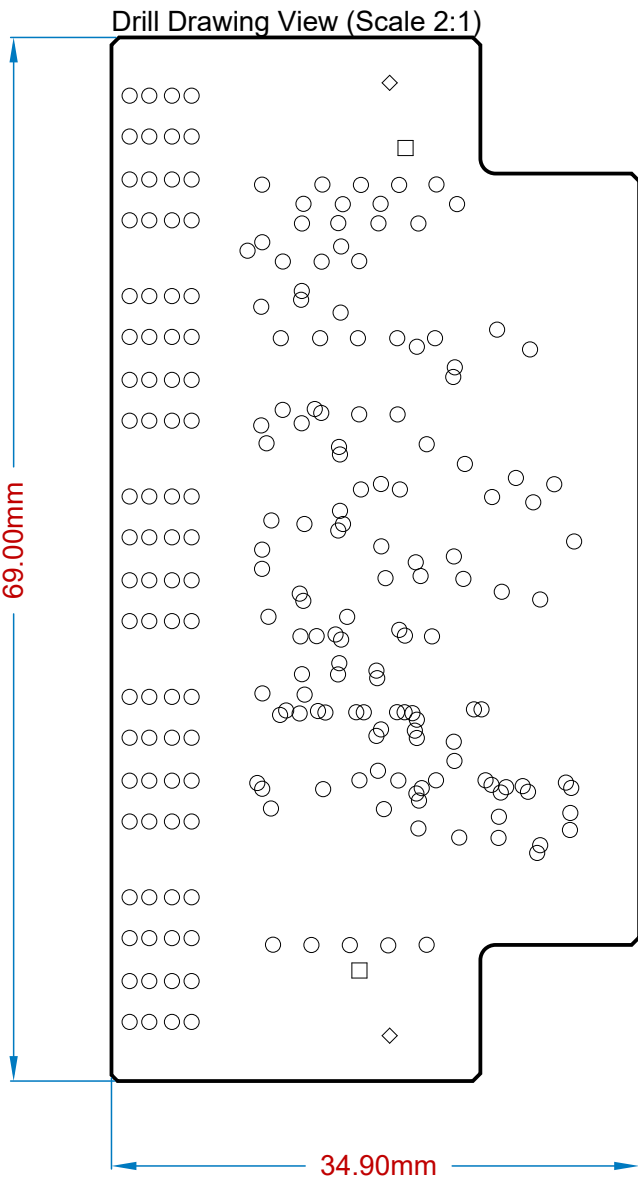
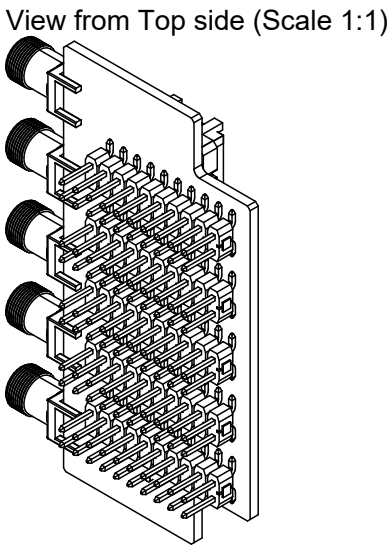


Manufacturing documentation:



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
○	218	0.20mm	Plated	
□	2	1.27mm	Non-Plated	
◇	2	2.70mm	Plated	
	222 Total			



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber	Comments
	Top Overlay			Legend	GTO	
	Surface Material	0.025mm	SM-001	Solder Mask	GTS	black
Gold	Top Surface Finish	0.004mm		Surface Finish		
CF-004	Top Layer	0.035mm		Signal	GTL	
Prepreg		0.084mm	PP-009	Dielectric		-
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1	
Core		1.200mm	Core-043	Dielctric		-
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2	
Prepreg		0.084mm	PP-009	Dielectric		-
CF-004	Bottom Layer	0.035mm		Signal	GBL	
Gold	Bottom Surface Finish	0.004mm		Surface Finish		
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	black
	Bottom Overlay			Legend	GBO	
Total thickness: 1.566mm						

- Notes:
1. Impedance matched stack-up
 2. 0.1mm trace width / clearance

ETH zürich			Project: FMC Test Module	
Drawing number:	Rev: A	Format: A3	Laboratory: IIS	Sheet: FMC_Test_Module_fabrication.PCBDwf
Date: 10.02.2021			Drawn by: MAYERPH	Page: 1 of 1
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\FMC_Test_Module\FMC_Test_Module_fabrication.PCBDwf				