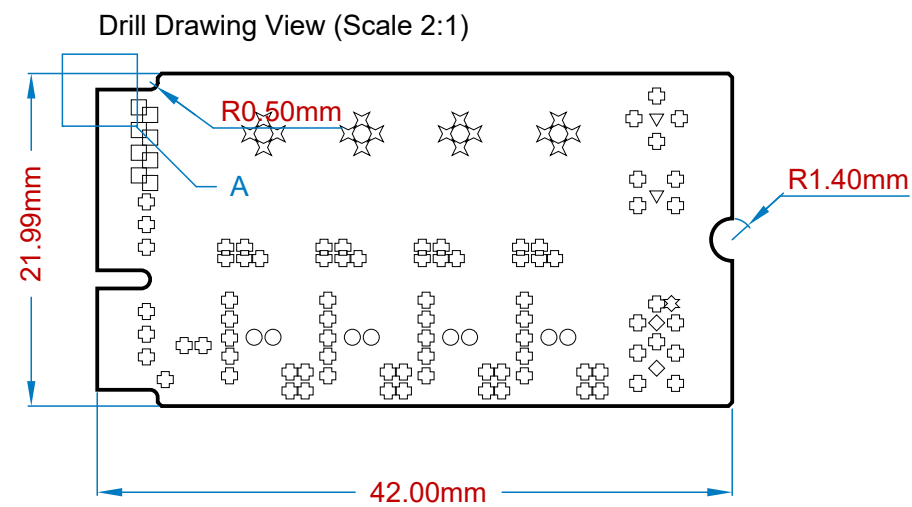
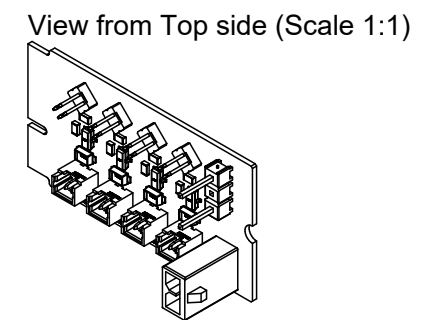
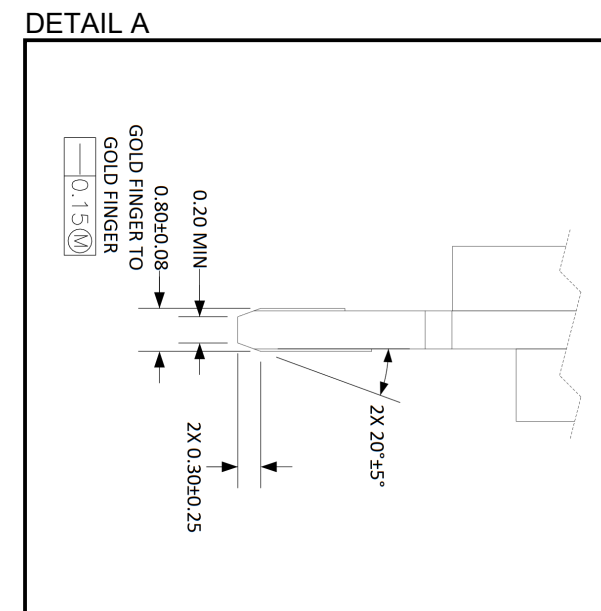


Manufacturing documentation:



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
	8	0.20mm	Plated	
	81	0.30mm	Plated	
	8	0.50mm	Plated	
	16	0.70mm	Plated	
	1	1.02mm	Non-Plated	+/-0.05mm
	2	1.02mm	Plated	+/-0.05mm
	2	1.02mm	Plated	
118 Total				



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber	Comments	Dk	Frequency
	Top Overlay			Legend	GTO			
Surface Material	Top Solder	0.025mm	SM-001	Solder Mask	GTS	black	4	1GHz
Nickel, Gold	Top Surface Finish	0.004mm		Surface Finish				
CF-004	Top Layer	0.035mm		Signal	GTL			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1			
Core		0.406mm	Core-034	Dielctric		-	4,6	1GHz
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Bottom Layer	0.035mm		Signal	GBL			
Nickel, Gold	Bottom Surface Finish	0.004mm		Surface Finish				
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	black	4	1GHz
	Bottom Overlay			Legend	GBO			

Total thickness: 0.829mm

Notes:

1. impedance matched stackup
2. 0.1mm trace width / clearance



Project:

PWM Module

Drawing number:

Rev: PA

Format:

Laboratory: IIS

Sheet: PWM_Module_fabrication.PCBDwf

Date: 17/01/2023

Drawn by: MAYERPH

Page: 1 of 1

A3

File: C:\Users\admin\Desktop\tesi\FMC Peripheral Board\PWM_Module\PWM_Module_fabrication.PCBdwf