

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

uSD Card Module - Connector

Drawing number:

Rev: PA

Format:

Laboratory: IIS

Sheet: connector.SchDoc

Date: 17/01/2023 14:26:43

A4 Q

Drawn by: mayerph

Page 2 of 3

File: C:\Users\admin\Desktop\tes\FMC Peripheral Board\PWM_Module\Schematic\connector.SchDoc

A

B

C

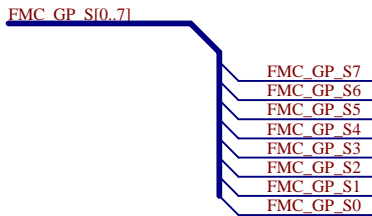
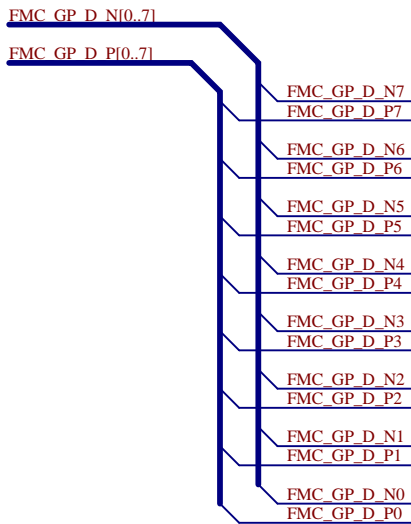
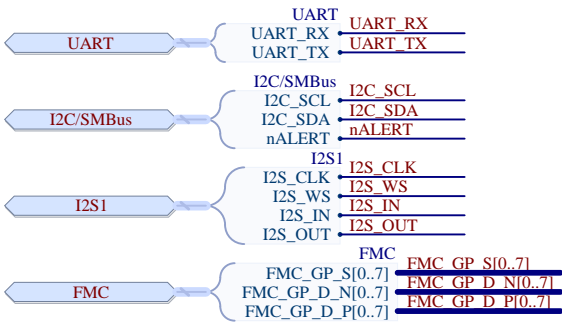
D

A

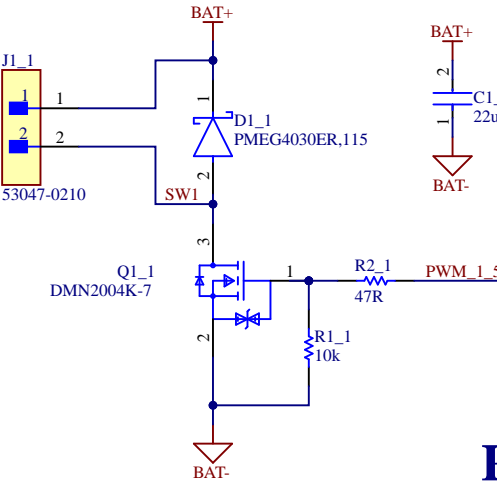
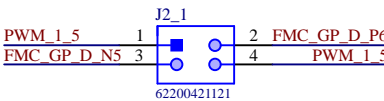
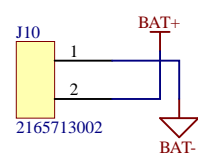
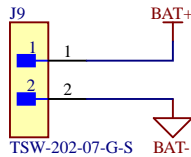
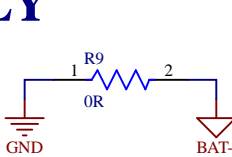
B

C

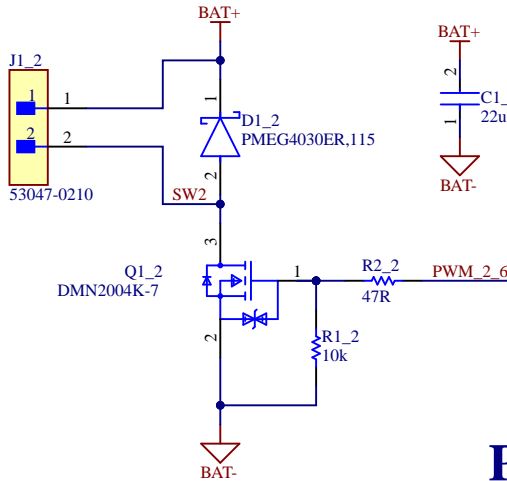
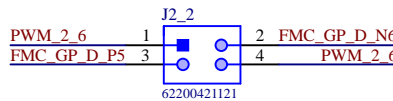
D



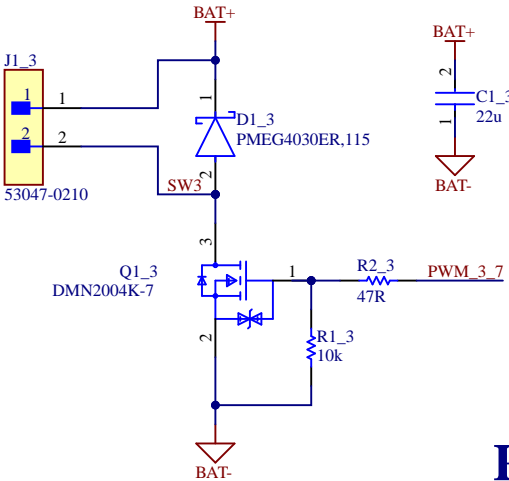
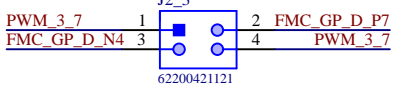
SUPPLY



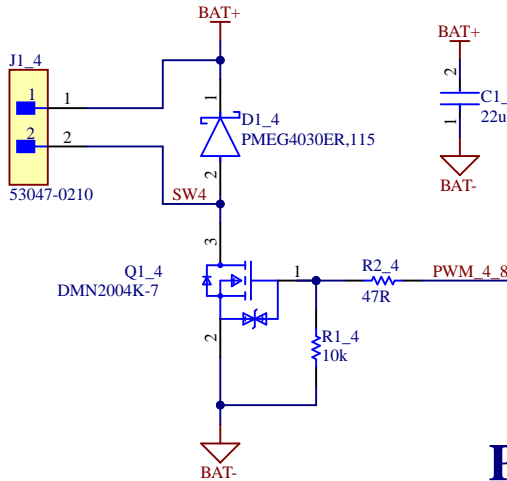
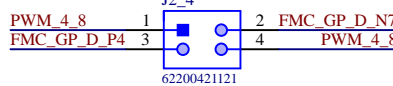
PWM1



PWM2



PWM3



PWM4

ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

PWM Module

Drawing number:

Rev: PA

Format:

Laboratory: IIS

Sheet: peripheral_PWM.SchDoc

Date: 17/01/2023 14:26:43

A3

Drawn by: mayerph

Page 3 of 3

File: C:\Users\admin\Desktop\tes\FMC Peripheral Board\PWM_Module\Schematic\peripheral_PWM.SchDoc