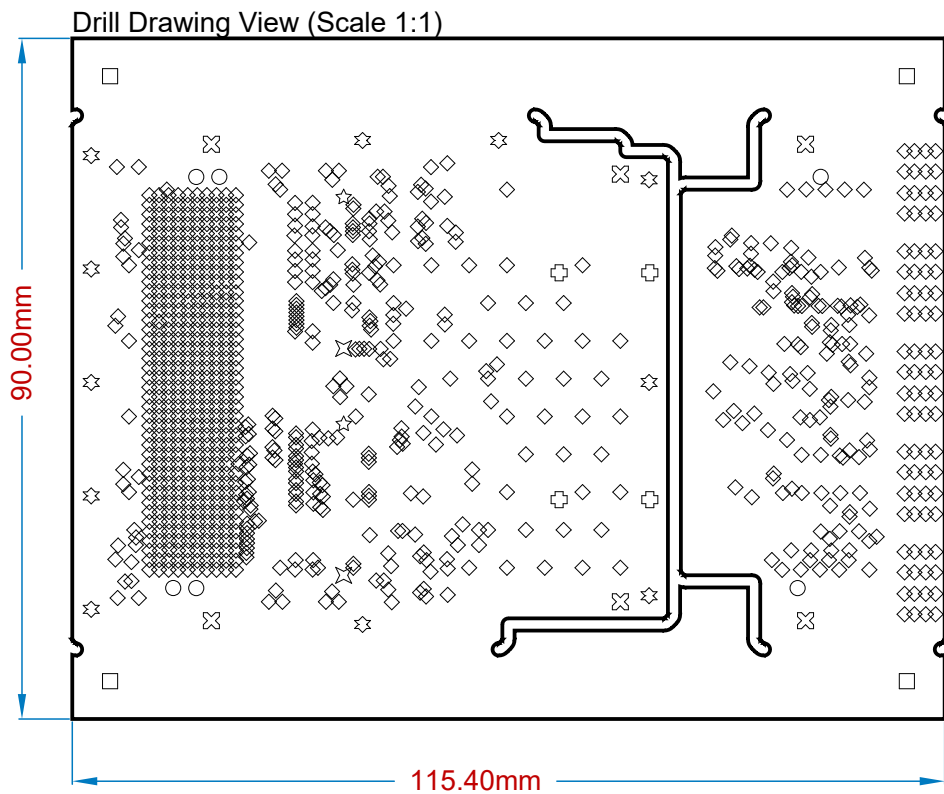
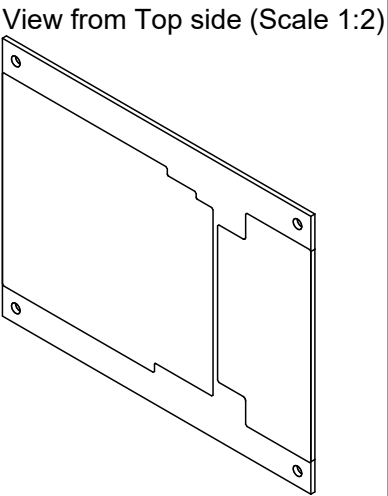


Manufacturing documentation:



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	931	0.20mm	Plated	
✕	2	1.10mm	Non-Plated	+/-0.05mm
○	6	1.27mm	Non-Plated	
☆	2	1.60mm	Non-Plated	+/-0.05mm
☆	11	2.20mm	Plated	
✕	6	2.70mm	Plated	
⊕	4	3.00mm	Plated	
□	4	3.20mm	Non-Plated	
966 Total				



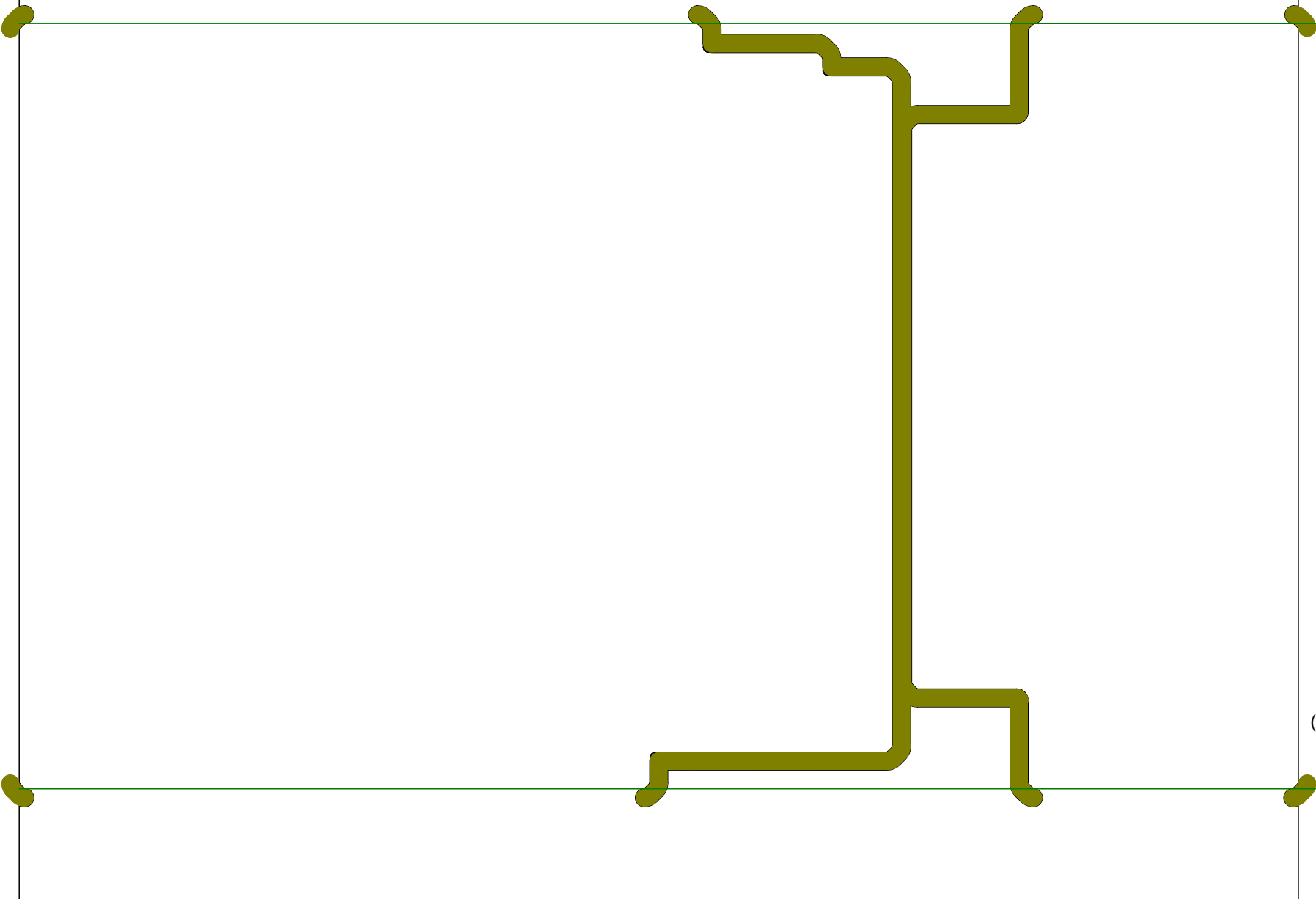
Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber	Dk	Comment
	Top Overlay			Legend	GTO		
Surface Material	Top Solder	0.025mm	SM-001	Solder Mask	GTS	4	black
Gold	Top Surface Finish	0.004mm		Surface Finish			
CF-004	Top Layer	0.035mm		Signal	GTL		
Prepreg		0.084mm	PP-009	Dielectric		4	
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1		
Core		1.200mm	Core-043	Dielctric		4.3	
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2		
Prepreg		0.084mm	PP-009	Dielectric		4	
CF-004	Bottom Layer	0.035mm		Signal	GBL		
Gold	Bottom Surface Finish	0.004mm		Surface Finish			
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	4	black
	Bottom Overlay			Legend	GBO		
Total thickness: 1.566mm							

- Notes:
1. Impedance matched stack-up
  2. 0.1mm trace width / clearance

<b>ETH zürich</b>			Project: FPGA Multiboard 1.6mm	
Drawing number:	Rev: A	Format: A3	Laboratory: IIS	Sheet: MultiBoard_1p6mm.PCBDwf
Date: 15.02.2021			Drawn by: MAYERPH	Page: 1 of 2
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\00_MultiBoardAssembly\Manufacturing\MultiBoard_1p6mm.PCBDwf				

Mechanical 4 (Scale 2:1)



U-Scoring  
(Gerber .M7)

(Gerber .M8)  
Route tool path 1.6mm  
U-Scoring

<div>ETHzürich</div>			Project: FPGA Multiboard 1.6mm	
Drawing number:	Rev: A	Format: A3	Laboratory: IIS	Sheet: MultiBoard_1p6mm.PCBDwf
Date:	15.02.2021		Drawn by: MAYERPH	Page: 2 of 2
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\00_MultiBoardAssembly\Manufacturing\MultiBoard_1p6mm.PCBDwf				