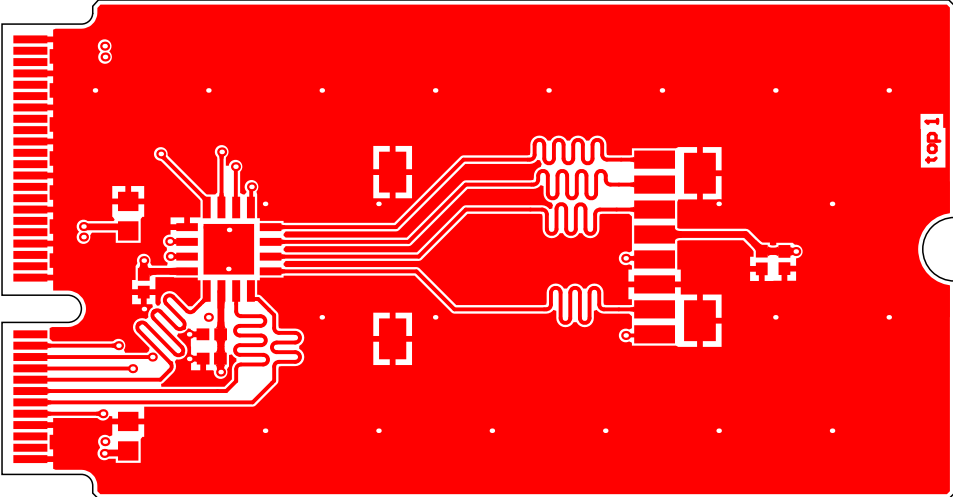


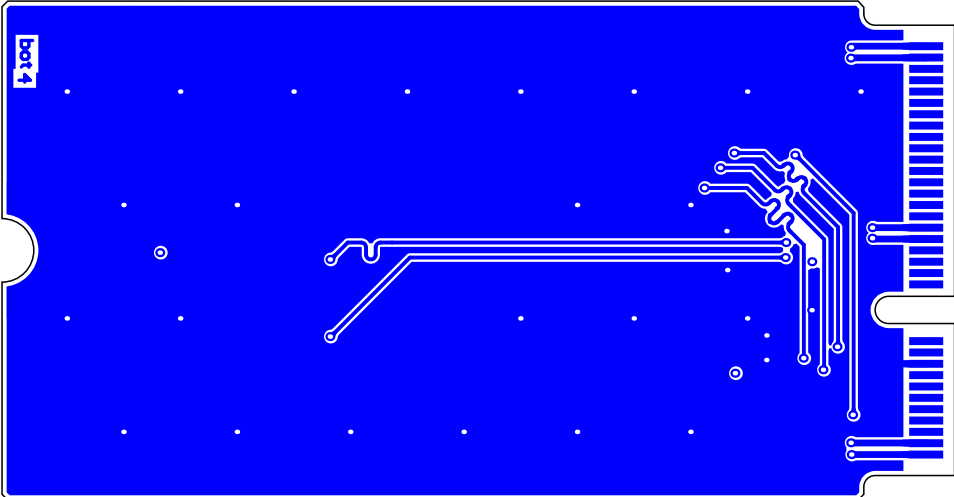
Layout documentation:

View from Top side (Scale 1:1)

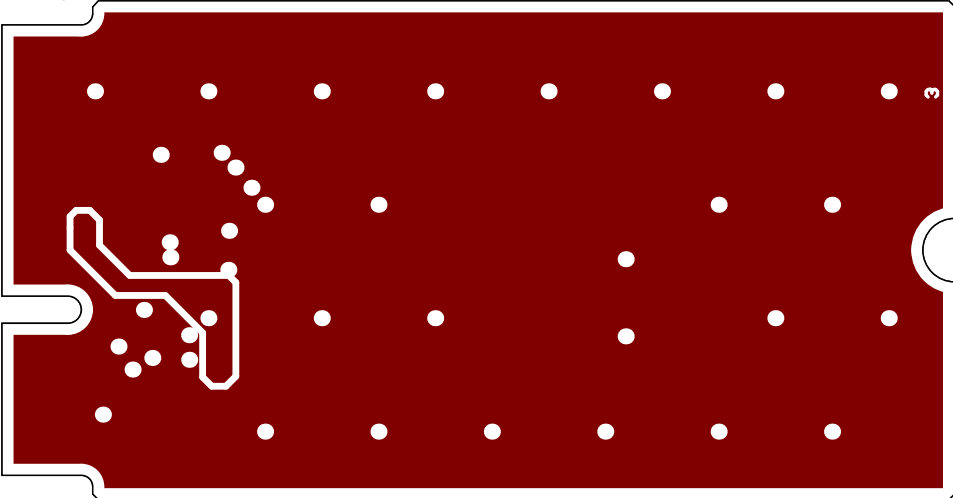
Top Layer (Scale 3)



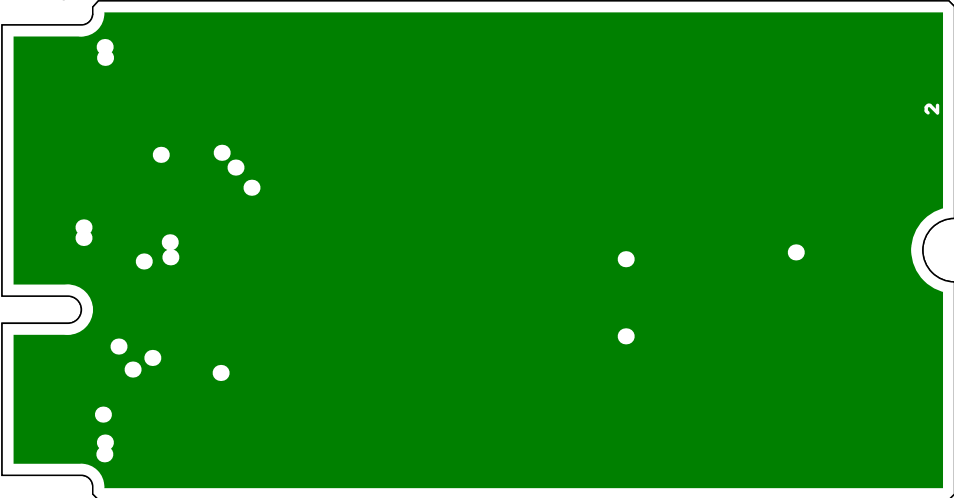
Bottom Layer (Scale 3)




Int2 (PWR) (Scale 3)



Int1 (GND) (Scale 3)



			Project: uSD Card Module	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: uSD_Module_layout.PCBDwf
Date: 01.02.2021			Drawn by: MAYERPH	Page: 1 of 1
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\uSD_Module\uSD_Module_layout.PCBDwf				