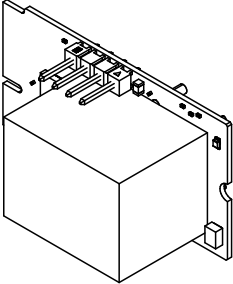
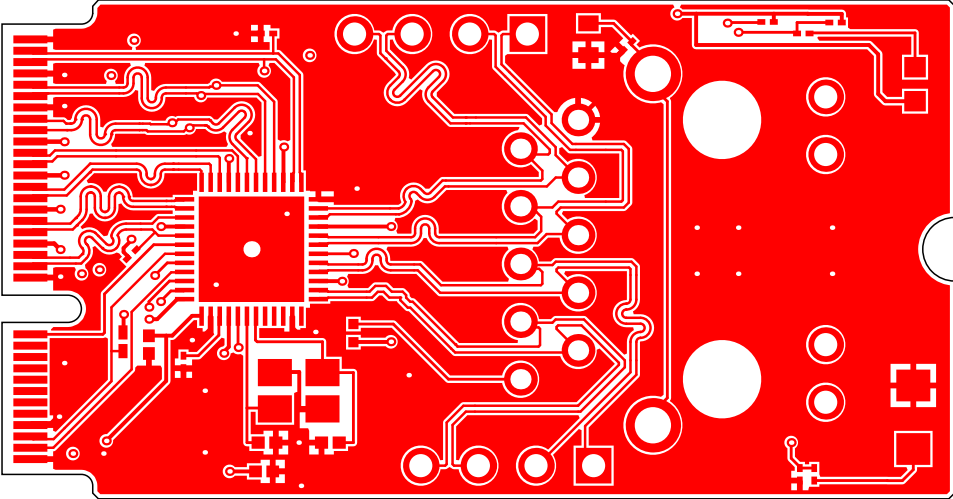


Layout documentation:

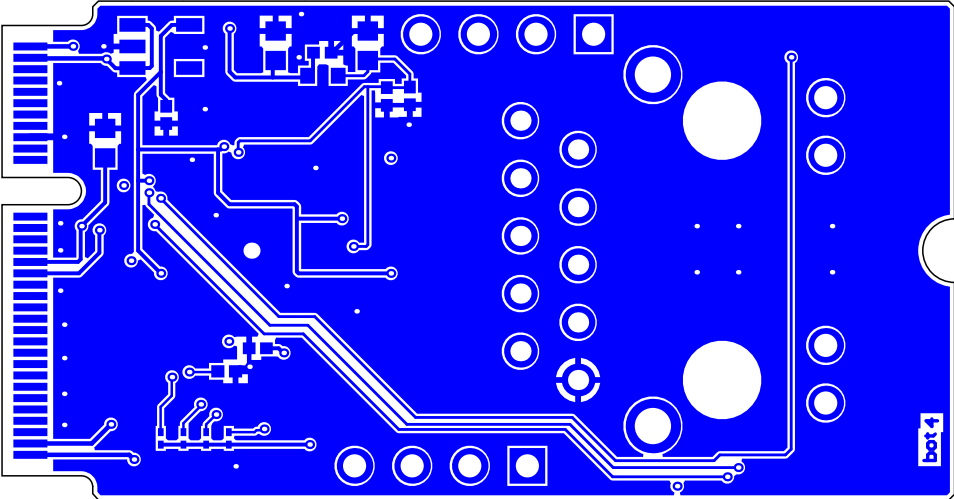
View from Top side (Scale 1:1)



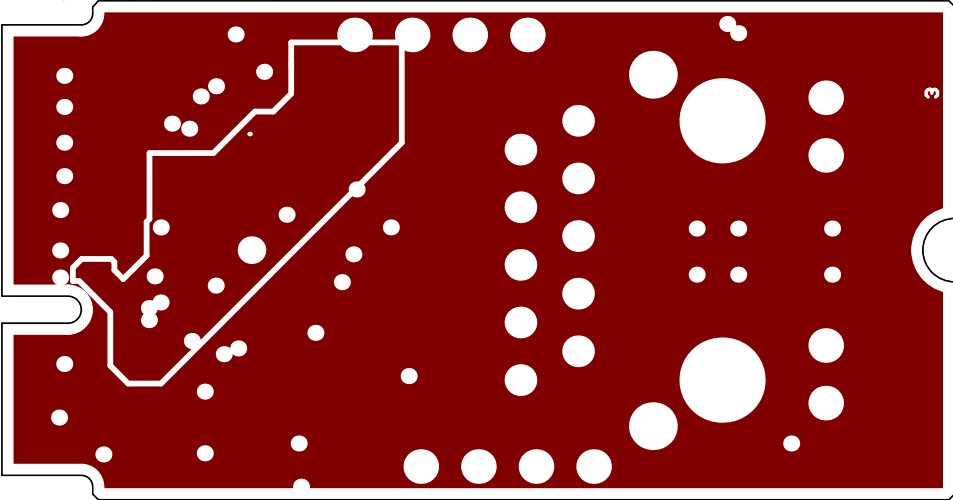
Top Layer (Scale 3:1)



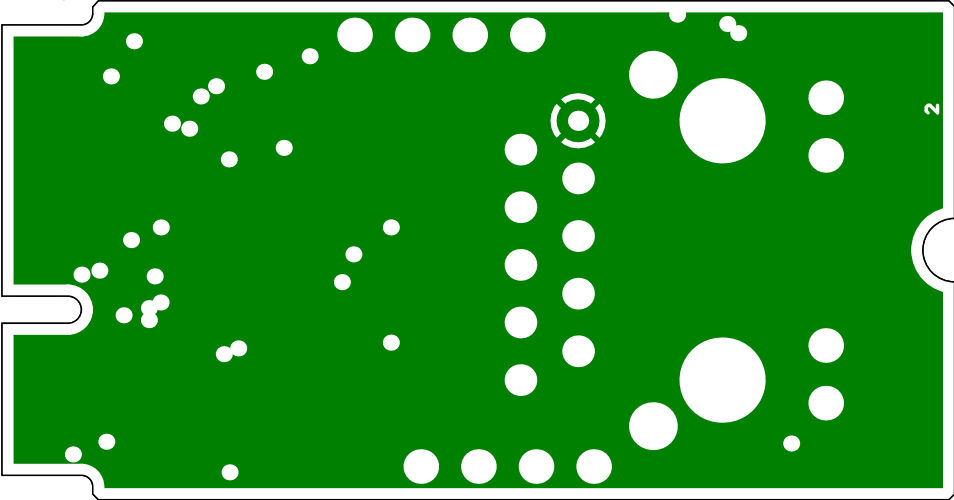
Bottom Layer (Scale 3:1)



Int2 (PWR) (Scale 3:1)



Int1 (GND) (Scale 3:1)



|  |         |               |                                 |                                 |
|--|---------|---------------|---------------------------------|---------------------------------|
|  |         |               | Project:<br><br>Ethernet Module |                                 |
| Drawing number:  | Rev: PA | Format:<br>A3 | Laboratory: IIS                 | Sheet: ETH_Module_layout.PCBDwf |
| Date: 16/04/2024   |         |               | Drawn by: Mattia Sinigaglia     | Page: 1 of 1                    |
| File: D:\Project\2022\PCB\Open Sourcing PCB\FMC Peripheral Board\04_Expansion_Modules\Ethernet_Module\ETH_Module_layout.PCBDwf |         |               |                                 |                                 |