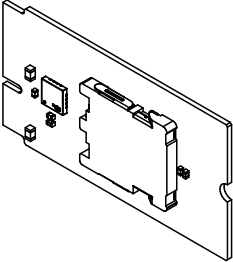
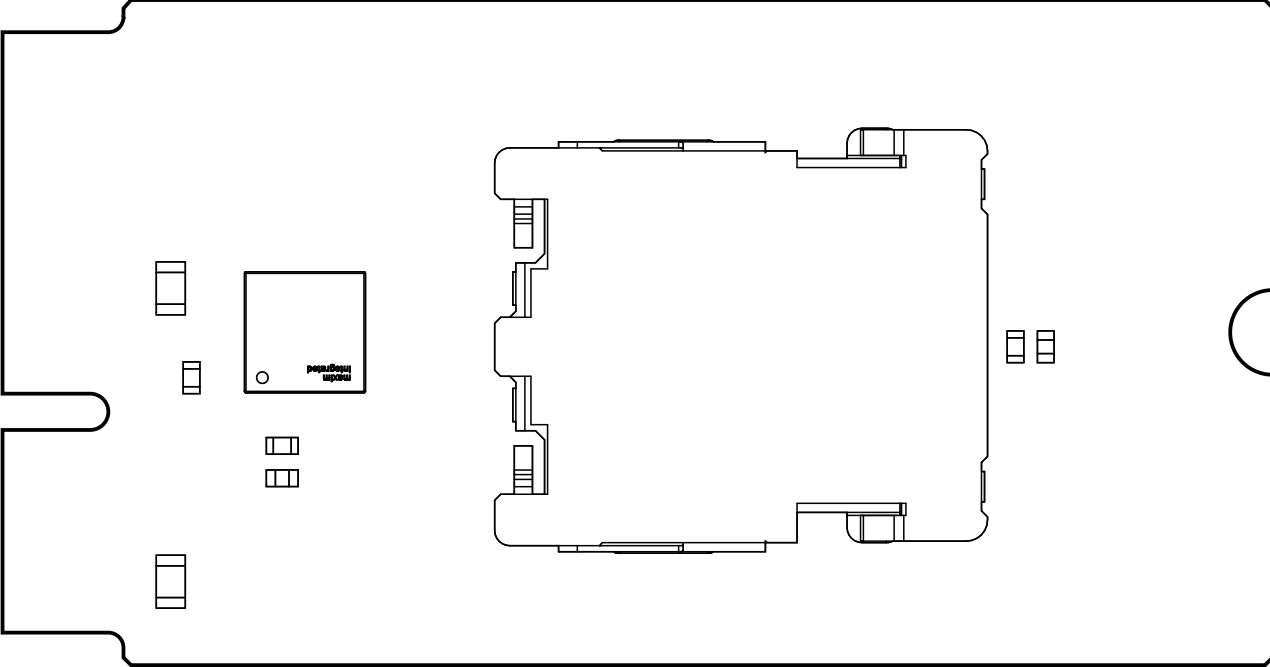


Assembly documentation:

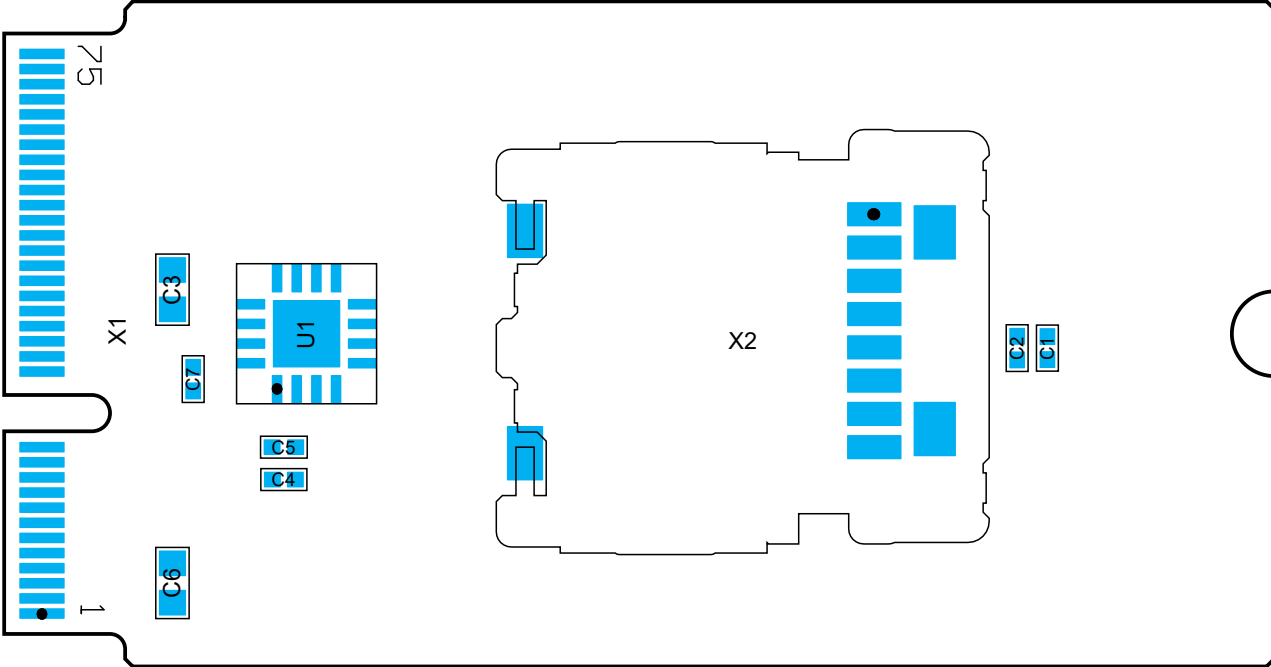
View from Top side (Scale 1:1)



View from Top side (Scale 4:1)



View from Top side (Scale 4:1)



Bill Of Materials

Line #	Quantity	Designator	Comment	Value	Tolerance	Power	Footprint	Manufacturer	Manufacturer Part Number
1	2	C1, C4	1uF	1uF	10%		CAPC1005X60X30LL05T10	Kemet	C0402C105K9PACTU
2	3	C2, C5, C7	100nF	100nF	10%		CAPC1050X55X23LL05T13	Samsung Electro-Mechanics	CL05B104KO5NNNC
3	2	C3, C6	10uF	10uF	20%		CAPC1608X90X35LL15T15	AVX Interconnect / Elco	06036D106MAT2A
4	1	U1	MAX13035EETE+				FP-T1644-4-MFG	Maxim	MAX13035EETE+
5	1	X2	ST1W008S4AR1500				ST1W008S4AR1500	JAE Electronics	ST1W008S4AR1500

Notes:

- 1. Pin 1 respectively marked with dot
- 2. Cathode marked with K

			Project: uSD Card Module	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: uSD_Module_assembly.PCBDwf
Date: 03.02.2021			Drawn by: MAYERPH	Page: 1 of 1
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\uSD_Module\uSD_Module_assembly.PCBDwf				