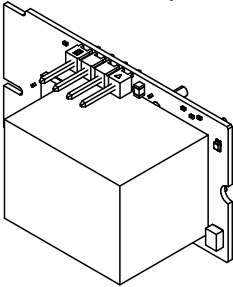
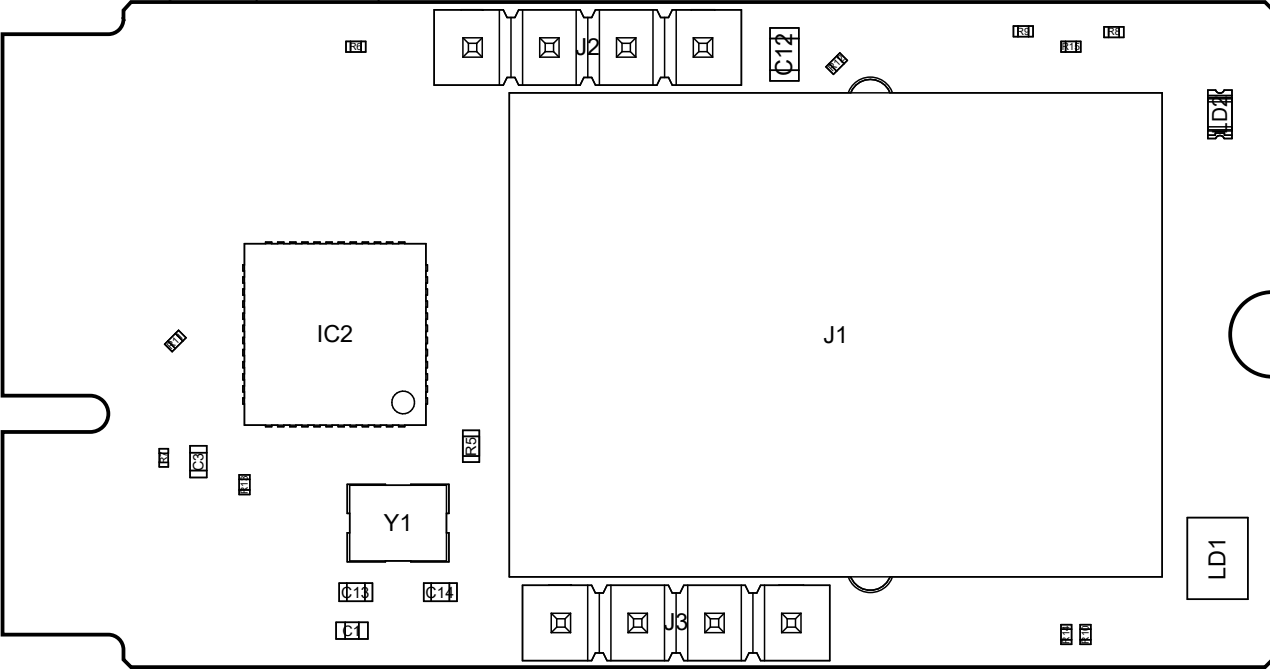


Assembly documentation:

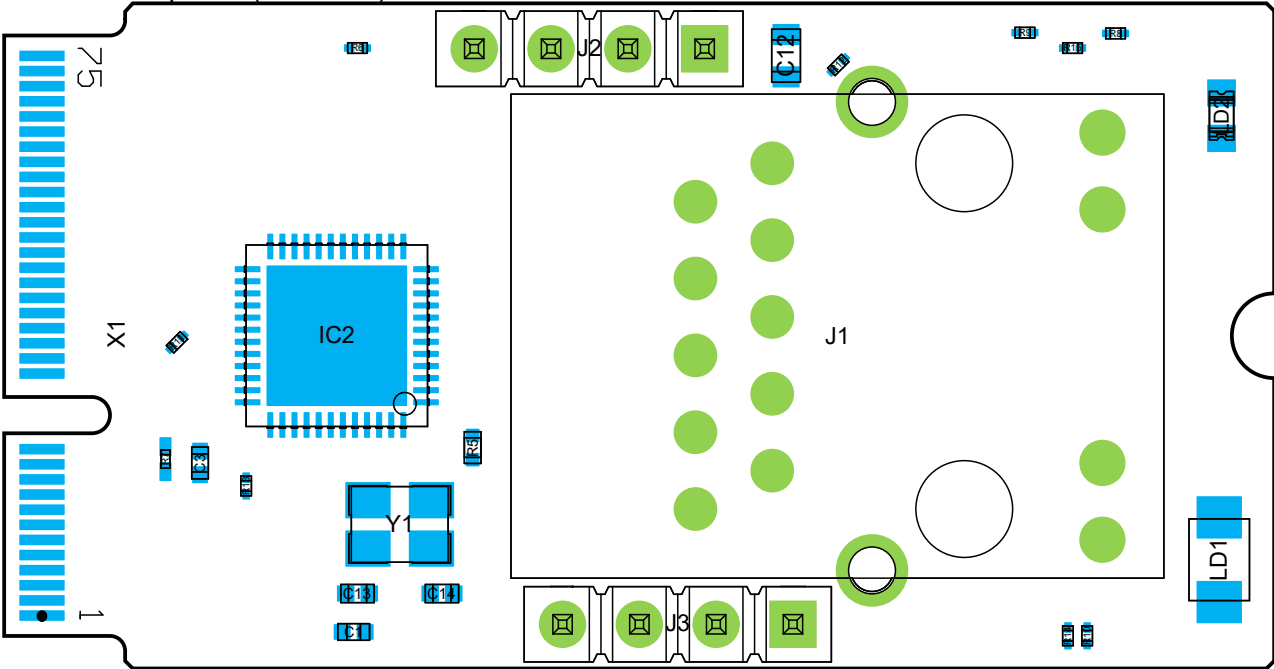
View from Top side (Scale 1:1)



View from Top side (Scale 4:1)



View from Top side (Scale 4:1)



Bill Of Materials

Line #	Quantity	Designator	Value	Description	P/N	Manufacturer
	3	C1, C2, C10	1uF			Kemet
	4	C3, C4, C5, C7	100nF	Chip Capacitor, 100 nF, +/- 10%, 16 V, -55 to 125 degC, 0402 (1005 Metric), RoHS, Tape and Reel		Samsung Electro-Mechanics
	3	C6, C8, C9	10uF			AVX Interconnect / Elco
	3	C11, C13, C14		0402YA270JAT2A		AVX Interconnect / Elco
	1	C12	1nF	CAP 1nF 10V ±1% 0603 (1608 Metric) Thickness 1mm SMD		
	1	FB1		Ferrite Bead		
	1	IC1		Integrated Circuit		
	1	IC2		GbE PHY with RGMII interface & EEE feature, 1.8V & 1.5V		Realtek
	1	J1		Connector		
	2	J2, J3		CONN HEADER VERT 4POS 2.54MM		
	1	LD1		LED		
	1	LD2		LED		
	9	R1, R2, R3, R4, R6, R8, R9, R11, R14		CRCW02014K70FKED		Vishay Dale
	1	R5		ERJ2GE0R00X Chip Resistor, 0 Ohm, 0.1 W, -55 to 155 degC, 0402 (1005 Metric), RoHS, Tape and Reel		Panasonic
	1	R7		CRCW02011K50FKED		
	1	R10		CRCW02014K70FKED		Vishay Dale
	1	R12		CRCW02011M00FKED		Vishay Dale
	1	R13		CRCW02012K49FKED		Vishay Dale
	1	R15		CRCW02014K70FKED		Vishay Dale
	1	Y1		Crystal or Oscillator		

Project:  
**Ethernet Module**

Drawing number:  
Date: 16/04/2024

Rev: PA

Format:  
**A3**

Laboratory: IIS  
Drawn by: Mattia Sinigaglia

Sheet: ETH\_Module\_assembly.PCBDwf  
Page: 1 of 1

File: D:\Project\2022\PCB\Open Sourcing PCB\FMC Peripheral Board\04\_Expansion\_Modules\Ethernet\_Module\ETH\_Module\_assembly.PCBDwf