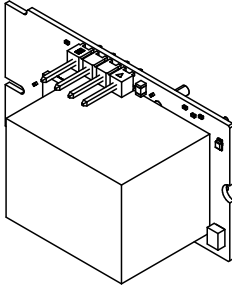


Connectors documentation:

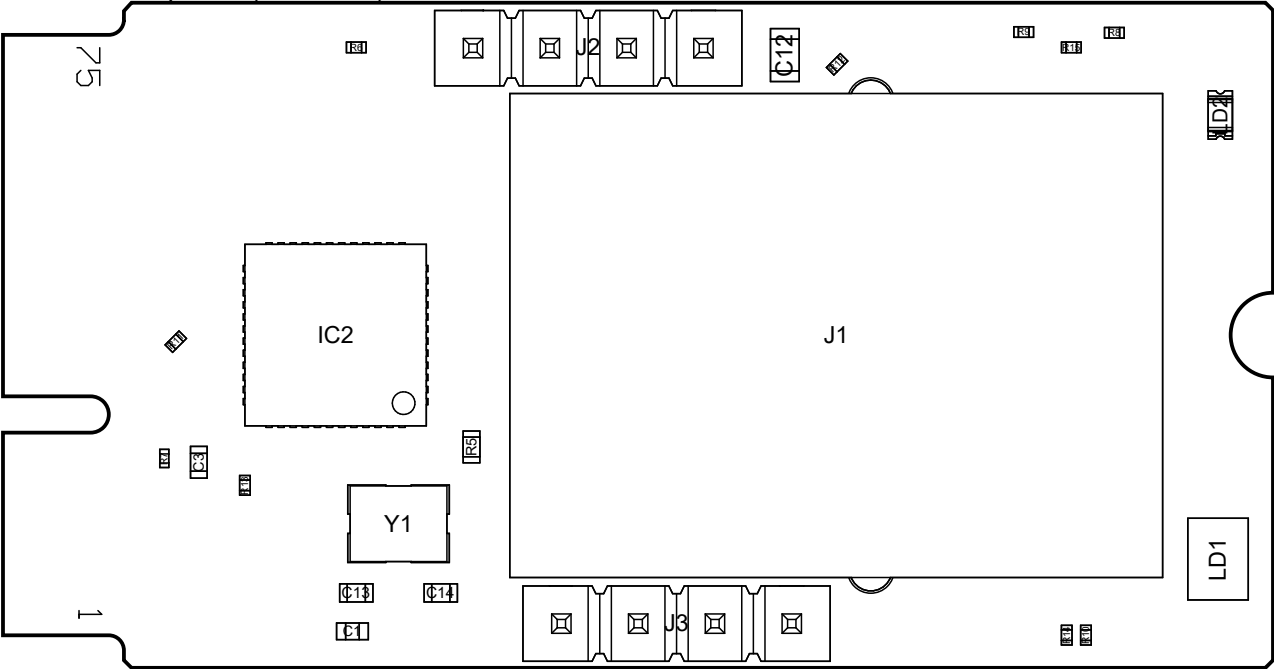
View from Top side (Scale 1:1)




Signals

FMC_GP_D_N7	RXCTL/ PHY_AD2
FMC_GP_D_P7	RXD0
FMC_GP_D_N6	RXD1
FMC_GP_D_P6	RXD2
FMC_GP_D_N5	RXD3
FMC_GP_D_P5	RXC
FMC_GP_D_N4	PMEB
FMC_GP_D_P4	TXC
FMC_GP_D_N3	TXD0
FMC_GP_D_P3	TXD1
FMC_GP_D_N2	TXD2
FMC_GP_D_P2	TXD3
FMC_GP_D_N1	TXCTL
FMC_GP_D_N0	MDIO
FMC_GP_S7	MDC
FMC_GP_D_P1	INTB
FMC_GP_D_P0	PHYSTRB

View from Top side (Scale 4:1)



			Project:  Ethernet Module	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: ETH_Module_connectors.PCBDwf
Date: 16/04/2024			Drawn by: Mattia Sinigaglia	Page: 1 of 1
File: D:\Project\2022\PCB\Open Sourcing PCB\FMC Peripheral Board\04_Expansion_Modules\Ethernet_Module\ETH_Module_connectors.PCBDwf				