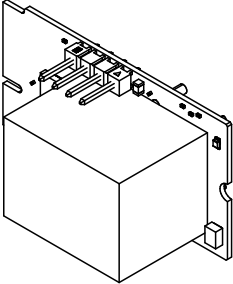
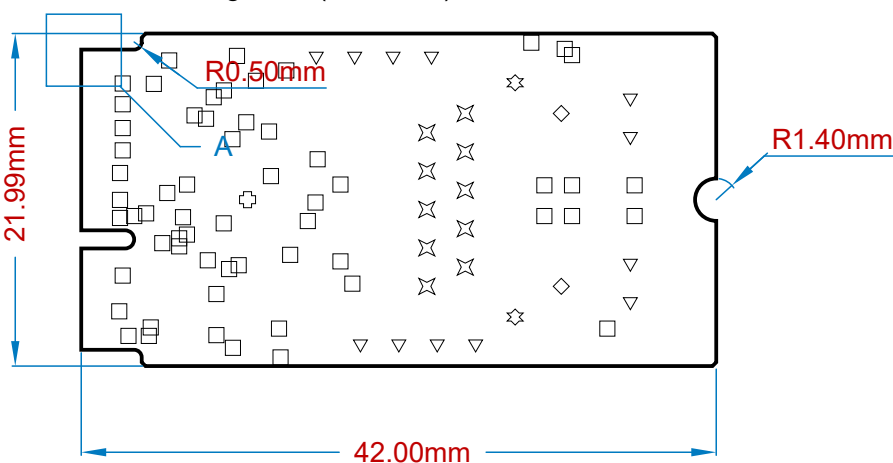


Manufacturing documentation:

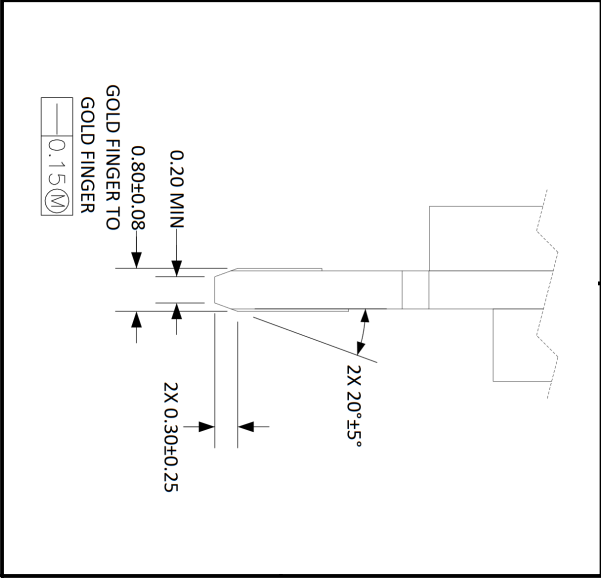
View from Top side (Scale 1:1)



Drill Drawing View (Scale 2:1)



DETAIL A



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
□	60	0.20mm	Plated	
⊕	1	0.71mm	Plated	
✕	10	0.89mm	Plated	
▽	12	1.02mm	Plated	
☆	2	1.60mm	Plated	
◇	2	3.25mm	Non-Plated	
87 Total				

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber	Comments	Dk	Frequency
	Top Overlay			Legend	GTO			
Surface Material	Top Solder	0.025mm	SM-001	Solder Mask	GTS	black	4	1GHz
Nickel, Gold	Top Surface Finish	0.004mm		Surface Finish				
CF-004	Top Layer	0.035mm		Signal	GTL			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1			
Core		0.406mm	Core-034	Dielctric		-	4,6	1GHz
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Bottom Layer	0.035mm		Signal	GBL			
Nickel, Gold	Bottom Surface Finish	0.004mm		Surface Finish				
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	black	4	1GHz
	Bottom Overlay			Legend	GBO			
Total thickness: 0.829mm								

Notes:

- impedance matched stackup
- 0.1mm trace width / clearance

ETH zürich			Project: Ethernet Module	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: ETH_Module_fabrication.PCBDwf
Date: 16/04/2024			Drawn by: Mattia Sinigaglia	Page: 1 of 1
File: D:\Project\2022\PCB\Open Sourcing PCB\FMC Peripheral Board\04_Expansion_Modules\Ethernet_Module\ETH_Module_fabrication.PCBDwf				