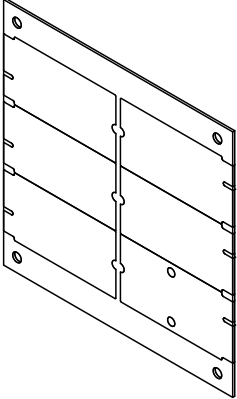
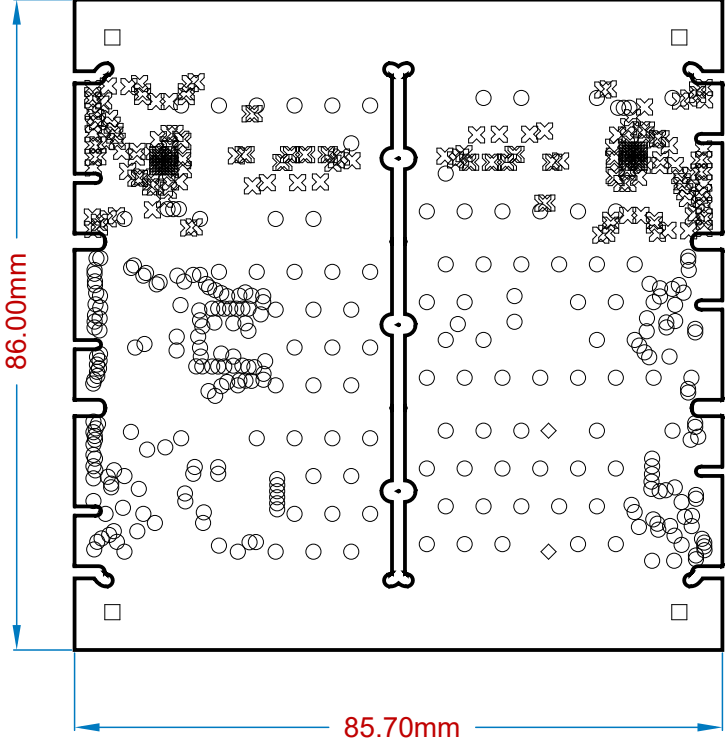


Manufacturing documentation:

View from Top side (Scale 1:2)



Drill Drawing View (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
⊗	216	0.15mm	Plated	
○	301	0.20mm	Plated	
◇	2	2.70mm	Plated	
□	4	3.20mm	Non-Plated	
	523 Total			

Layer Stack Legend

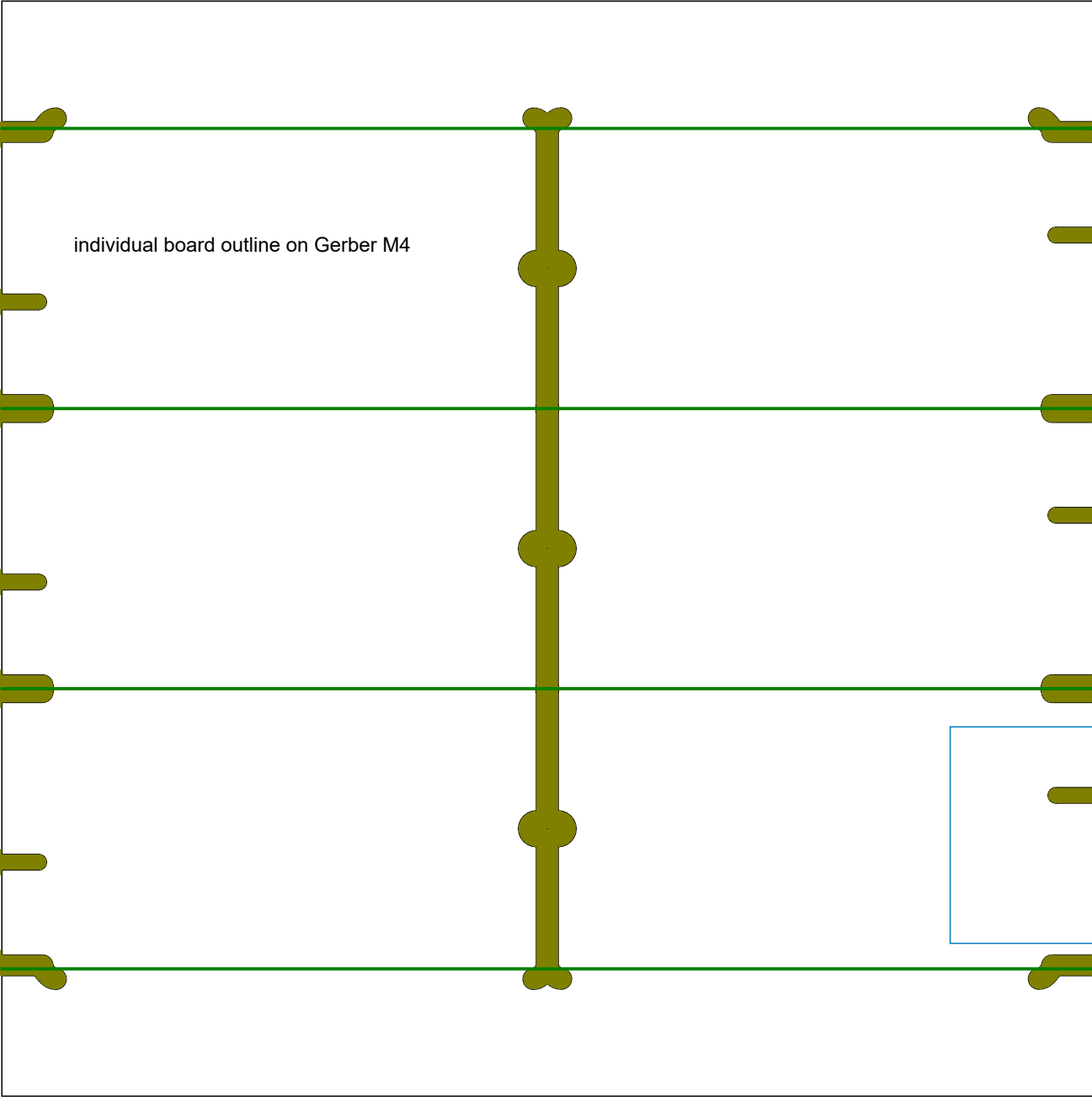
Material	Layer	Thickness	Dielectric Material	Type	Gerber	Dk	Comment
	Top Overlay			Legend	GTO		
Surface Material	Top Solder	0.025mm	SM-001	Solder Mask	GTS	4	black
Nickel, Gold	Top Surface Finish	0.004mm		Surface Finish			
CF-004	Top Layer	0.035mm		Signal	GTL		
Prepreg		0.112mm	PP-015	Dielectric		4.3	
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1		
Core		0.406mm	Core-034	Dielctric		4.6	
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2		
Prepreg		0.112mm	PP-015	Dielectric		4.3	
CF-004	Bottom Layer	0.035mm		Signal	GBL		
Nickel, Gold	Bottom Surface Finish	0.004mm		Surface Finish			
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	4	black
	Bottom Overlay			Legend	GBO		
Total thickness: 0.829mm							

Notes:

1. Impedance matched stack-up
2. 0.075mm trace width / clearance
3. Gold Fingers, Hard Gold, 20° Beveling
4. Via in Pad

ETH zürich			Project: FPGA Multiboard 0.8mm	
Drawing number:	Rev: A	Format: A3	Laboratory: IIS	Sheet: MultiBoard_0p8mm.PCBDwf
Date: 15.02.2021			Drawn by: MAYERPH	Page: 1 of 2
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\00_MultiBoardAssembly\Manufacturing\MultiBoard_0p8mm.PCBDwf				

Route Tool Path (Scale 2:1)



individual board outline on Gerber M4

Route tool path 1.6mm (Gerber .M8)

U-Scoring (Gerber .M7)

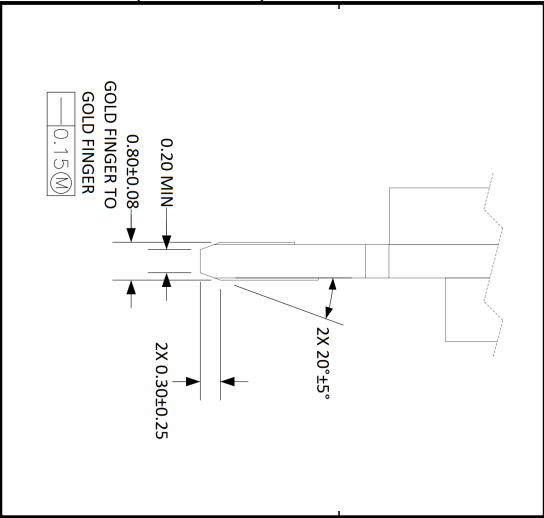
Card Key <1.6mm

U-Scoring

U-Scoring

U-Scoring

DETAIL A (Scale 4:1)



ETH zürich			Project: FPGA Multiboard 0.8mm	
Drawing number:	Rev: A	Format: A3	Laboratory: IIS	Sheet: MultiBoard_0p8mm.PCBDwf
Date: 15.02.2021			Drawn by: MAYERPH	Page: 2 of 2
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\00_MultiBoardAssembly\Manufacturing\MultiBoard_0p8mm.PCBDwf				