

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

FMC Test Module - FMC

Drawing number:

Rev: PA

Format:

Laboratory: IIS

Sheet: FMC.SchDoc

Date: 11.02.2021 11:37:12

Drawn by: mayerph

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File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\FMC_Test_Module\Schematic\FMC.SchDoc

A

B

C

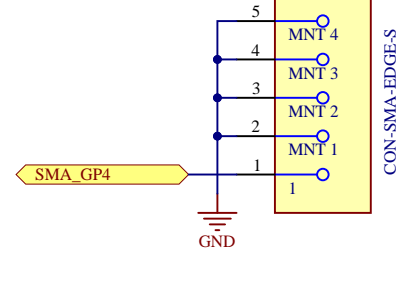
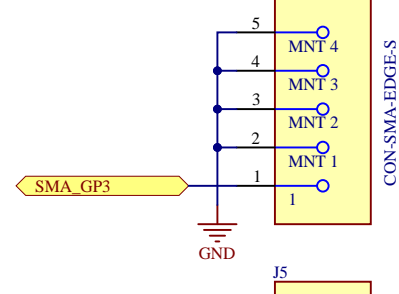
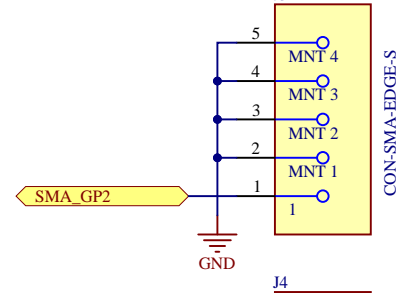
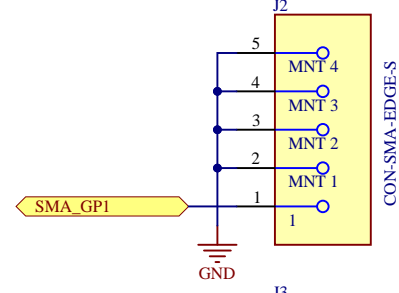
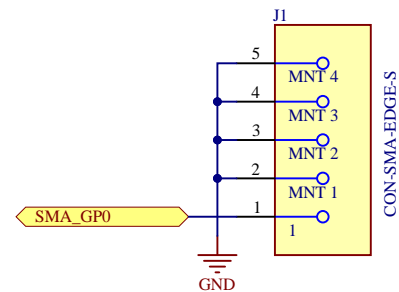
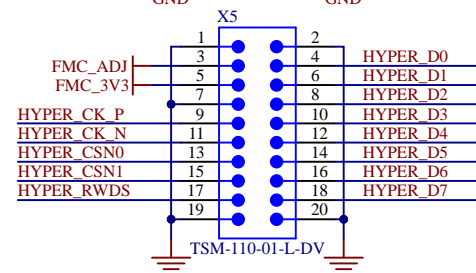
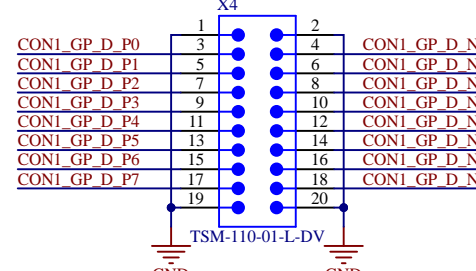
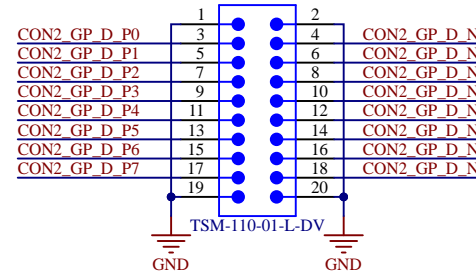
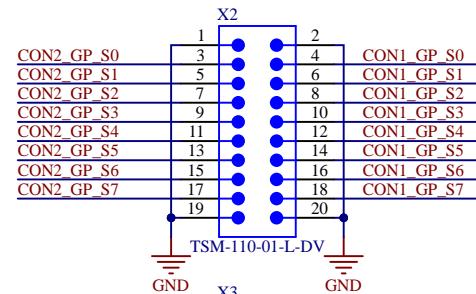
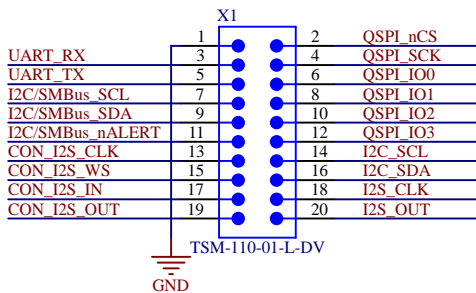
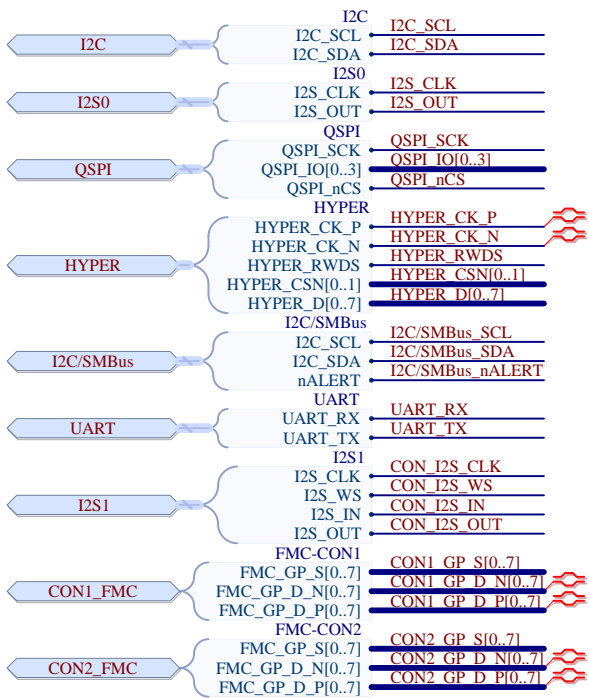
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FMC Test Module

Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: FMC_Test_Module.SchDoc
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