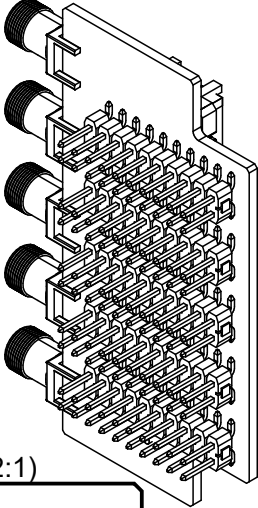
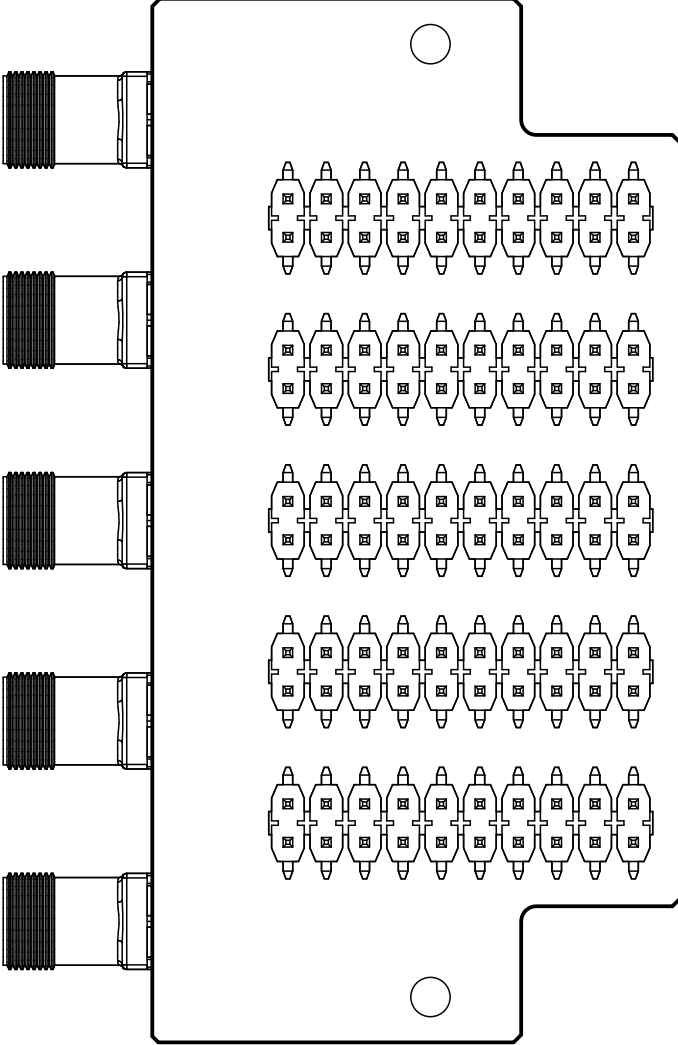


Assembly documentation:

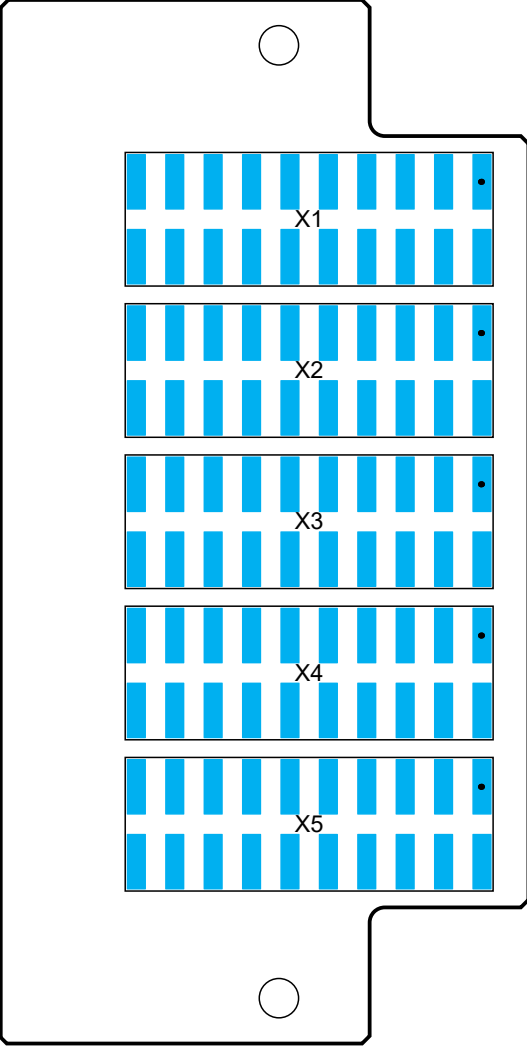
View from Top side (Scale 1:1)



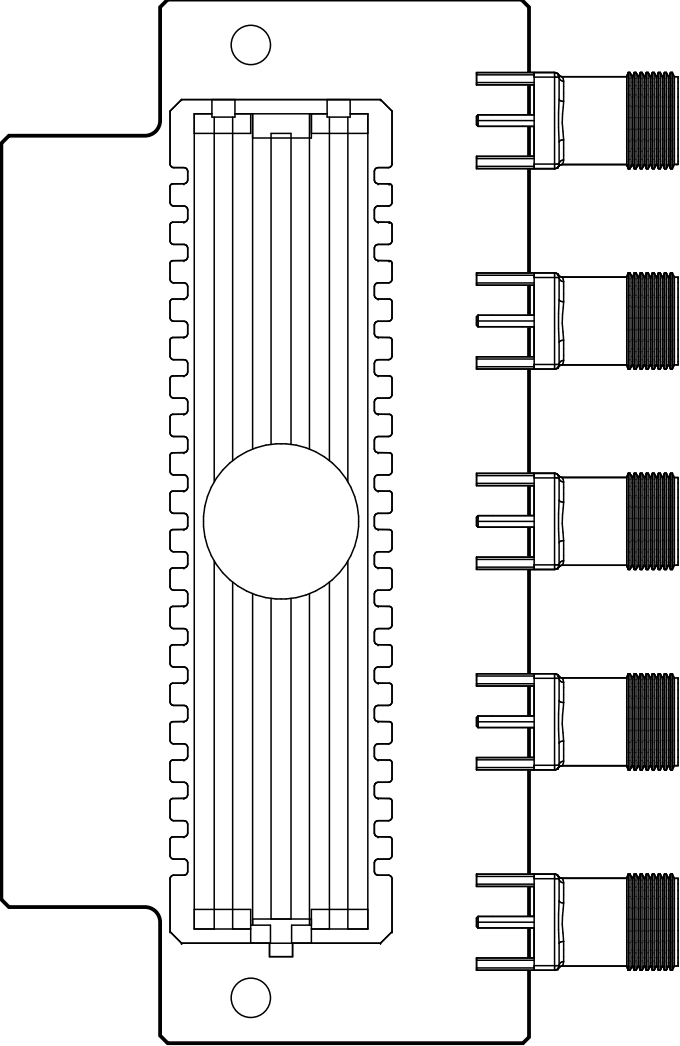
View from Top side (Scale 2:1)



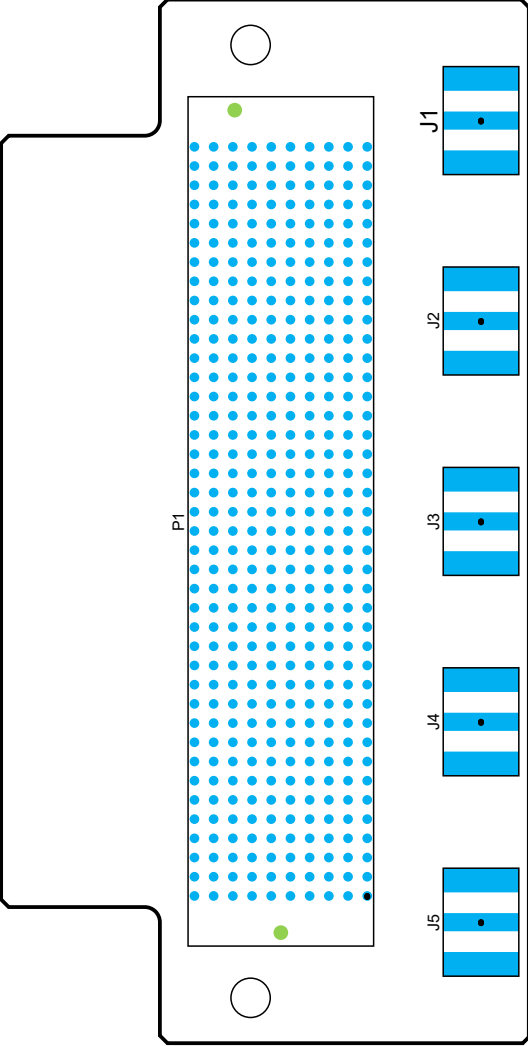
View from Top side (Scale 2:1)



View from Bottom side (Scale 2:1)



View from Bottom side (Scale 2:1)



Bill Of Materials

Line #	Quantity	Designator	Comment	Value	Tolerance	Power	Footprint	Manufacturer	Manufacturer Part Number
1	5	J1, J2, J3, J4, J5	CON-SMA-EDGE-S				FP-CON-SMA-EDGE-S-MFG	RF Solutions Ltd.	CON-SMA-EDGE-S
2	1	P1	ASP-134488-01				BTB-SM1-27-2V400M	Samtec	ASP-134488-01
3	5	X1, X2, X3, X4, X5	TSM-110-01-L-DV				SMTC-TSM-110-01-X-DV	Samtec	TSM-110-01-L-DV

Notes:

- 1. Pin 1 marked with dot
- 2. Cathode marked with K

Project:
FMC Test Module

Drawing number:
Date: 10.02.2021

Rev: A

Format:
A3

Laboratory: IIS
Drawn by: MAYERPH

Sheet: FMC_Test_Module_assembly.PCBDwf
Page: 1 of 1

File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\FMC_Test_Module\FMC_Test_Module_assembly.PCBDwf