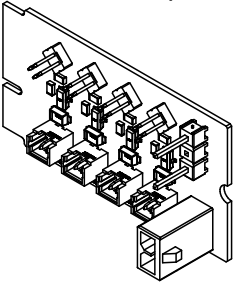
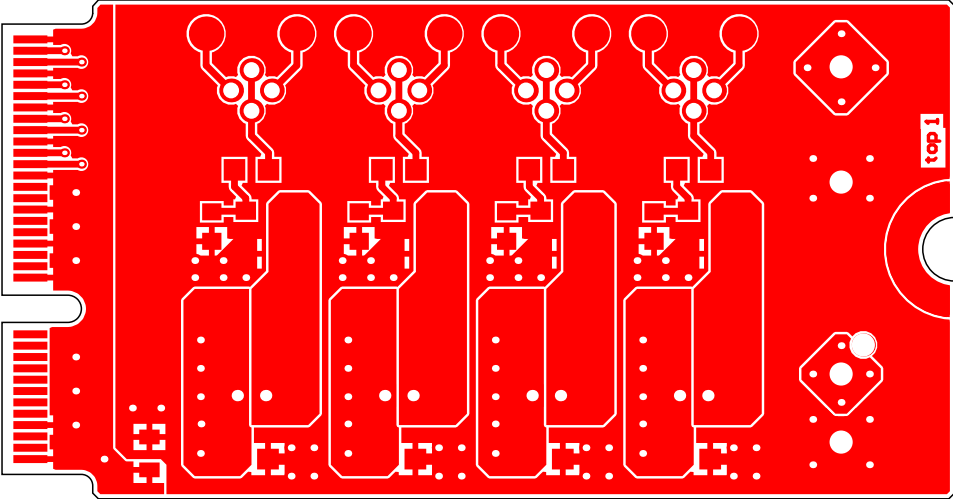


Layout documentation:

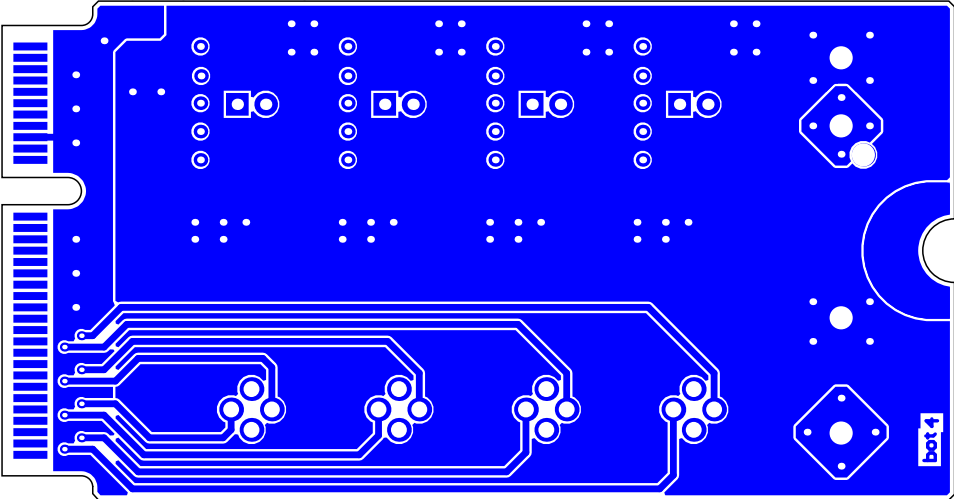
View from Top side (Scale 1:1)



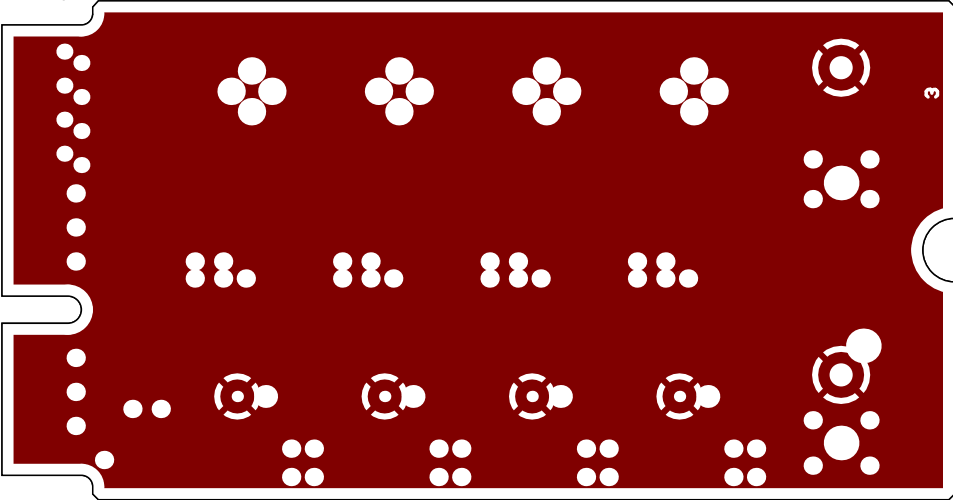
Top Layer (Scale 3:1)



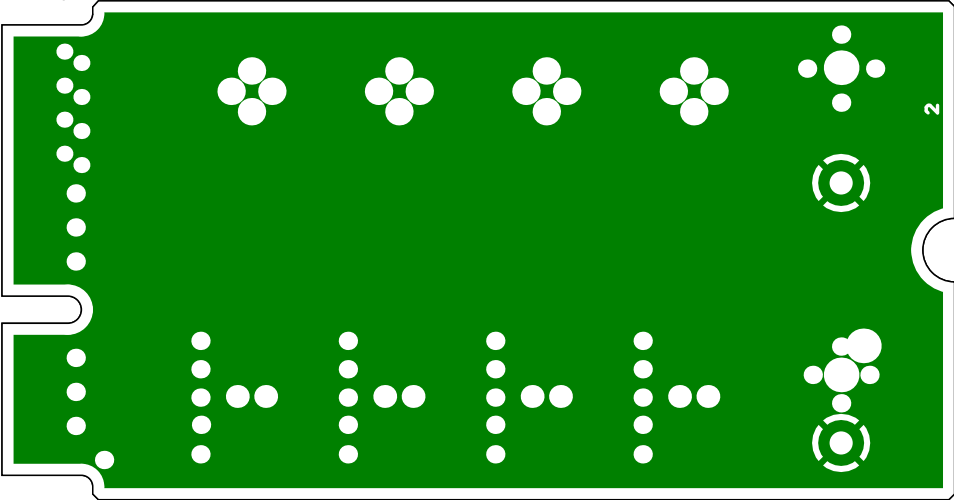
Bottom Layer (Scale 3:1)



Int2 (PWR) (Scale 3:1)



Int1 (GND) (Scale 3:1)



<b>ETH</b> zürich			Project: PWM Module	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: PWM_Module_layout.PCBDwf
Date: 17/01/2023			Drawn by: MAYERPH	Page: 1 of 1
File: C:\Users\admin\Desktop\tes\FMC Peripheral Board\PWM_Module\PWM_Module_layout.PCBDwf				