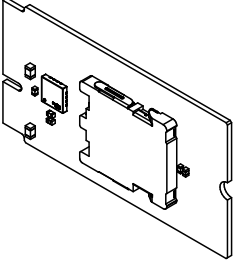


Connectors documentation:

View from Top side (Scale 1:1)

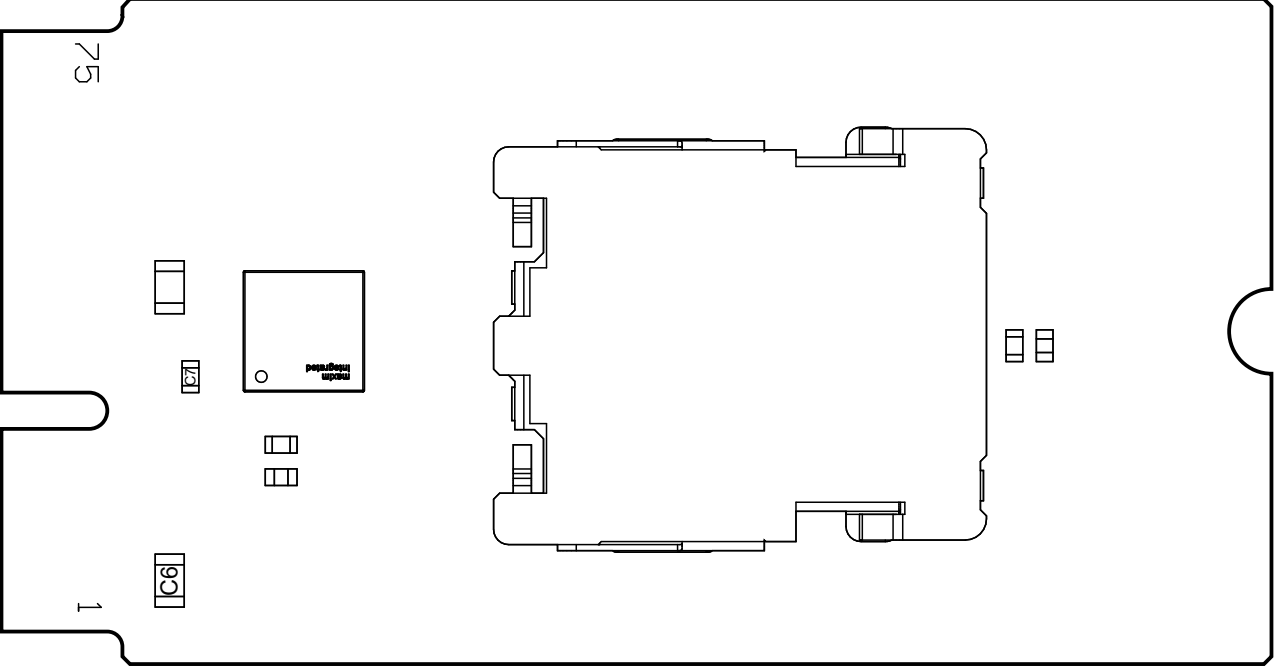



Singals

FMC_GP_S0	CLK/SCLK
FMC_GP_S1	CD_CMD/MISO
FMC_GP_S2	SD_D0/MISO
FMC_GP_S3	SD_D1
FMC_GP_S4	SD_D2
FMC_GP_S5	SD_D3/nCS
FMC_GP_S6	CLK_RET

V_FMC_ = [1.62V 3.2V]

View from Top side (Scale 4:1)



			Project: uSD Card Module	
Drawing number:	Rev: PA	Format:	Laboratory: IIS	Sheet: uSD_Module_connectors.PCBDwf
Date: 01.02.2021		A3	Drawn by: MAYERPH	Page: 1 of 1
File: C:\Users\phili\polybox\05_AltiumProjects\FPGA Peripheral Board\uSD_Module\uSD_Module_connectors.PCBDwf				