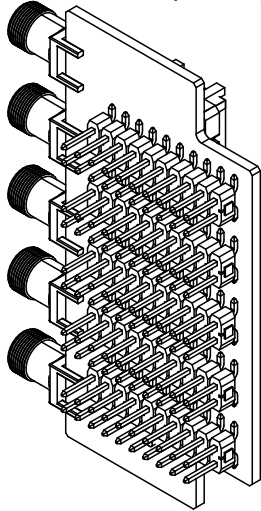
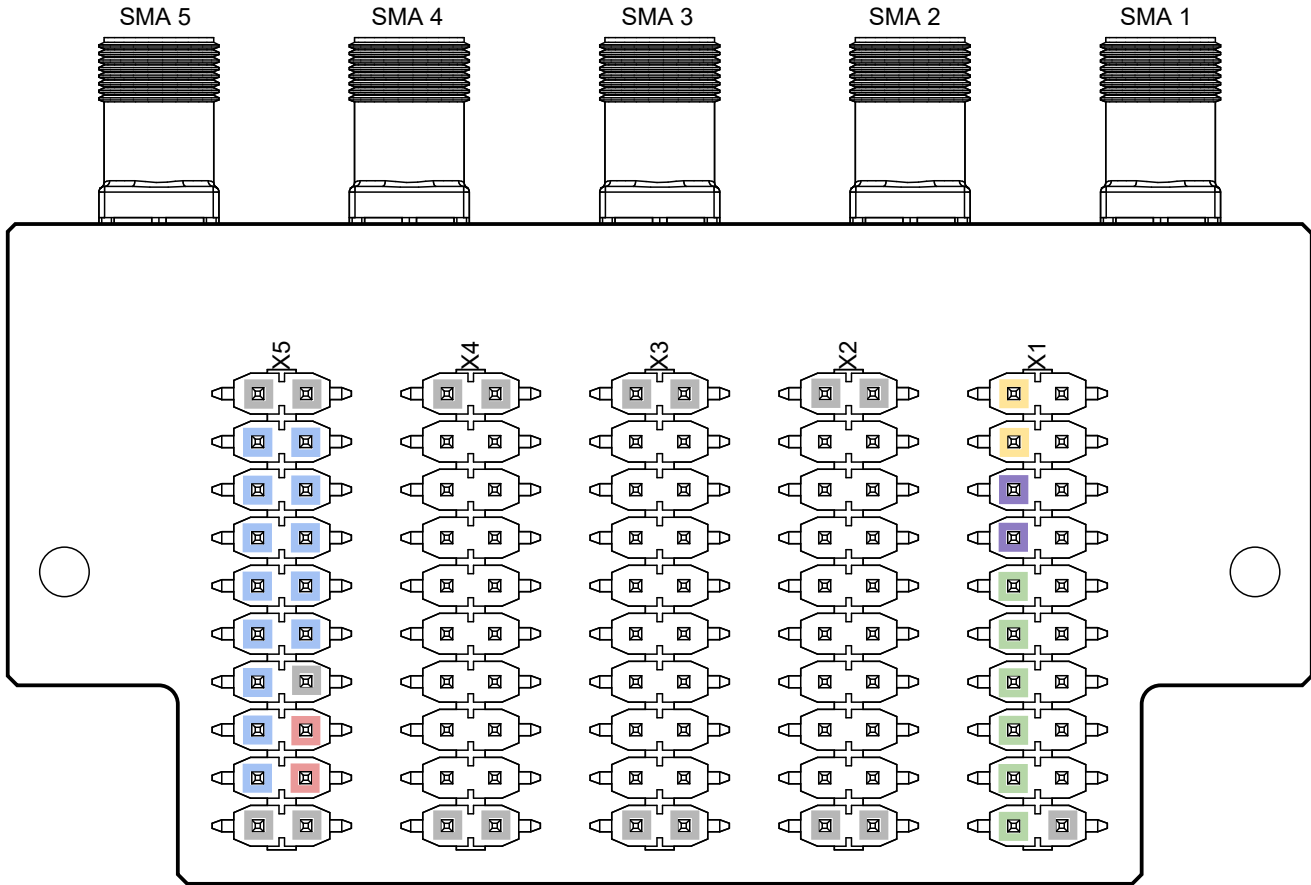


Connectors:

View from Top side (Scale 5:2)

View from Top side (Scale 1:1)



X5		X4		X3		X2		X1	
GND	GND	GND	GND	GND	GND	GND	GND	I2S OUT	CON I2S OUT
HYPER D7	HYPER RWDS	CON1 GP D N7	CON1 GP D P7	CON2 GP D N7	CON2 GP D P7	CON1 GP D S7	CON2 GP D S7	I2S CLK	CON I2S IN
HYPER D6	HYPER nCS1	CON1 GP D N6	CON1 GP D P6	CON2 GP D N6	CON2 GP D P6	CON1 GP D S6	CON2 GP D S6	I2C SDA	CON I2S WS
HYPER D5	HYPER nCS0	CON1 GP D N5	CON1 GP D P5	CON2 GP D N5	CON2 GP D P5	CON1 GP D S5	CON2 GP D S5	I2C SCL	CON I2S CLK
HYPER D4	HYPER CK_N	CON1 GP D N4	CON1 GP D P4	CON2 GP D N4	CON2 GP D P4	CON1 GP D S4	CON2 GP D S4	QSPI IO3	I2C/SMBus nA
HYPER D3	HYPER CK_P	CON1 GP D N3	CON1 GP D P3	CON2 GP D N3	CON2 GP D P3	CON1 GP D S3	CON2 GP D S3	QSPI IO2	I2C/SMBus SDA
HYPER D2	GND	CON1 GP D N2	CON1 GP D P2	CON2 GP D N2	CON2 GP D P2	CON1 GP D S2	CON2 GP D S2	QSPI IO1	I2C/SMBus SCL
HYPER D1	FMC 3V3	CON1 GP D N1	CON1 GP D P1	CON2 GP D N1	CON2 GP D P1	CON1 GP D S1	CON2 GP D S1	QSPI IO0	UART TX
HYPER D0	FMC ADJ	CON1 GP D N0	CON1 GP D P0	CON2 GP D N0	CON2 GP D P0	CON1 GP D S0	CON2 GP D S0	QSPI SCK	UART RX
GND	GND	GND	GND	GND	GND	GND	GND	QSPI nCS	GND