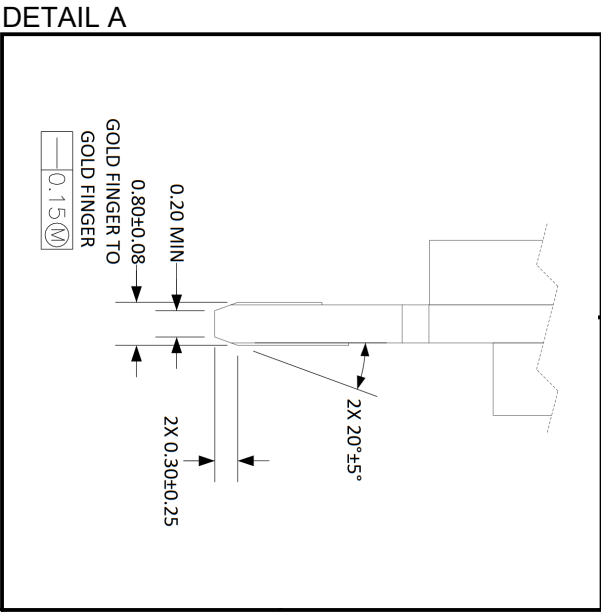
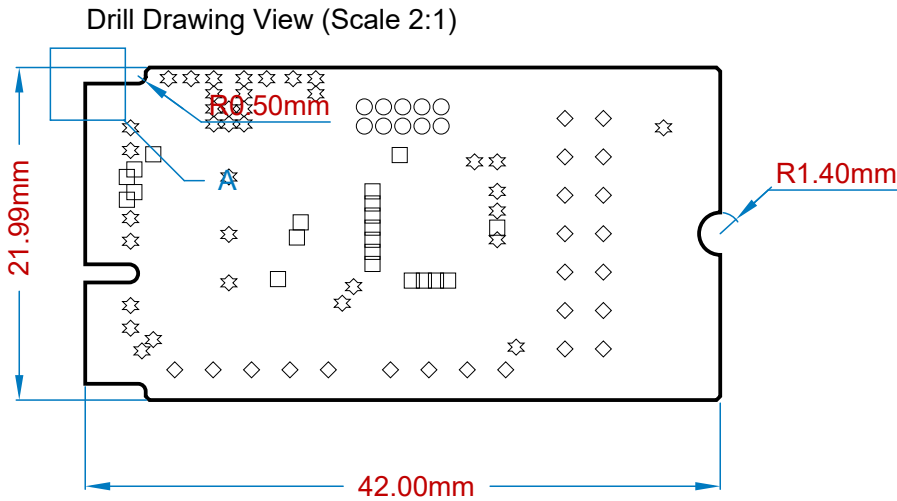
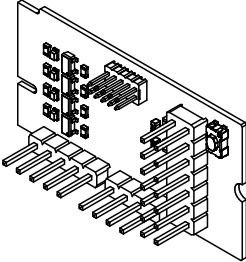


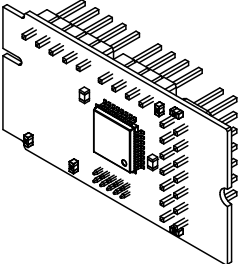
Manufacturing documentation:



View from Top side (Scale 1:1)



View from Bottom side (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
□	21	0.20mm	Plated	
☆	36	0.30mm	Plated	
○	10	0.70mm	Plated	
◇	23	1.10mm	Plated	
90 Total				

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber	Comments	Dk	Frequency
	Top Overlay			Legend	GTO			
Surface Material	Top Solder	0.025mm	SM-001	Solder Mask	GTS	black	4	1GHz
Nickel, Gold	Top Surface Finish	0.004mm		Surface Finish				
CF-004	Top Layer	0.035mm		Signal	GTL			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Int1 (GND)	0.035mm		Internal Plane	GP1			
Core		0.406mm	Core-034	Dielctric		-	4,6	1GHz
CF-004	Int2 (PWR)	0.035mm		Internal Plane	GP2			
Prepreg		0.112mm	PP-015	Dielectric		-	4,3	1GHz
CF-004	Bottom Layer	0.035mm		Signal	GBL			
Nickel, Gold	Bottom Surface Finish	0.004mm		Surface Finish				
Surface Material	Bottom Solder	0.025mm	SM-001	Solder Mask	GBS	black	4	1GHz
	Bottom Overlay			Legend	GBO			
Total thickness: 0.829mm								

- Notes:
- impedance matched stackup
  - 0.1mm trace width / clearance

Project:  
**UART Module**

Drawing number:  
Date: 21/12/2022

Rev: PA

Format:  
**A3**

Laboratory: IIS

Drawn by: MAYERPH

Sheet: UART\_Module\_fabrication.PCBDwf

Page: 1 of 1

File: C:\Users\admin\Desktop\tes\FMC Peripheral Board\UART\_Module\UART\_Module\_fabrication.PCBDwf