

## Connectors documentation:

## M.2 Signals

FMC_GP_D_N4	UART_RTS
FMC_GP_D_P4	UART_CTS
FMC_GP_D_N3	UART_RX
FMC_GP_D_P3	UART_TX

JTAG (P1)

1	1V8
2	JTAG_TMS-SWDIO
3	GND
4	JTAG_TCK-SWDCLK
5	GND
6	JTAG_TDO-SWO
7	NC
8	JTAG_TDI
9	GND
10	JTAG_RST

### UART to FPGA (P3)

1	UART_TX
2	UART_RX
3	UART_CTS
4	UART_RTS
5	GND

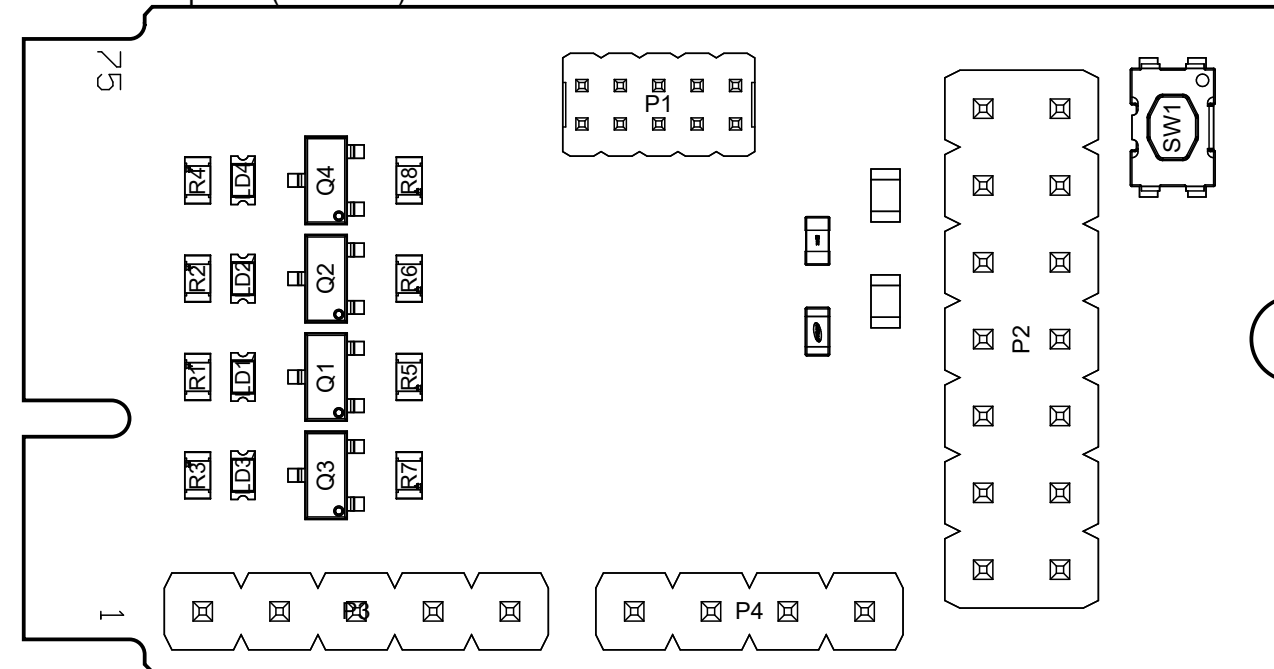
### External GPIO (P2)

1	1V8
2	GND
3	EXT_I2C_SCL
4	EXT_I2C_SMBA
5	EXT_I2C_SDA
6	EXT_TIM2_CH1(AIN)(PWM)
7	EXT_TIM1_CH1(PWM)
8	EXT_TIM2_CH2(AIN)(PWM)
9	EXT_SPI_CS(AIN)
10	EXT_SPI_SCK(AIN)
11	EXT_SPI_MISO(AIN)
12	EXT_SPI_MOSI(AIN)
13	1V8
14	GND

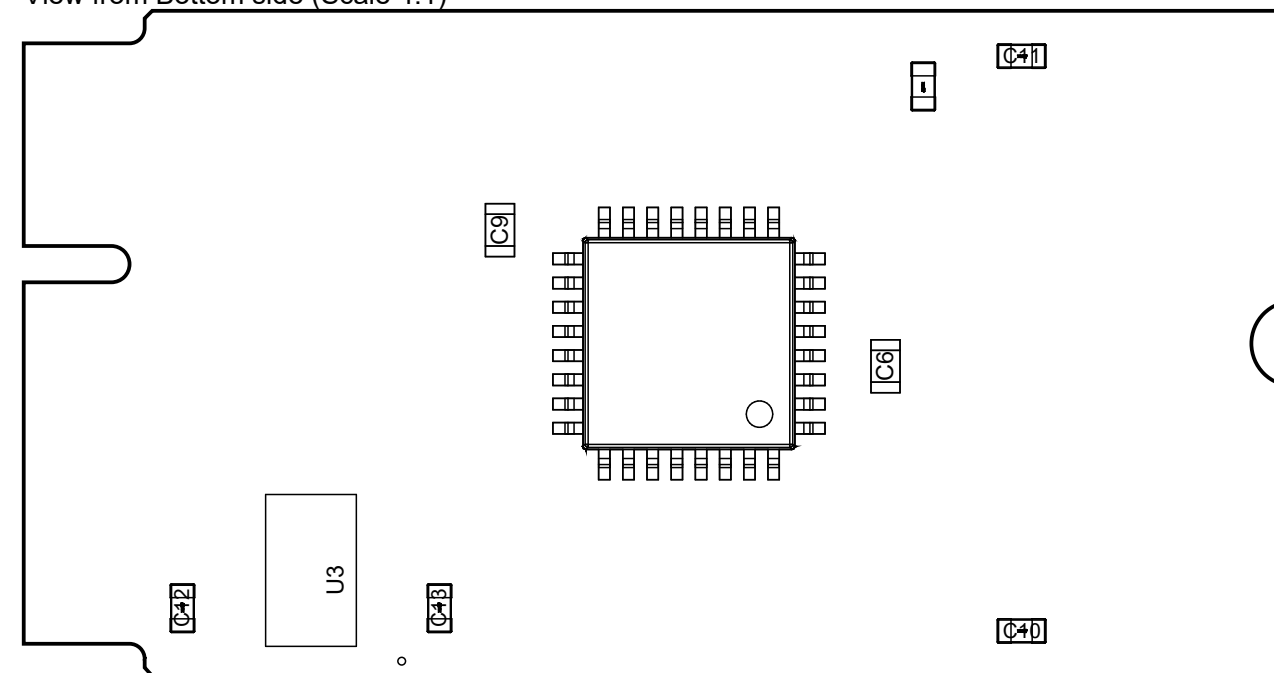
### External UART (P4)

1	1V8
2	EXT_UART_TX(AIN)
3	EXT_UART_RX(AIN)
4	GND

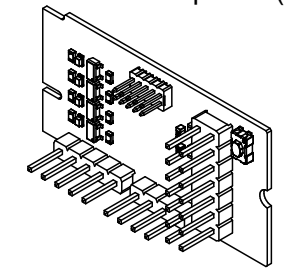
View from Top side (Scale 4:1)



View from Bottom side (Scale 4:1)



View from Top side (Scale 1:1)



View from Bottom side (Scale 1:1)

