Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

School of Computer Science and Engineering (SCOPE)
B.Tech. CSE/CPS/AL AND ML PAT CATEGORY

PAT INTERNSHIP ALREADY STARTED – MEETNG WITH THE VIT GUIDE
ONCE IN TWO WEEKS AND UPDATING THE PAT INTERNSHIP WORK

Program: B.Tech. CSE   **Batch: 2019-2023**   **Course Code: CSE1904**

Register No.: 19BCE1027
Name of the Student: Aryaman Mishra   **Mobile No.6390159999**

PAT INTERSHIP Company Name:AMD (Advanced Micro Devices)	
Internship start date :8th August 2022 Internship end date (Approximate):20th May 2023	

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Technology Working on and Work in Progress:
- HIP Architecture
- ROCm Stack for High Computing Operations and Non-Graphical Functions
- HIPIFY-CLANG
- HIPIFY-PERL
- Work in Progress: Test-hipify CLI in Linux Environment

Dec 5 to Dec 9 2022 **Week 1 Report**	**While processing sample named System Wide Atomics,the executable rendered all user permissions on device as read-only.Ticket was lodged to rectify and new docker image was pulled to continue work on testhipify to run other samples by Python.**
Dec 12 to Dec 16 2022 **Week 2 Report**	**Recompilation of all samples was performed one-by-one specifically. Installation of CUDA to support AMD Architecture was performed.**
Dec 26 to Dec 30 2022	**Patch encodings were giving out patch rejection files so provisions were made to apply all patches on samples correctly.CUDA-kit dependency issue was resolved.Non-supported HIPIFY functions were debugged and deprecated functions of RocXT and ROCtracer were updated.**

Meeting with guide	Regular / ~~Irregular~~

Aryaman 10/1/23

Signature of the Student with date

Name & Signature of the VIT Guide with date