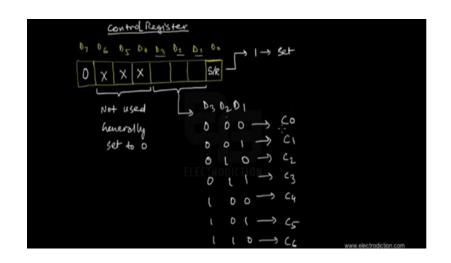
CAT 2 Revision

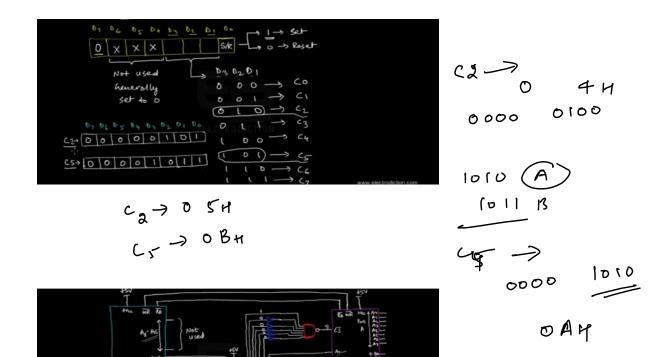
Operate 8255A in BSR mode & write a program to do the following.

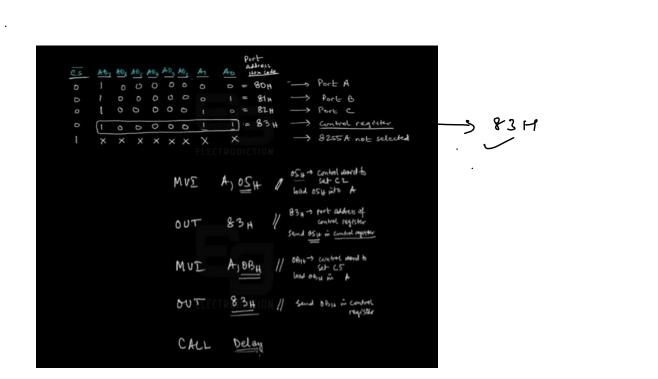
* Reset lines C2 & C5

* Reset lines C2 & C5 after 0.3 Sec









8255A

```
A, 044 //
MVI
         83#
OUT
        A, OA# 11
IVM
OUT
         83 H
```

```
76
0013
                    H LT
Subroutine Delay program ]
                                          3MHz
             Delay: ~LXI
        FF
                JI: DEX
2003
        23
                              2003H (Ji)
        C2
                    JNZ
2004
                                           1- LXI -> 10T -> 10X0.33: 43.
        03
2005
        20
2006
                              5.28 x 65535
                    · RET
2007
        09
                                                         = 6.6 as
                              = [0.34] secs = 0.3 secs
```

Assume selected counter is Counter-1, Mode for Square wave is Mode-3, Address for Counter 1 is 82H

Calculation of CWR:

			-			-	
0	1 1	1	1	0 1	1	1	1 1

CWR is (77H) Calculation of count:

$$N = \frac{Input \ clock \ frequency}{Output \ required \ frequency}$$

$$N = \frac{1 \ MHz}{1 \ KHz} = 1000$$

Code SEGMENT ASSUME CS: Code MOV AL, 77H // Initialize Control Word = 0111 0111 OUT 86H, AL Back: MOV AL, 00H OUT 82H, AL // Lower byte of BCD count MOV AL, 10H // Upper byte of BCD count OUT 82H, AL HLT Code ENDS END

33 MS