Total No. of Questions : 4]	200	SEAT No.:	
P1273		[Total	No. of Pages : 2
	OCT/FE/INSEM/-6		

			F.E. (Semester - I)		
		P	BASIC ELECTRONICS ENGINEERIN	\mathbf{G}	
(2019 Pattern)					
Time	e: 1	Hour		Max. Marks : 30	
			the candidates:		
	1)	Ans	wer Q,1 or Q,2, Q.3 or Q.4		
	<i>2</i>)	Figi	are to right indicate full marks.		
			7, 39		
Q 1)	a)	Cor	mpare active and passive components explain passive	components.[5]	
	b)	Exp	plain the operation of full wave Rectifier with suitab	ole diagram and	
		, 7	ve forms.	[5]	
	c)	Exp	plain the construction and working principle of LED.	[5]	
		-			
			OR O		
Q 2)	a)	Exp	plain impact of electronics on Industry.	[5]	
	b)	Exp	plain the construction and working of = P - N junction	diode. Draw its	
		V -	I characteristics	[5]	
	c)	Dra	w circuit diagram of zener diode as voltage regulator	and Explain it.	
	,			[5]	
			28°.		
Q3)	a)	Dra	w and explain BJT as a switch.	[5]	
~	b)		plain construction and operation of N - channel EMC	70.	
				Ξ)	
	c)	For inverting amplifier using op = Amp, if $R_f = 100 \text{ K}\Omega$, $R_1 = 10 \text{K}\Omega$, $V_{CC} = \pm 10 \text{V}$, $V_i = 2 \text{V}$			
		i)	Calcalate output voltage.		
		ii)	Is the result in part (i) practically possible? Justify.	[5]	
			OR 6.V		

- Explain construction of BJT with respect to area and doping **Q4**) a) concentration. Mention the types of BJT. [5]
 - Explain construction and operation of p channel EMOSFET. [5]
 - Write ideal and practical values of five parameters of op-Amp. [5]

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