Aryan Shrivastava

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EDUCATION

International Institute for Information Technology Hyderabad

Hyderabad, India

B. Tech in ECE: Recent SGPA: 8.62, CGPA: 8.02

Pursuing, Expected Graduation: 2027

Sun Valley International School

Ghaziabad, UP, India

Class XII CBSE; 95%

2023

Sun Valley International School

Ghaziabad, UP, India

Class XII CBSE; 98.4%

2021

Projects

DataHack Link: Project-1 Link

• Our goal was to predict how likely individuals are to receive their xyz and seasonal flu vaccines using data analysis based upon some test sets given. The hackathon was organised by IIT Guwahati

4-Bit CLA Adder

Link: Project-2 Link

• Implemented a 4-Bit Carry Look Ahead Adder to meet the minimum propagation delay and tested the circuit using NGSPICE, made layout using MAGIC and implemented on FPGA using Verilog

Two Link Manipulator

Link: Project-3 Link

• Implemented a two link robotic arm using controls system and simulated the results using Simulink by MATLAB with the aim to reduce the error and increase the response time simultaneously.

Quadrature Down Oscillator

Link: Project-4 Link

 Analysed and designed a Quadrature Down Oscillator (QDC) using LTSPICE and implemented the same through hardwares.

Portfolio Website

Link: Website Link

• Designed and developed a personal portfolio website.

TECHNICAL SKILLS

Languages: Verilog, C/C++, MySQL, HTML5, CSS, JavaScript

SPICE: NGSPICE, LTSPICE

Technologies: MAGIC, FPGA, IoT, LaTeX

ACHIEVEMENTS

· Vigyan ko Janno Pratiyogita: 2019

• Sample

Sample