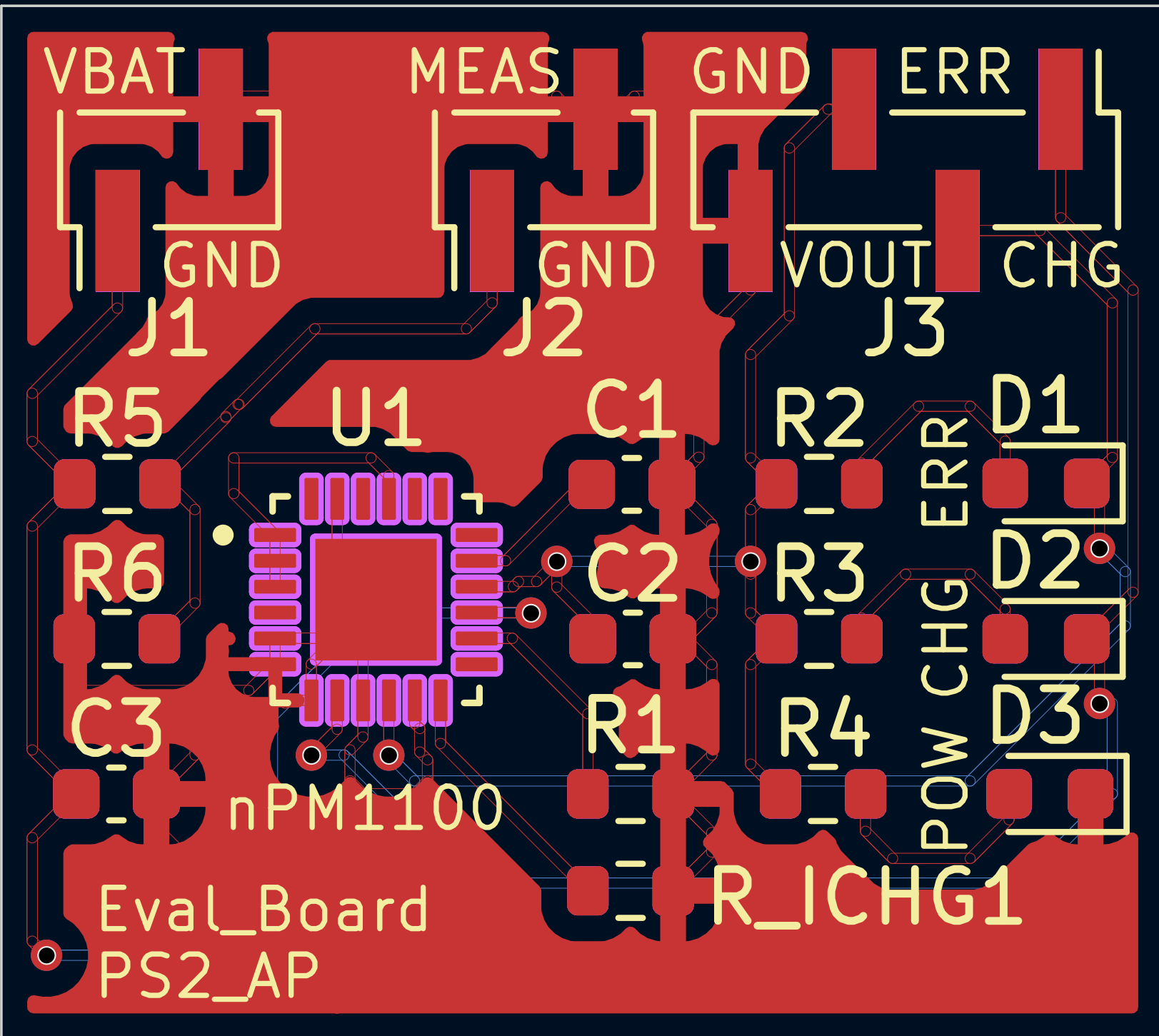
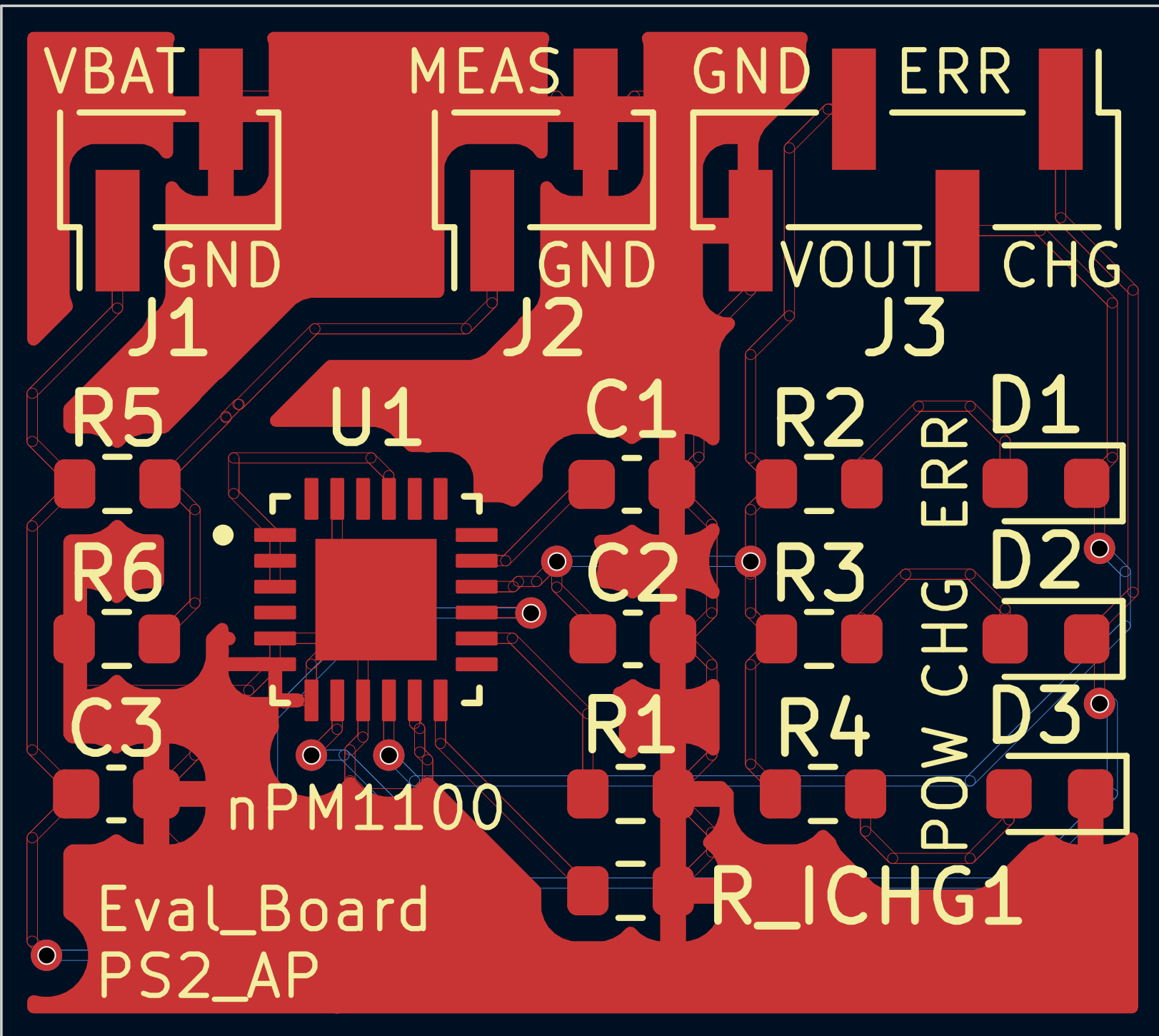


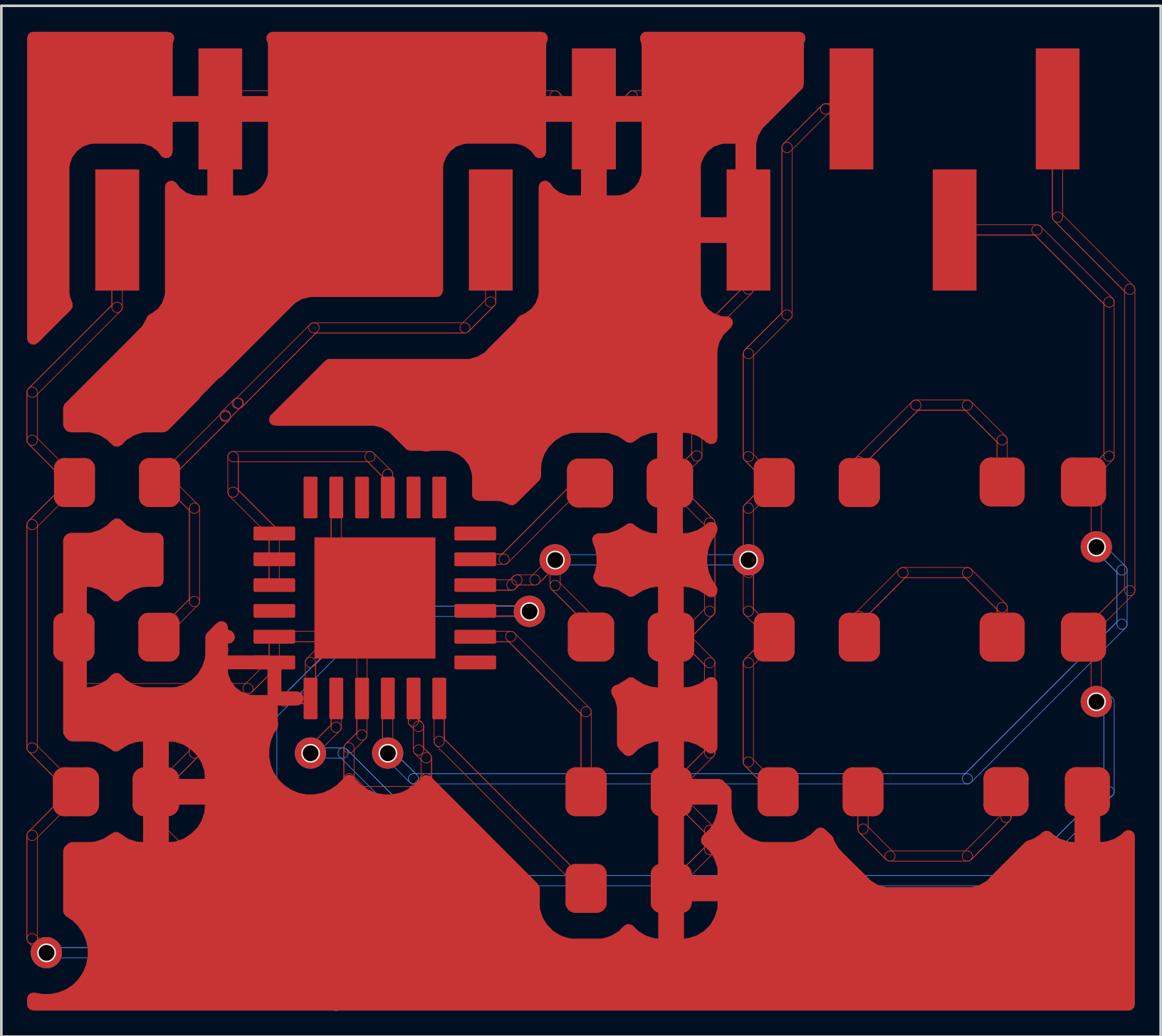
PCB DESIGN



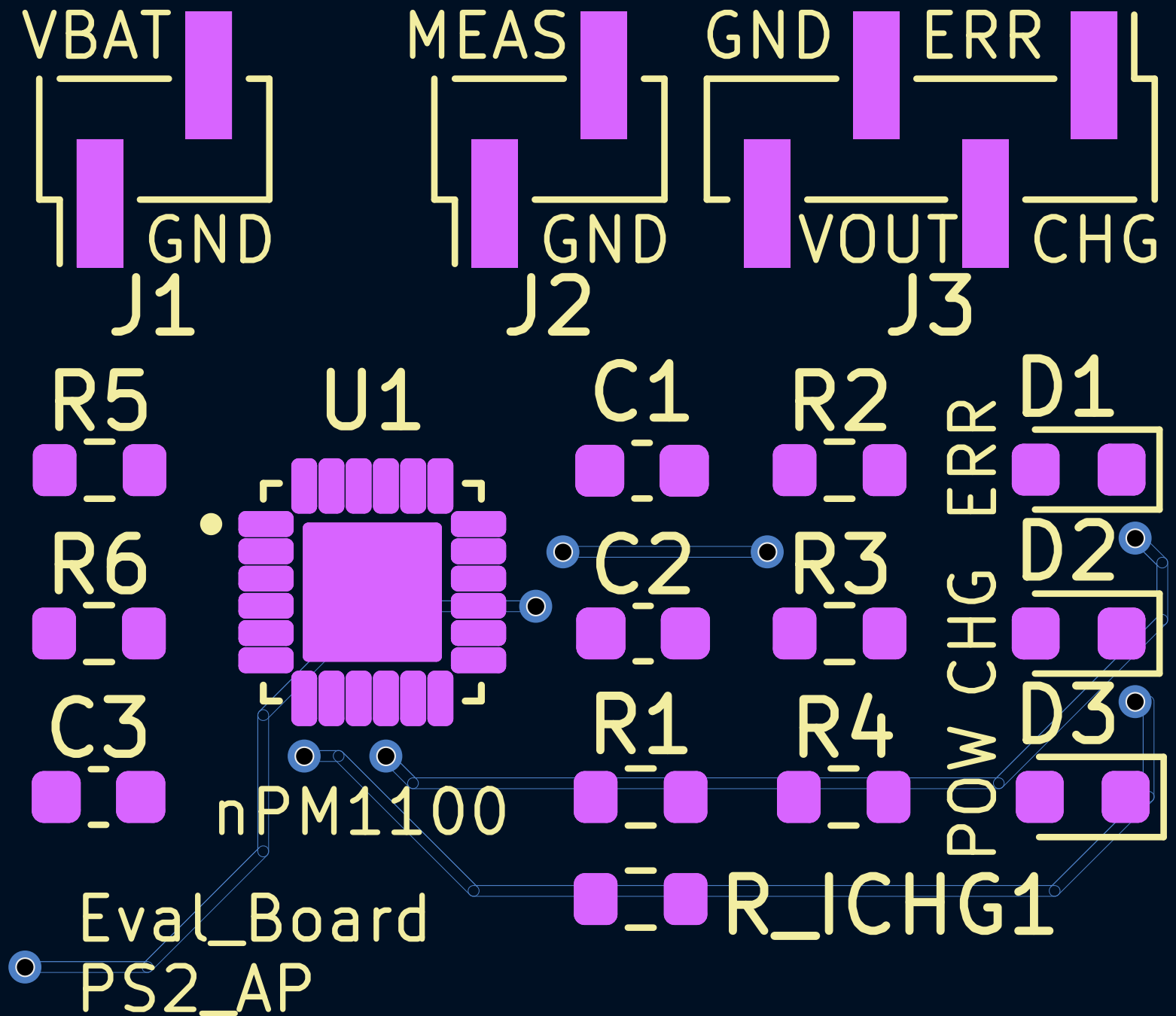
Without Masks/Paste



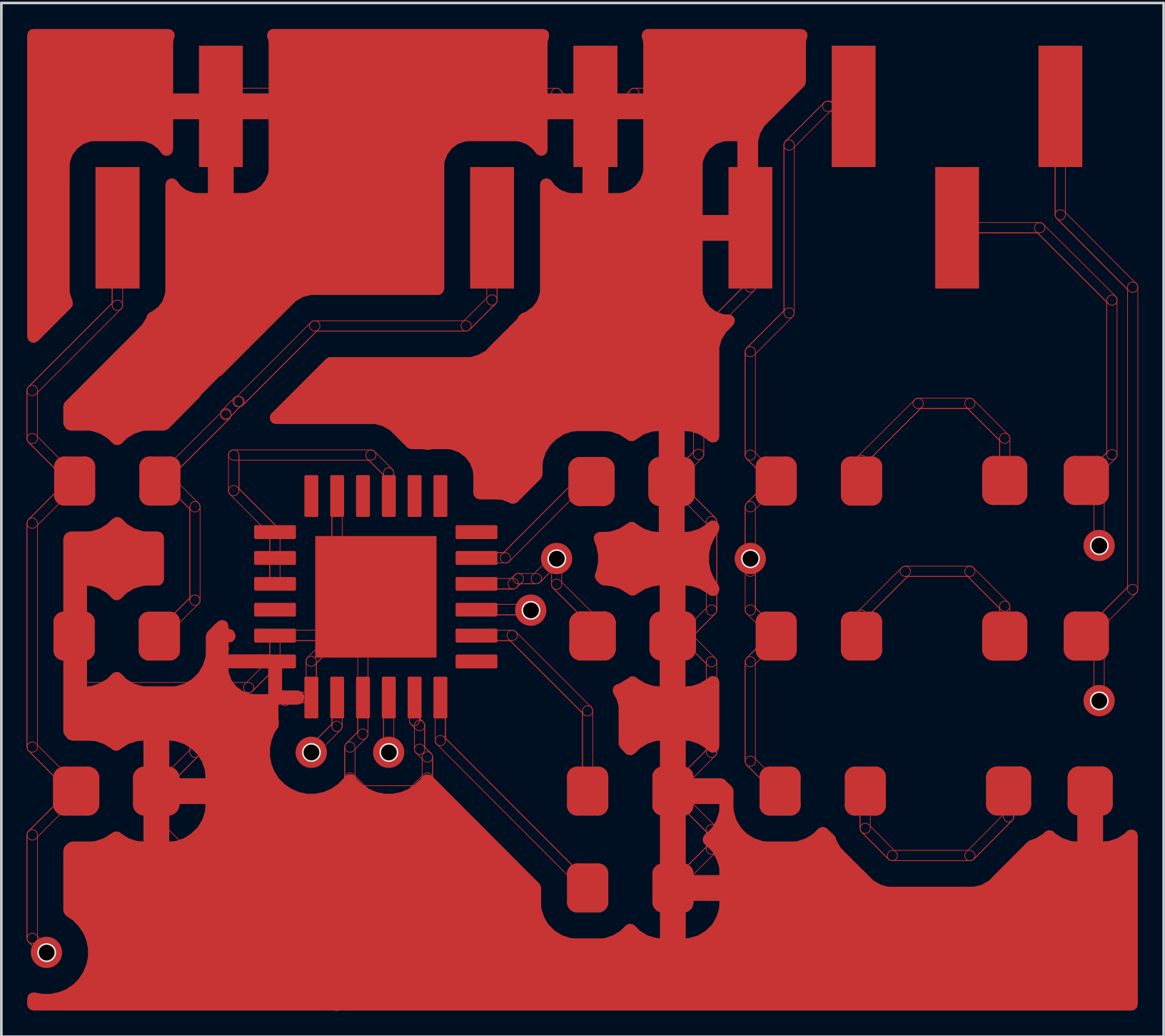
Front and Back Cu. Only



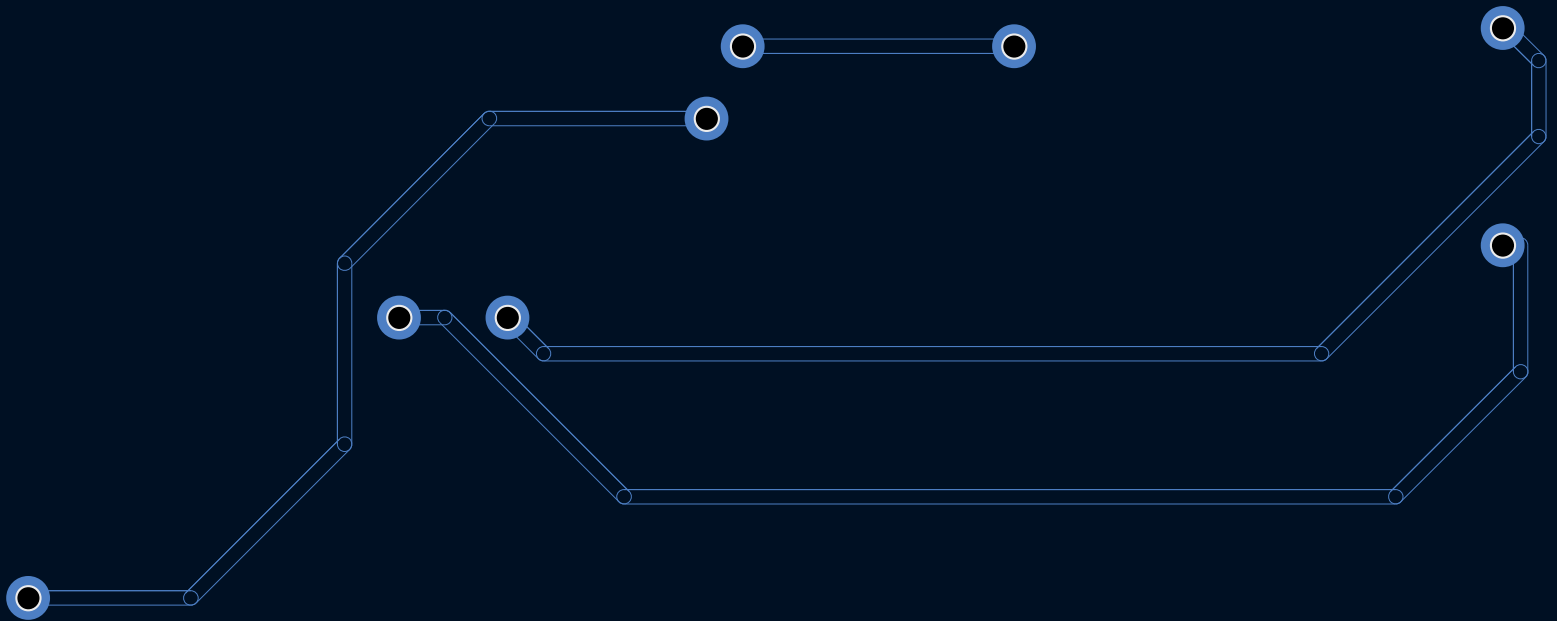
Back Cu with Silkscreen



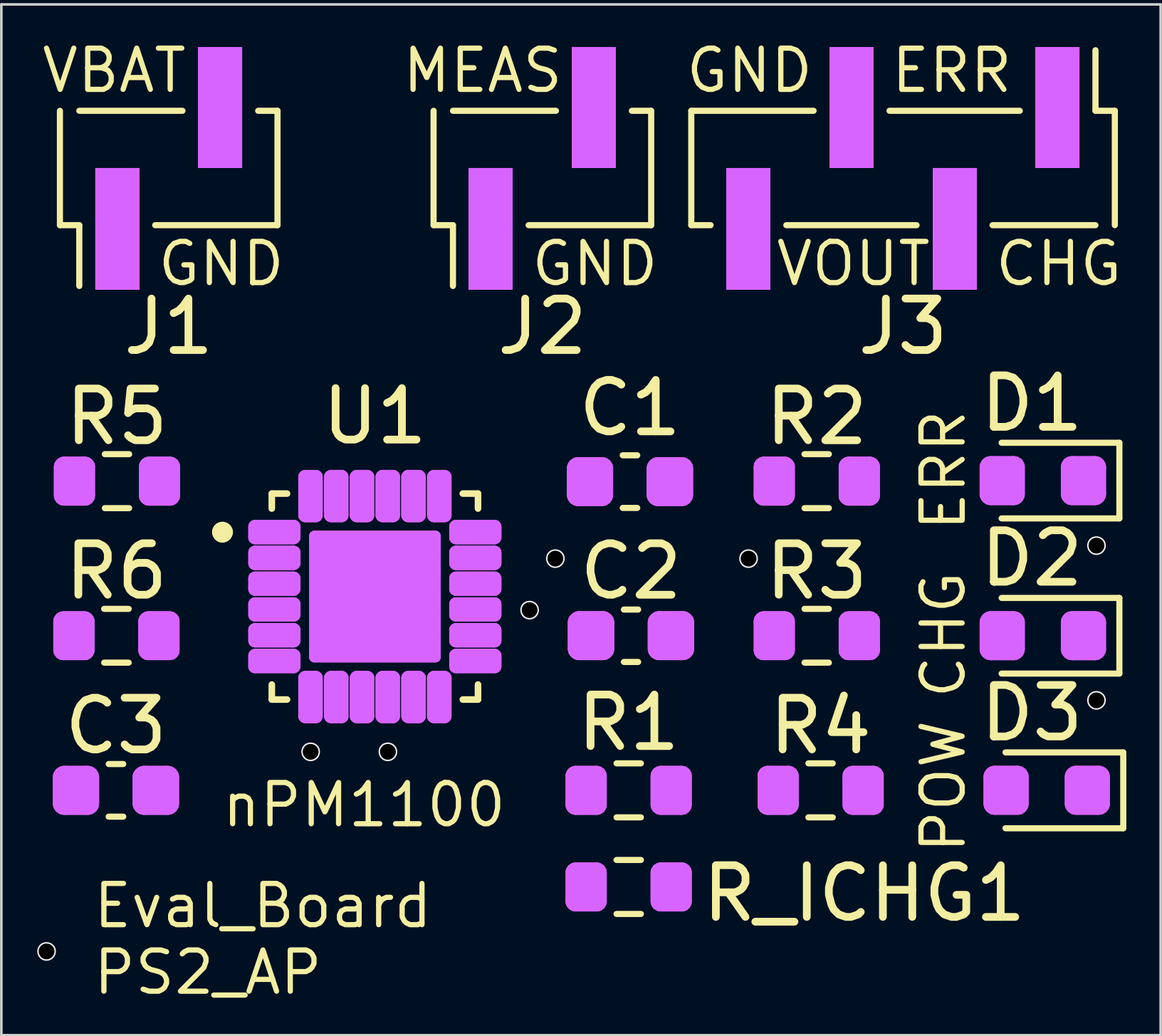
Front Cu Only



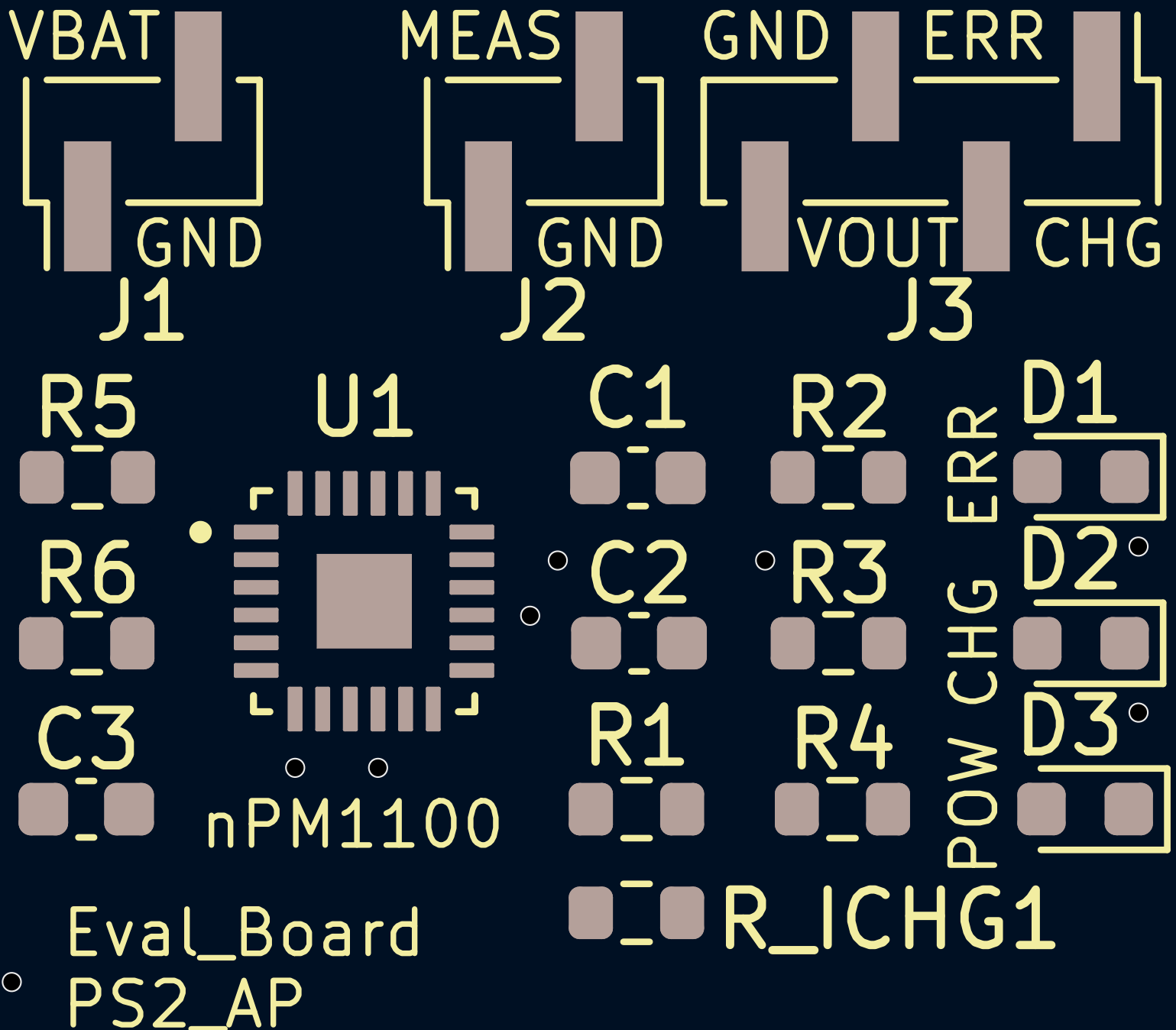
Back Cu Only



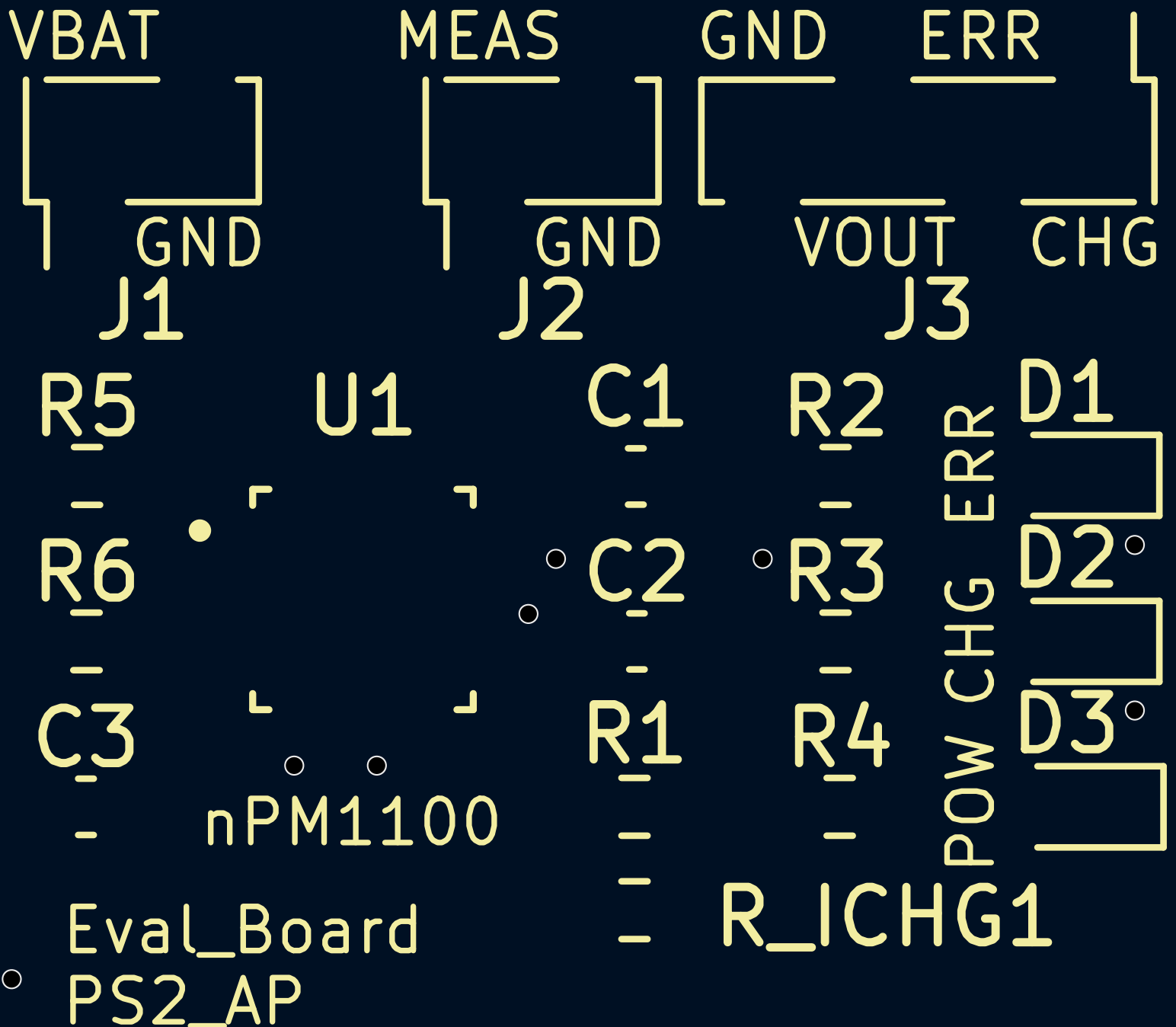
Silkscreens and Masks



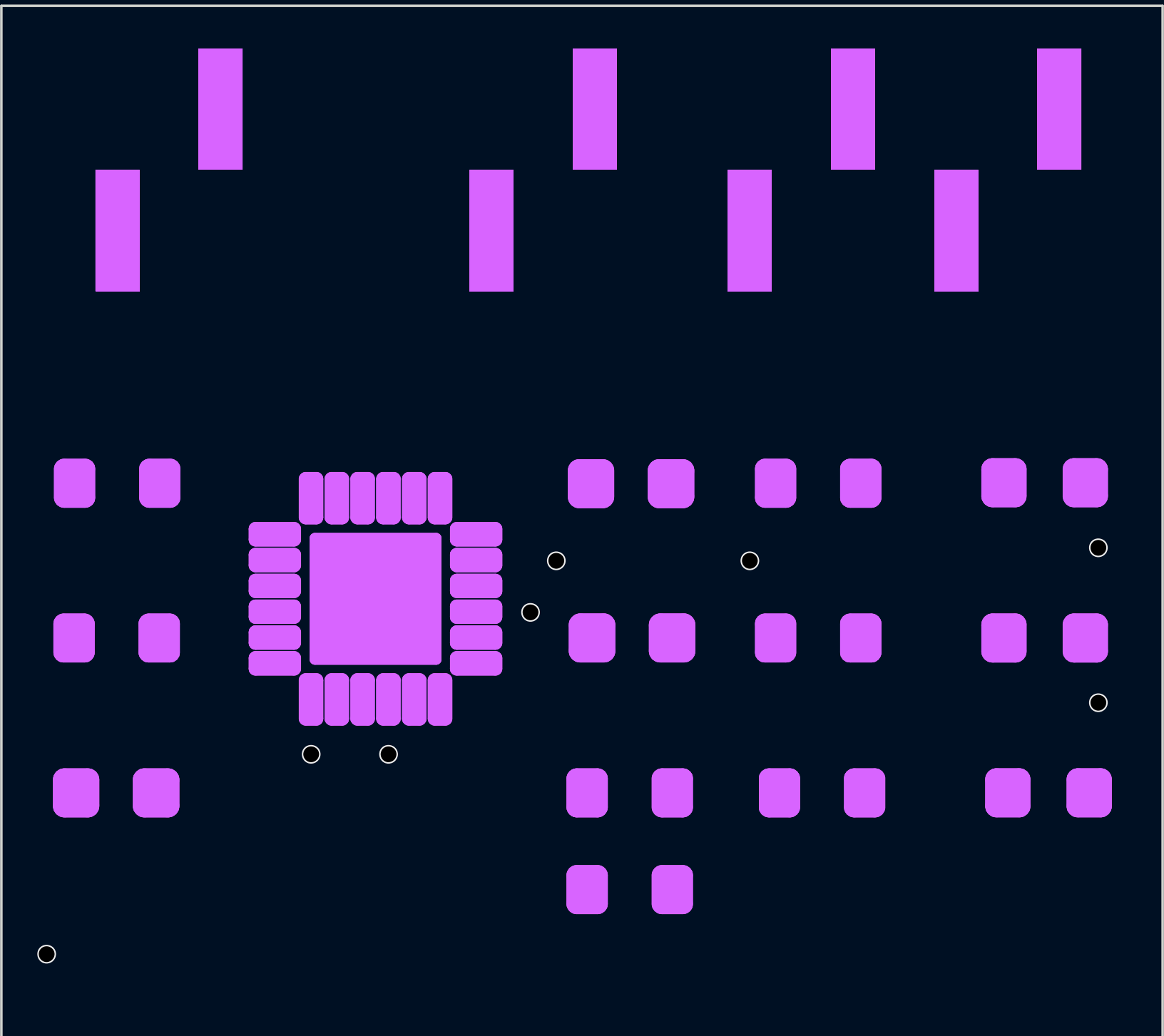
Silkscreens and Pastes



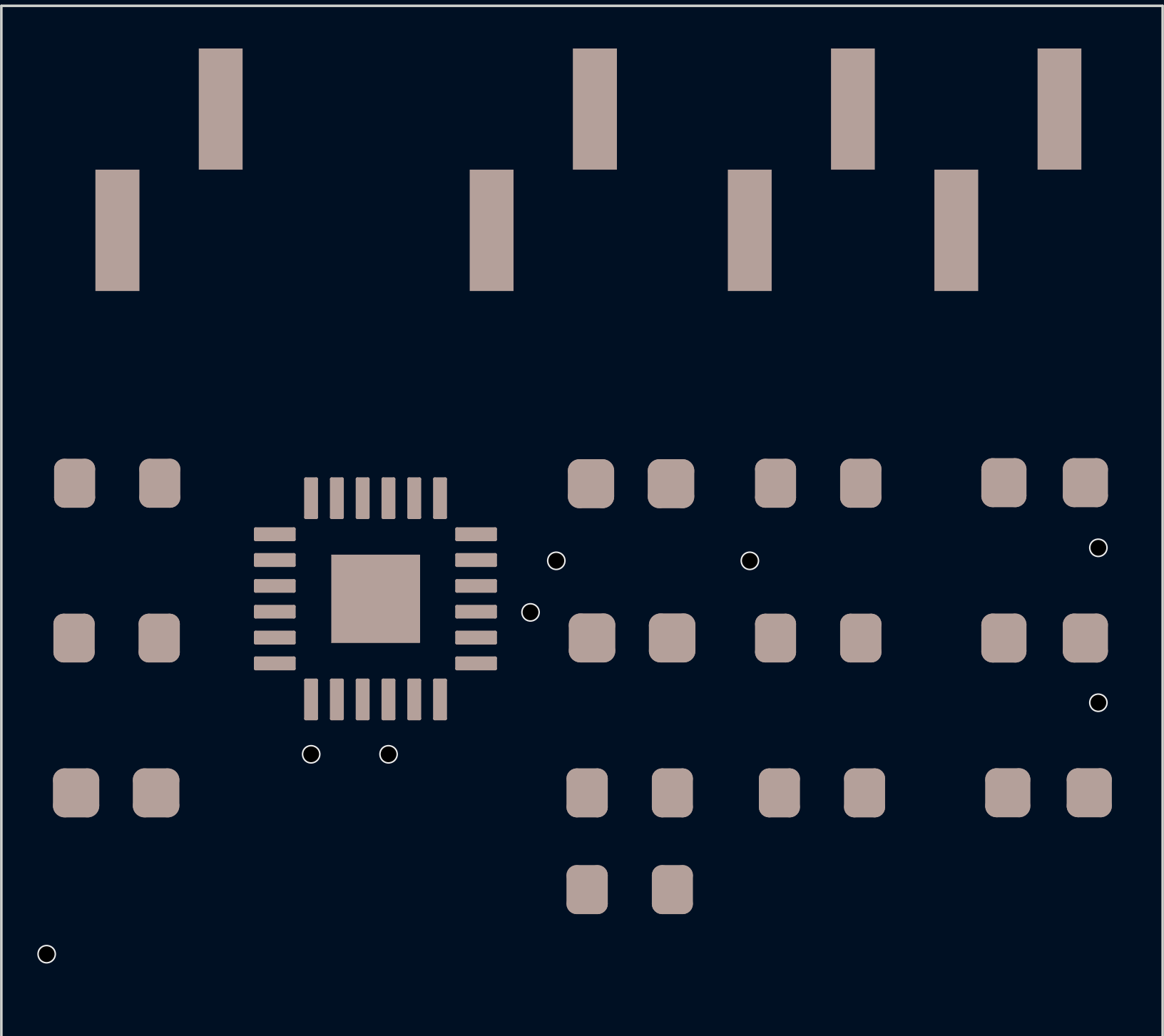
Silkscreens only

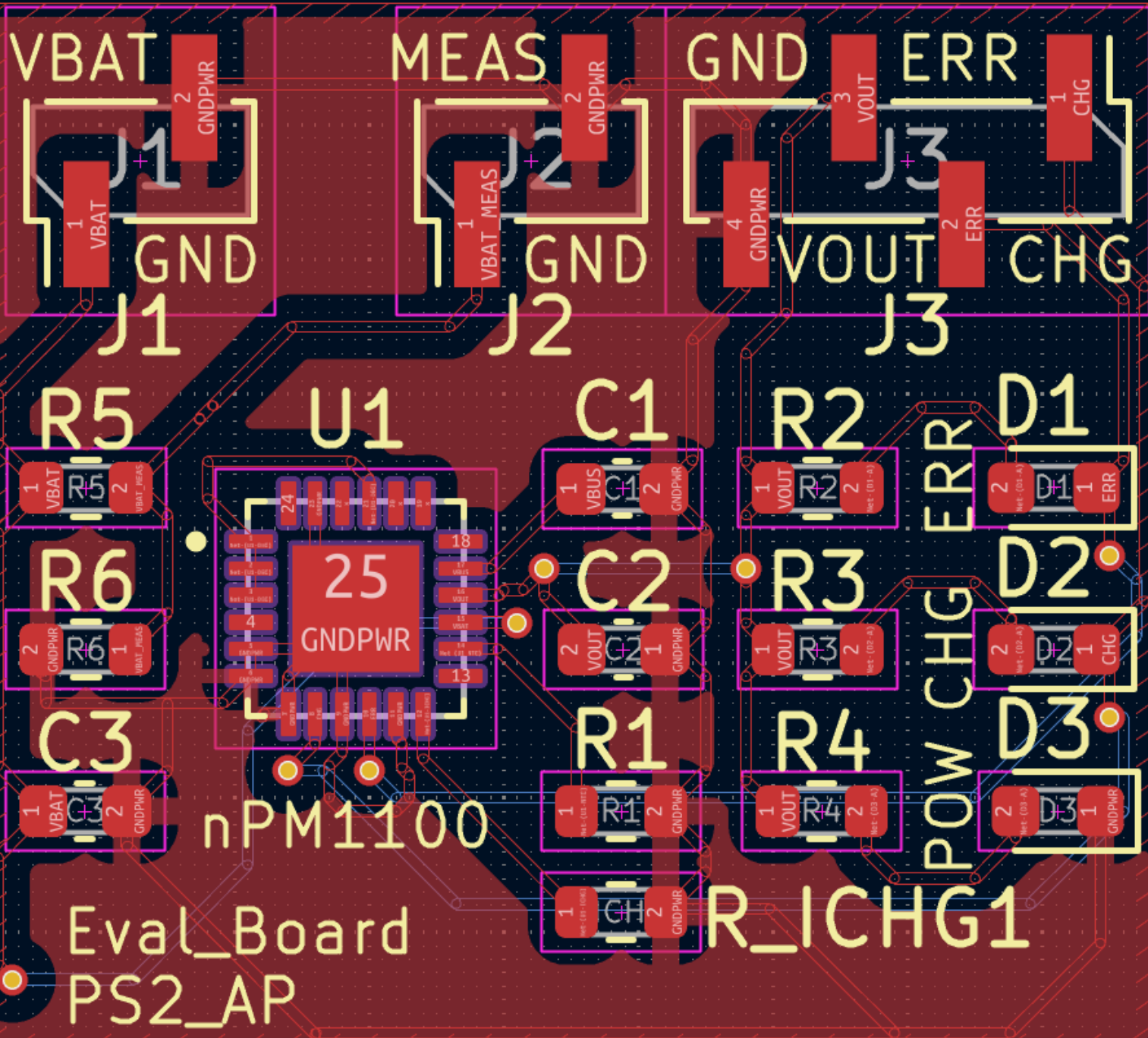


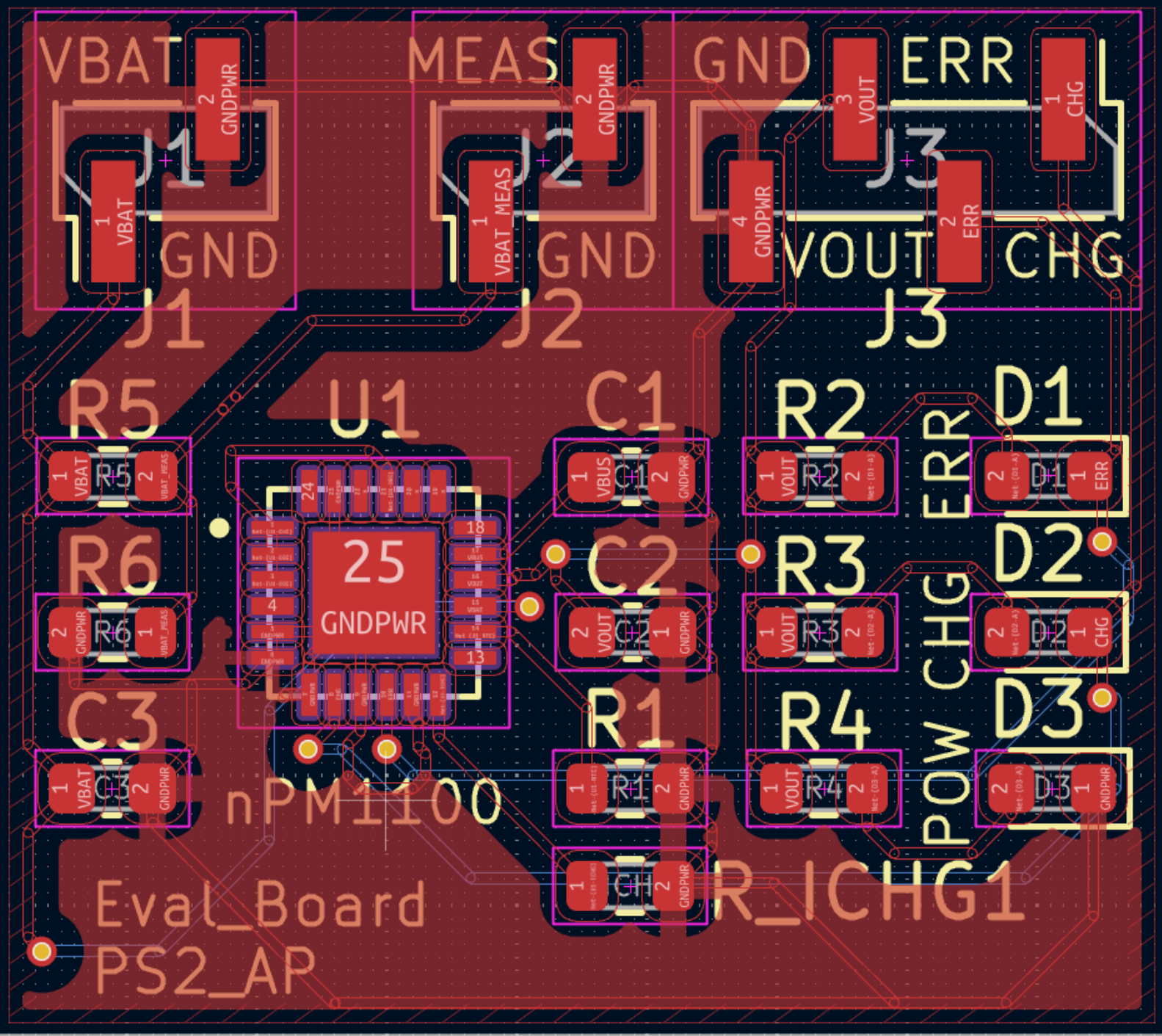
Masks only

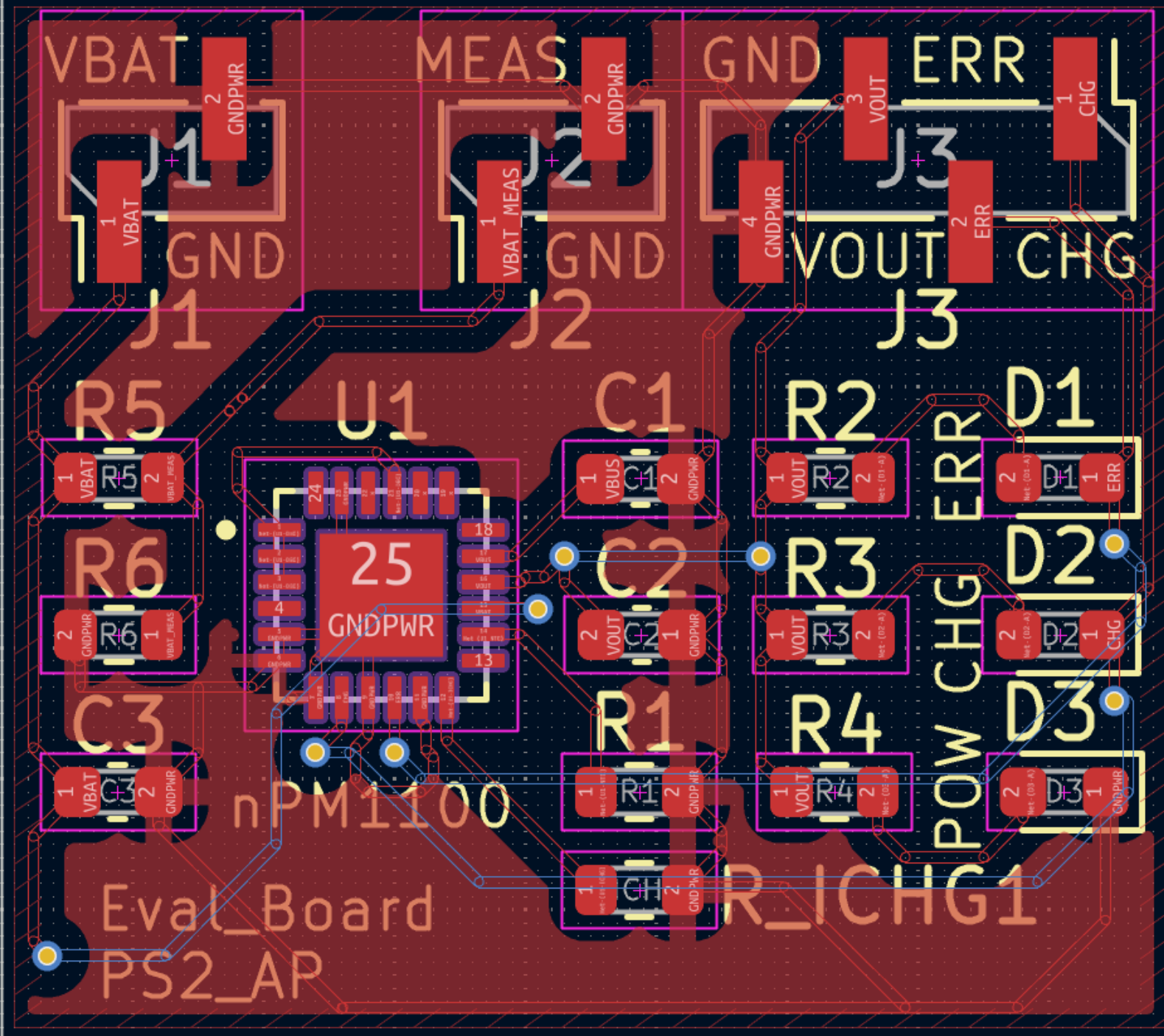


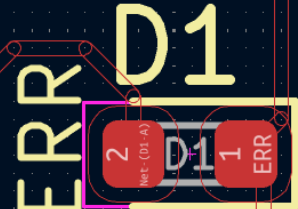
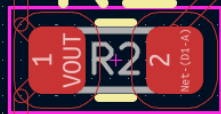
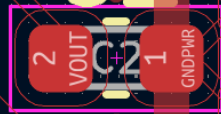
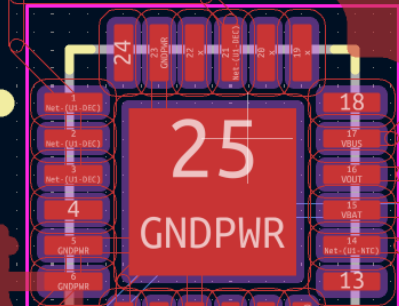
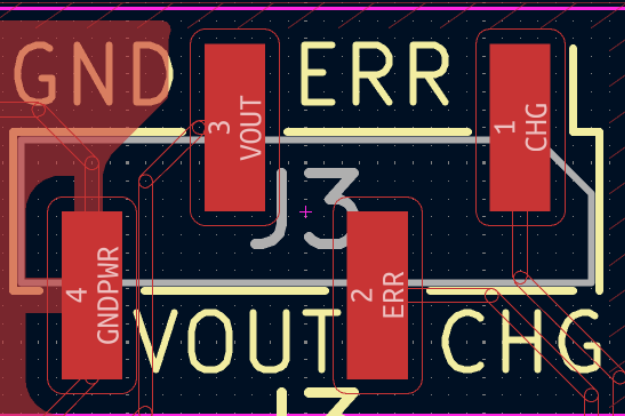
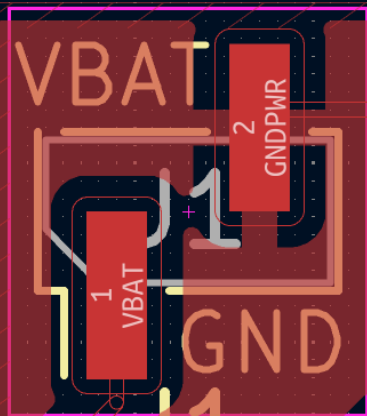
Pastes only

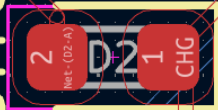
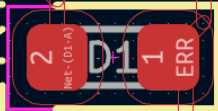
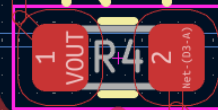
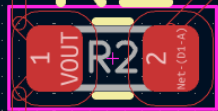
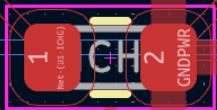
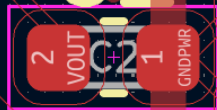
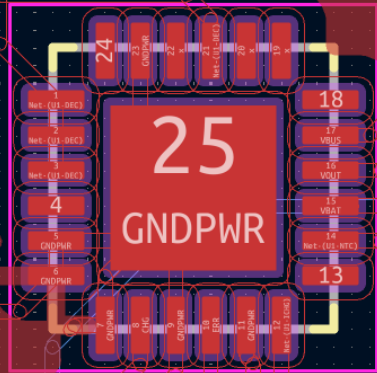












POW CHG ERR

ERR

CHG

VBA

VOUT

VOUT

nPM1100

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PS2_AP

R_ICHG1

