

-> A, B, C, D are the centers where people go to create audhar card. [Can consider A, B, C, D as threads too?

-> count is incremented like this if we do count ++

temp = count + 1 has on count = temp.

-> Let's assume center A and B both simultaneously requested to increment count as someone created their author card.

So if both request come at same time when count = 11 then center A request is handled in some distributions temps count + 2 hast rejet of willbox sont

Simultaneously request by center B is also handled temp = Count +1

- Because the thread schedules = 13qmotom can surp between

Then Sinelly count is updated it was to about . else do court to extemp of typetto lies about

- → But as we can see instead of +2 we only did +1 due to simultaneous request and due to this we lost some data.
 - -> Therefore process synchronization is needed to handle this problem.
 - -> Process synchronization techniques play a key role in maintaining the consistency of shared data.
 - -> The audhar center or the central database is the critical section. and the condition that was arising due to which there was inconsistent data is called sace condition.

Critical section (c.s)

-> The critical section refers to the segment of code where processes / threads access shared resources such as common variable. and files and perform write operations on them.

interrupted mid-execution.

Race condition { Major thread scheduling issue?

- A race condition occurs when two or more threads can time. It have good to change it at the same

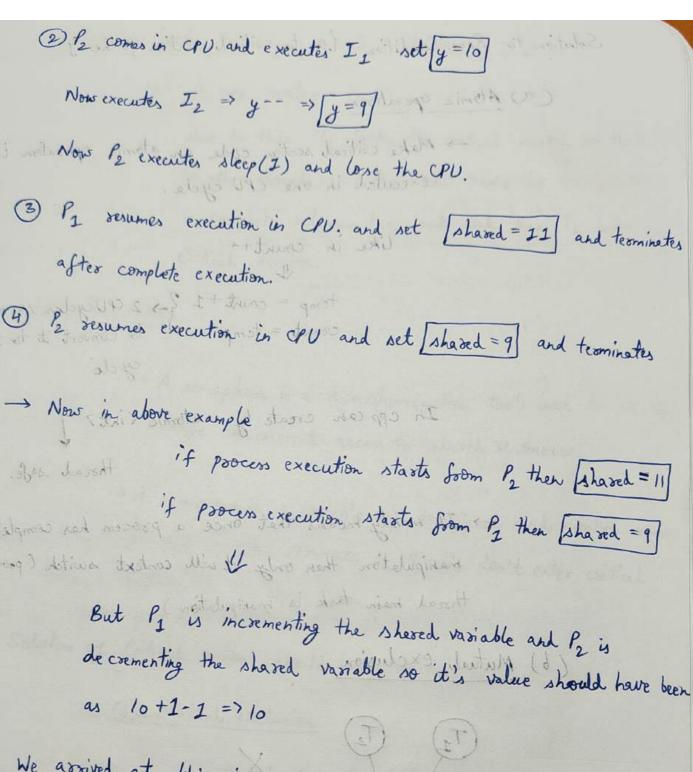
-> Because the thread scheduling algorithm can swap between threads at any time, you don't know the order in which the threads will attempt to access the shared data.

Therefore the result of the change in data is dependent on the thread scheduling algorithm i.e. both threads are railed airt alkred et bahan i meitas mes sange approprie

Int
$$x = shared$$
; I_1 int $y = shared$; I_2 $f = --$; I_3 I_4 I_5 I_6 I_6 I_6 I_7 I_8 I_8 I_8 I_9 I_9

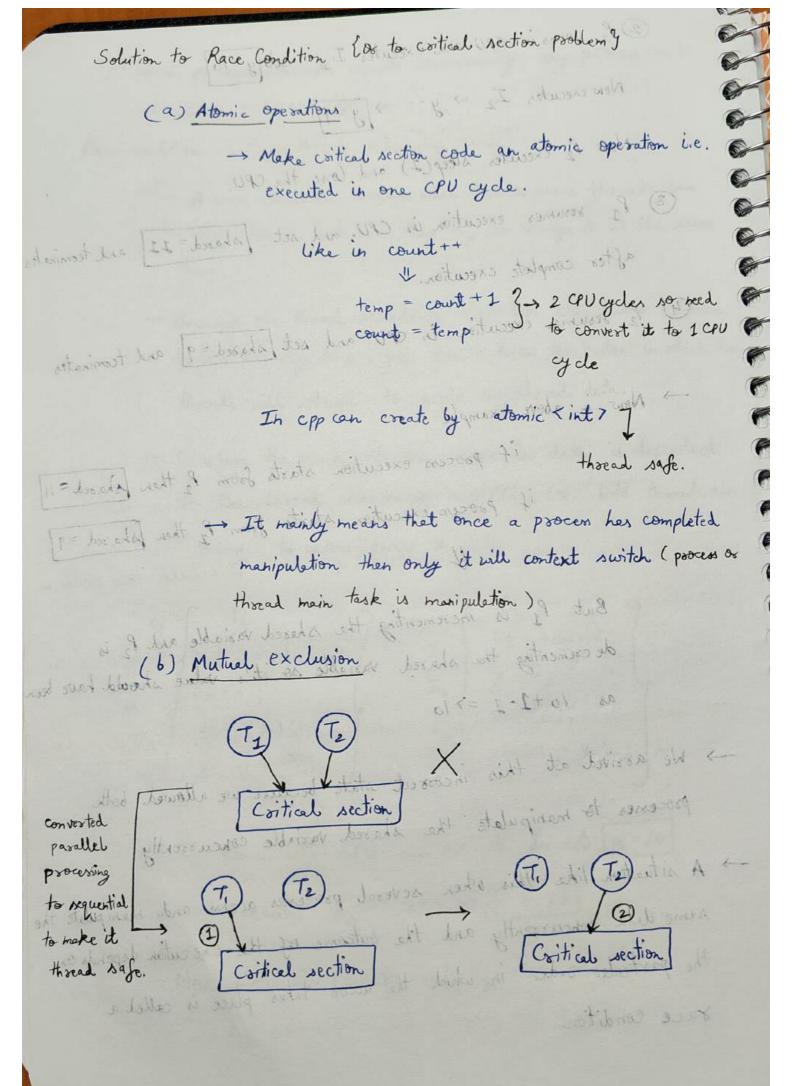
mornatest data is called race condition. 1) I comes into CPV and execute I, set | 2 = 10 Now executes $I_2 \Rightarrow \chi + + \Rightarrow \chi = 11$ orable where it has agreent of code where

Now Alexp(1) executes and Py more the QU. and files and perform write operations of on them.



-> We arrived at this incorrect state because we allowed both processes to manipulate the shared variable concurrently.

A situation like this when several processes access and manipulate the same data concurrently and the outcome of the execution depends on the particular order in which the access takes place is called a race condition.



-> Can use locks to boing mutual exclusion

eg: in cpp mutex. I want of which

due to this T1 locks the coitical section so that no other thread can enter then once the manipulation is complete releases the lock and then T2 locks the critical section.

Electricity indefinite blacking &

(C) Semaphoses

A semaphose is a synchronization tool used in 0s to manage concurrent access to shared resources.

A semaphose is a variable used to signal and control access so that multiple processes or threads don't enter critical sections at the same time.

Solution of Coitical section should have 3 conditions

(1 1 Mutual exclusion

→ Only one thread/process at a time can access critical section.

[Prevents race conditions?

Progress

while (tran (" 1);

Coltical acction

tion o

→ Each thread/process should have fair chance to go in critical rection.

Demainder section

- There shouldn't be any fixed order like first Py process will go then only P2 process will go. due to this Is looks the coition action no that

→ If no process is in the contical section and some processes want to enter it one of them must be allowed to proceed without unnecessary delay. (c) Some phases Ersevents indefinite blocking &

Bounded waiting (not that main can or cannot be fulfilled but @40)

should be fulfilled)

There must be a limit on how long a process / thread waits

to enter its critical section, after requesting it.

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Can we solve problem of sace condition by using single flag variable?

Solution of It that western whould have & conditions . T

while (from != 0); who while (1) while (1)

Conticel section

tuon = 1

bemainder section

while (turn (=1);

Critical section

turn = 0

mosges semainder section

- Each though process whould have for charce

go in contrat rection

-> Here we have taken our flag variable as turn. If turn = 0 then

Te condition of 0!=0 is false and it enters critical section While To condition of 0! = 1 is tone so it just executes while.

- After To work is done with critical section it leaves the critical section and makes twon = I due to which the condition of while for T2 is false as 1!=1 is false

Then To enters contical section and when it is done it makes the turn = 0. Till the time To just executes remaining section.

- -> Then I executes their remainder section. I do
- → Hence we have achieved mutual exclusion.

 → But there is a fixed priority as if twon = 0 than T1 will be executed first then Tz, if twon = I then Tz will be executed first then T1. (as if T2 doesn't want to go in coitical section when twon o) then T2 will be waiting until T2 goes
- -> Hence it doesn't fulfill progress condition therefore we cannot solve race condition by using single flag variable.
- So this single flag method was improved and is called Peterson's

Single flag improvement method Critical exercise

- [1] of

Peterson's solution as obtained god and and and and and 0 flag [2] - indicate if a thread is ready to enter the 6 alide stars to describe section, flag [i] = tome limplies that Pi 6 0 is ready. - After I work is done with critical nection it leaves the critical turn indicates whose turn is to enter the critical section. There Is extress critical section and when it is time it makes the tran- o, (I'll the time To just executes sensish(1) white Then Is executed their some index acoton = [0] golf then = I rejunded destrum destribute of and and while (turn = I && flag [1] == T);

while (turn = I && flag [1] == T); Critical section . Sent I went I went to all between flag[0]=F first than I was if I doesn't want to go in contral a then To will be writing with To Joen Hence it doesn't fulfill pagara condition therefore we cannot To race condition by naing single fleg variable. IT So this single fly method was improved and is called follows flag[1] = T turn = 0 while (turn==0, && flag[0]==T); Critical core section flag[1] = F

→ In the above code if we execute 71 first then

Slag [0] = T & turn = 1

then if it context switches to T_2 then executes it's first line then flag [1] = T then it again context switches to T_1 .

- → When it resumes Tz execution, the condition for while is true so it loops then when it context switches back to Tz and executes it twon = 0 and when it checks while condition both are true so it loops around.
 - Now when it is context switched to T1 back the condition for & while is false as twon!=1 so it enters critical section and when the work is completed it makes fleg [0] = false.
- → Due to this while condition of T2 is also false and it enters critical.

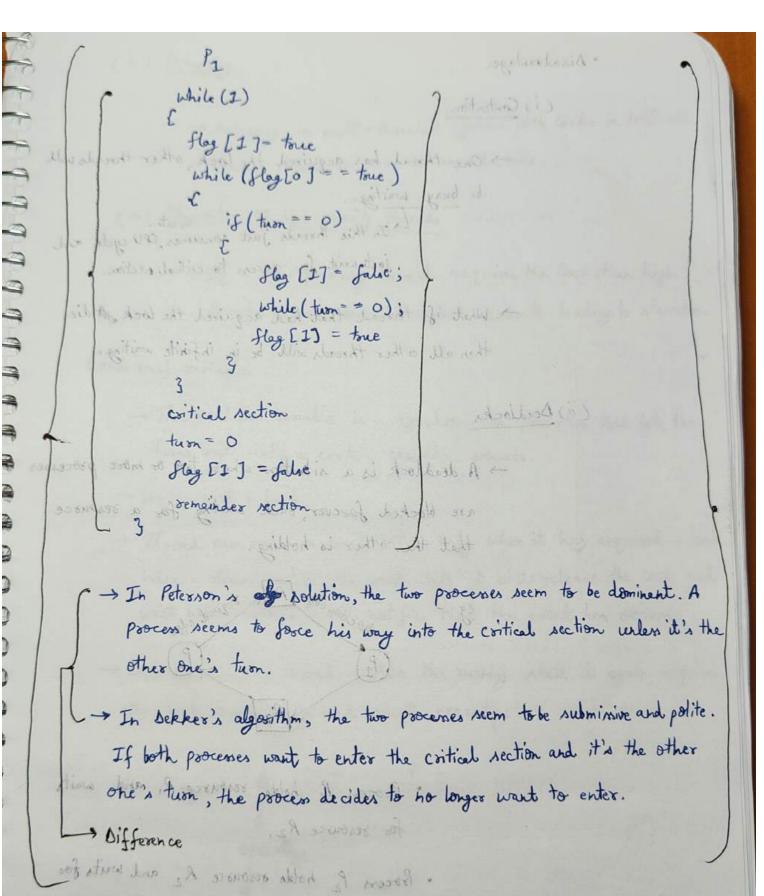
 section.
- → As there is no specific order and mutual exclusion is there so it can be used as a solution for race condition.
- In other case if T2 is not ready to enter critical section so whole T2 will be executed first as flag [1]! = true hence when T2 is fully executed it makes flag [0] = false :
- Initially the values are false in fleg array.

salab = LoJani.

In above example used 2 shared variables 7 (i) flag array (flag [0], flag [1]) and and trail at a Contrament of at relations trained to it well flag [2] T then it again contact withhe to TI. Peterson's solution can be used to avoid race condition but holds good for only 2 process/threads. pages than when it context middles buck to Is and executed to = 0 and when it decks while condition both (Similar to Peterson's Solution) when their context swither to Dekker's Algorithm initially flag [0] = False turn = sandom (0/1) & sand to saturance Stag [1] = False public (I) has saled sale is I pretilies while sist of and set flag to J= toue is be sales lisses while (flag [1] == tour) son sol without are transo if (tuon = = 1)

with gradient solves of whose ton it.

Slag [0] = false; while (turn == 1) } a tail datums of May fleg [0] = toue; aday 10] got padom it both axs critical section de dole in the section flag [0] = false remainder section ?



Mutex/Locks

→ Locks can be used to implement mutual exclusion and avoid race condition by allowing only one thread/ process to access critical section

· Disadvantager

(i) Contention

To this threads just consumers CPU cycle and

just wait for access to critical section.

coitical decition

-> What if thread that had acquired the lock of dies then all other threads will be in infinite waiting.

(ii) Deadlocks

→ A deadlock is a situation where two or more processes are blocked forever, each waiting for a resource that the other is holding.

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· Brocess P2 holds resource R2 and writes for resource R1.

-> Locks can be used to implement mutual exclusion and avoid me by allowing only one threaty process to accome contical section

(iii) Debugging_

Debugging in multi-threaded systems with locks is difficult because thread execution is unpredictable.

(iv) Starvation of high priority threads weitibes to time

If a low priority thread acquires the lock then high priority thread comes it cannot execute leading to starvation.