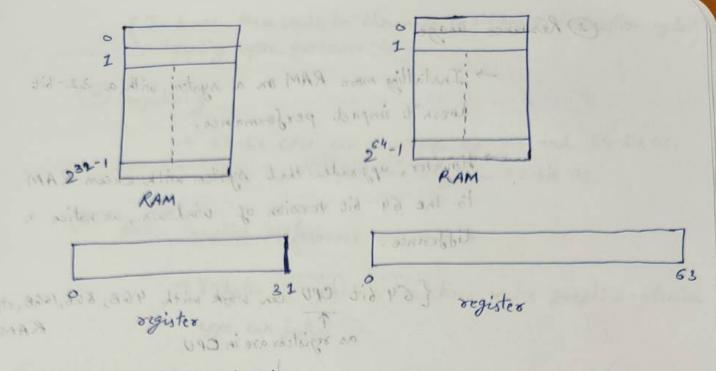
64 bit OS hours is mildered a fit -> A 32-bit Os has 32-bit registers and it can access 232 unique memory address i e 4GB of physical hed go) solvate attated a sol atool IT 30/20I8 str -A 64-bit OS has 64-bit registers and it can access 17, 179, 869, 184 GB.

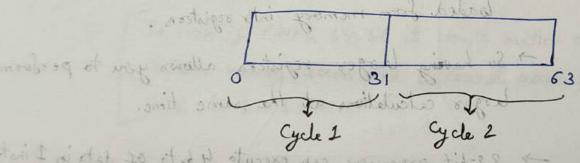
(ii) MER bording



32-6it CPU architecture can process 32 6its of data & information

-> 64-6it CPU aschitecture can process 64 bits of data & information.

eg: - for 32 bit CPU to process a 64 bit data or information will be needed to do in two cycles



Adventages of 64-bit over the 32-bit os

(2) Addressable memory

32-6it CPU+ 2^{32} memory addresses 64-6it CPU $\rightarrow 2^{64}$ memory addresses

2 byto - 8 bits

2) Resource usage

-> Installing more RAM on a system with a 32-bit doesn't impact performance.

However, upgrade that system with excess RAM to the 64-bit version of windows, we notice a difference.

E64 bit CPU can work with 4GB, 8GB, 16GB, etc. 7 RAM) RAM)

32 bit os 64 bit Os.

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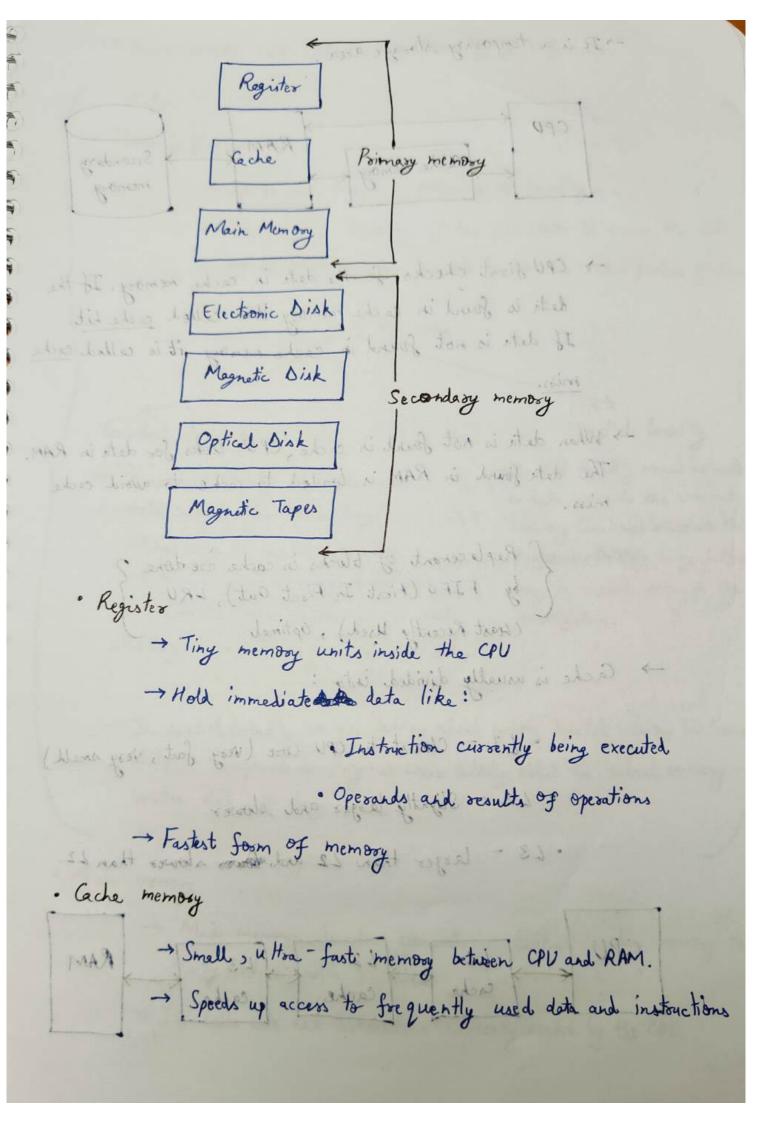
(3) Performance

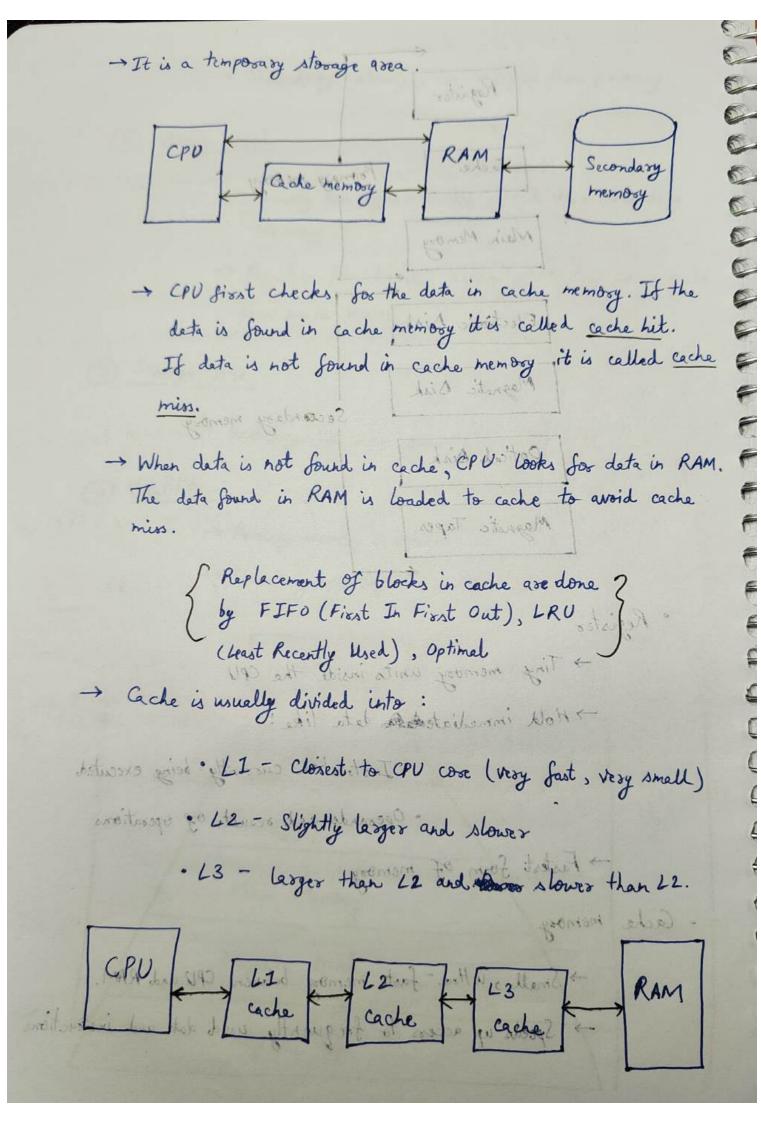
you're performing math in your code, operands are loaded from memory into registers.

- I so having larger registers allows you to perform larger calculations at the same time.
- → 32-bit processor can execute 4 byte of data in 1 instruction cycle while 64-bit processor can execute 8 byter of data in 1 instruction cycle.

(In 1 sec these could be thousands to billions of instruction cycles) depending upon processor design (4) Compatibility (Access speed -> 64-bit CPV can own both 32-bit and 64-bit 05. While 32-bit CPU can only our 32-bit os. (5) Better Graphics performance -> 8-bytes graphics calculations make graphics-intensive apps our faster, wie agreet? (3) * Note: - 32 bit 05 = 5 232 unique address => 4,294, 967,296 bytes) This means OS can only "see" or "point to" up to 46 B of So even if we add extra RAM it would be invisible. So even if CPU is 64 bit it won't matter as 05 controls memory access and it cannot access more than 4GB in 32-bit 05. Types of Storage - Comparison on the basis of: Main Member 1 Cost -> Pointsy storages are costly. -> Registers are most expensive due to expensive semiconductors & labour. AUGUSE

(In I nec these well be thousands to billion of instruction galai + Secondary storages are cheaper than primary (4) Compatibility 2 Access speed Primary has higher access speed than secondary 20 sid-23 aux byo no memory -> Registers has highest access speed then comes cache 0 intil singers atom wilder has singery styles 3 Storage size appa our factor Secondary has more space Wolatility - Primery memory is volatile addition of Secondary memory is non-voletile So easy if CAD is CAD it wanter as as controls increase a constant it constant series reports then 468 in 52/660s. Register Cache Main Membry Magnetic Disk access speed 1





-> Cache memory also satisfies the locality of reference. Extra Locality of reference RAM (Kathan Access Monday) It is also known as principle of locality ... It is the tendency of the processor to access the set of memory location repeatedly over a short period of time. to delibert of tomas - locality of reference de print and Way Back song Temposal locality Spatial locality -> Temposal locality means current -> Spatial locality means instructions Modelata or instruction that is being or data near to the current memory locations who team that is tetched may be needed soon. So we should store that data or instruction temporato being being betched may be needed soon in the in the cache memory so that we can avoid hear future. searching in main memory for the same data. - In spatial locality we are talking about nearly located memory locations while in temporal locality we were talking about the actual memory location that we being fetched. -) Used to above dati premovedly.

. Main memory

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Main memory is the computer's central working memory that exe directly interacts with the CPU. I good

It holds data and instructions currently needed by the CPU.

, sometime to Main memory to the grown at a ROM (Read Only Memory) RAM (Random Access Memory) → Data is lost when power is → Data is retained even when power is off. (voletile) 0 0 -> Used for holding currently Stores fromware (eg:-BIOS, 0 durning programs. Storing filesed -> Cannot be modified easily. data being used by those Rom Types of ROM Programs. turns were giled languist Silved letter SRAM good is test notonoton DEEPROM (Static RAM) (Dynamic RAM) fetched may be needed boom. So -> Needs constant -> Faster and refreshing used as a contlier, used in what do to be word allowed in mein memory. sees while in main member for the name date. · Secondary memory where there with the sen our of the distinger AT → Storage media, on which computer can store deta & Programs. habital grid and test mit and → Used to store data permanently.