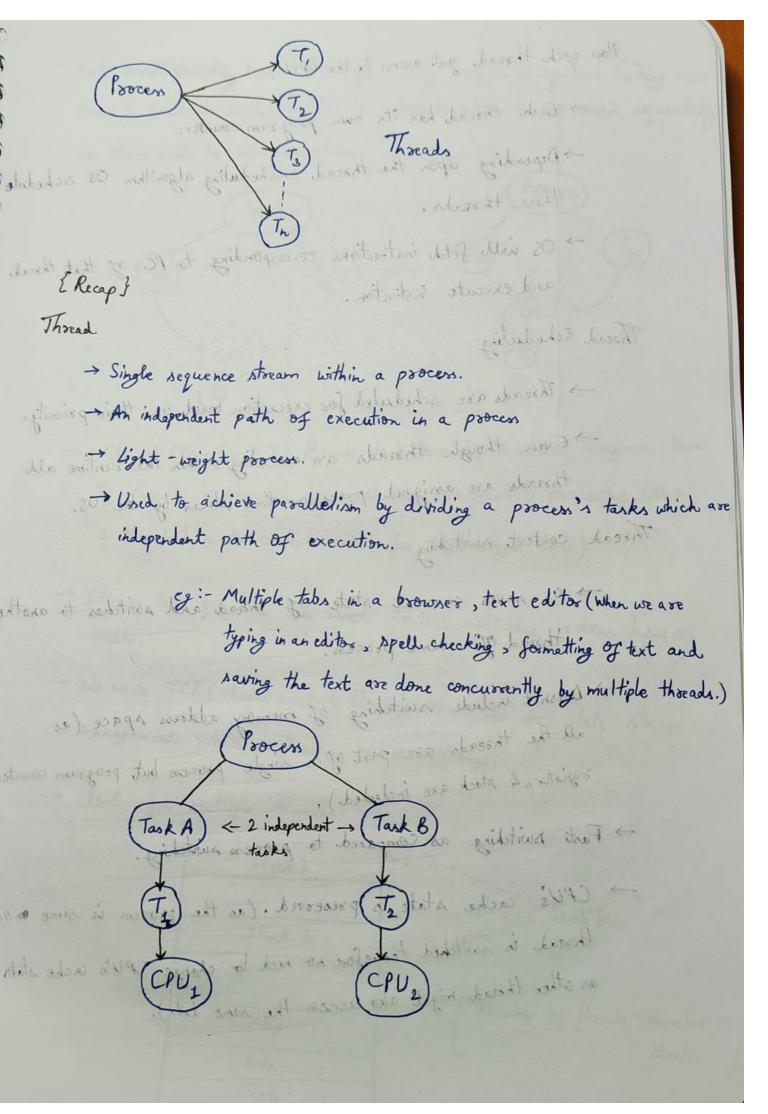
Concurrency -> Concurrency is the execution of the multiple instruction sequences at the same time. - It happens in the OS when there are several process threads running in parallel.



How each thread get access to the CPU?

- -> Each thread has its own program counter.
- Depending upon the thread scheduling algorithm Os schedule these threads.
- → OS will fetch instructions corresponding to PC of that thread and execute instruction.

- Thread Scheduling ... Threads are scheduled for execution based on their priority.
- Even though threads are executing within the runtime all threads are assigned processor time islices by the OS.

 Thread context switching witness to the the track square

C

C

Os saves current state of thread and switches to another -be thread of same process.

Doesn't include switching of memory address space (as all the threads are part of a single process but program counter, registers & stack are included).

Tast switching as compared to process switching.

- -> CPU's cache state is preserved. (as the process is same only thread is switched therefore no need to change CPU's cache state as other thread might also access the same data).

- But moderation multi-threading is only useful in systems having more than I CPU as with I CPU the threads, will be processed requentially so there will be no parallelism. It is bound Proces (09) esturo are go Procen isead-apoific data -> With 1 CPU first To thread is executed in above diagram then Iz thread is executed hence showing sequential processing. (Also 2 threads have to context switch for that single CPU). → I/O or time quentum based context switching is done here as A single process entry in the process table may point to multiple -> We have TCB (thread control block) like PCB for state storage management while performing context switching. PCB for multiple threads: Thosas table is maintained by This dring who Stack for Stack 2 => 7, €

Heap

Data

{ Number of threads = Number of? stacks

