SRM Institute of Science and Technology
Faculty of Engineering and Technology
Department of Computer Science and Engineering
18CSC203J – Computer Organization and Architecture
Model Practical Examination

1

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

Implement Carry look ahead adder.

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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2

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Design a 16:1 MUX using 4:1 MUX.**
2. **Write a program in 8086 to add two 8 bit numbers.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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3

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement Ripple Carry Adder for 4 bits input.**

2. **Write a program in 8086 to multiply two 8 bit numbers.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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4

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement Carry Save Multiplier for 3 bits input.**

2. **Write a program in 8086 to subtract two 8 bit numbers.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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5

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement 4 bit Parallel Adder/Subtractor.**

2. **Write a program in 8086 to find 2's complement of a number.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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6

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement 2 bit Binary Multiplier.**

2. **Write a program in 8086 to find Factorial of a number.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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7

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement ALU which performs AND, OR, XOR and ADD operations.**

2. **Write a program in 8086 to find 1's complement of a number.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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8

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Assemble and Disassemble a virtual Desktop.**

2. **Write a program in 8086 to add two 8 bit numbers using Indirect addressing mode.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1

Examiner2

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9

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Design a 16:1 MUX using 8:1 MUX.**

2. **Write a program in 8086 to subtract two 8 bit numbers using immediate addressing mode.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

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10

Degree : B.Tech **Year/Sem: II/III**
Date : **Duration : 2 periods**

Register Number:_____

1. **Implement ALU which performs NAND, NOR, XOR and SUB operations.**

2. **Write a program in 8086 to find 1's complement of a number.**

Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2