SRM Institute of Science and Technology Faculty of Engineering and Technology Department of Computer Science and Engineering 18CSC203J – Computer Organization and Architecture

Model Practical Examination

Degree : B.Tech Date :			Year/Sem: 1 Duration: 2		
Register Number:				•	
Implement Carry look ahead adder.					
Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)	
Examiner1		Exar	miner2		
Degree : B.Tech Date : Register Number:	SRM Institute of Faculty of Engir Department of Compu 8CSC203J – Computer Model Prac	neering and Tecluter Science and Organization a ctical Examinati	hnology Engineering nd Architectur		
	UX using 4:1 MUX.				
C	n in 8086 to add two 8 b	oit numbers.			
Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)	

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Examiner1	<u>Examiner2</u>	

SRM Institute of Science and Technology Faculty of Engineering and Technology Department of Computer Science and Engineering 18CSC203J – Computer Organization and Architecture

Model Practical Examination

Degree Date	: B. I ech	Year/Sem: 11/111 Duration: 2 periods			
Register	r Number:				
1. I 1	mplement Ripp	le Carry Adder for 4 bi	ts input.		
2. V	Vrite a program	in 8086 to multiply tw	o 8 bit numbers	•	
Aim/Alg	gorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)
Examine	ar1		Evar	niner2	
Examin	OI 1		Exai	IIIICIZ	
		SRM Institute of S	Science and Tec	hnology	
		Faculty of Engin Department of Compu			
	18	3CSC203J - Computer		nd Architectur	e 4
Degree Date	: B.Tech			Year/Sem: l ration: 2 perio	
	r Number:		Σ	auton (2 per lo	
_		y Save Multiplier for 3	hits innut		
	•		•		
		in 8086 to subtract tw			I m
Aim/Alg	gorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)
		1	1	i	ı

Examiner1 Examiner2

SRM Institute of Science and Technology Faculty of Engineering and Technology Department of Computer Science and Engineering 18CSC203J – Computer Organization and Architecture Model Practical Examination

Year/Sem: II/III

Degree : B.Tech Date :		Year/Sem: II/III Duration: 2 periods		
Register Number:_				
1. Implement 4	bit Parallel Adder/Subtrac	etor.		
2. Write a prog	ram in 8086 to find 2's con	iplement of a ni	umber.	
Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)
Examiner1		Ever	miner2	
Examiner		⊏xai	IIIIlei Z	
Degree : B.Tech Date :	SRM Institute of S Faculty of Engin Department of Compu 18CSC203J – Computer Model Prac	eering and Tech iter Science and Organization an tical Examination	nnology Engineering nd Architectur	
Register Number:_			•	
	bit Binary Multiplier.			
	ram in 8086 to find Factori	al of a number.		
Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)
		1	- 1	i

Examiner1 Examiner2

SRM Institute of Science and Technology Faculty of Engineering and Technology Department of Computer Science and Engineering 18CSC203J – Computer Organization and Architecture Model Practical Examination

Degree : Date :	B.Tech	Year/Sem: II/III Duration: 2 periods			
Register N	lumber:				
1. Im j	plement ALU v	which performs AND,	OR, XOR and A	ADD operation	s.
2. W r	ite a program	in 8086 to find 1's con	nplement of a nu	ımber.	
Aim/Algor	rithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)
Examiner	1		Exan	niner2	
	180	SRM Institute of S Faculty of Engin Department of Compu CSC203J – Computer Model Prac	eering and Tech iter Science and	nnology Engineering nd Architecture on	8
Degree : Date :	B.Tech		Dui	Year/Sem: l cation : 2 period	
Register N	lumber:			-	
_		assemble a virtual Des			
			-	T 11 4 11	
		in 8086 to add two 8 b	it numbers usin	g Indirect addi	ressing mode.
Aim/Algor	rithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)

Examiner1 Examiner2

SRM Institute of Science and Technology Faculty of Engineering and Technology Department of Computer Science and Engineering 18CSC203J – Computer Organization and Architecture

Degree : B.Tech

Model Practical Examination Year/Sem: II/III

Date :	Duration: 2 periods				
Register Number:_	Register Number:				
1. Design a 16: 1	MUX using 8:1 MUX.				
2. Write a prog	ram in 8086 to subtract tw	o 8 bit numbers	using immedia	ate addressing mode.	
Aim/Algorithm(4)	Procedure(10)	Output(6)	Viva(5)	Total (25)	
Examiner1		Exar	niner2		
	SRM Institute of	Science and Tec	hnology		
	SRM Institute of Faculty of Engir	neering and Tecl	nology		
	Faculty of Engir Department of Comp 18CSC203J – Computer	neering and Tecluter Science and Organization a	nnology Engineering nd Architectur	e 10	
Degree : B.Tech	Faculty of Engir Department of Comp 18CSC203J – Computer	neering and Tecluter Science and Organization and Ctical Examination	nnology Engineering nd Architectur on Year/Sem:		
Degree : B.Tech Date :	Faculty of Engir Department of Comp 18CSC203J – Computer	neering and Tecluter Science and Organization and Ctical Examination	nnology Engineering nd Architectur on		
_	Faculty of Engir Department of Comp 18CSC203J – Computer	neering and Tecluter Science and Organization actical Examinati	nnology Engineering nd Architectur on Year/Sem:		
Date : Register Number:_	Faculty of Engir Department of Compo 18CSC203J – Computer Model Prac	neering and Tecluter Science and Organization and Circuit Examination Dur	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio	II/III ds	
Date: Register Number: 1. Implement A	Faculty of Engir Department of Comp 18CSC203J – Computer Model Prac	neering and Tecluter Science and Organization are tical Examination During Dynamics (1998)	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio	II/III ds	
Date: Register Number: 1. Implement A	Faculty of Engir Department of Composite 18CSC203J – Computer Model Prace	neering and Tecluter Science and Organization are tical Examination During Dynamics (1998)	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio	II/III ds	
Date: Register Number:_ 1. Implement A 2. Write a prog	Faculty of Engir Department of Computer 18CSC203J – Computer Model Prace	neering and Tecluter Science and Organization are tical Examination Dure D, NOR, XOR are mplement of a number of a	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio nd SUB operat	II/III ds	
Date: Register Number: 1. Implement A	Faculty of Engir Department of Composite 18CSC203J – Computer Model Prace	neering and Tecluter Science and Organization are tical Examination During Dynamics (1998)	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio	II/III ds	
Date: Register Number:_ 1. Implement A 2. Write a prog	Faculty of Engir Department of Computer 18CSC203J – Computer Model Prace	neering and Tecluter Science and Organization are tical Examination Dure D, NOR, XOR are mplement of a number of a	nnology Engineering nd Architectur on Year/Sem: ration: 2 perio nd SUB operat	II/III ds	

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