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Scoring in Plutonium

How to choose the correct hardware generically

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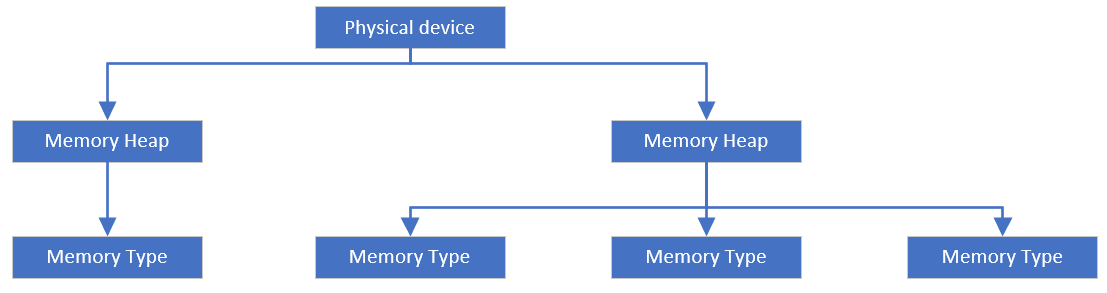
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# Memory type

Whenever a buffer is created, we need to assign it to a memory type on a specific memory heap. A Vulkan compatible graphics device can have multiple heaps available and, on those heaps, multiple memory types.  


The amount of memory heaps depends on the physical device as well as the types. So, we cannot safely pick a memory type at a preset index as that memory type will probably have vastly different properties on another physical device.

For this we need a system that can pick a memory type for a specific buffer usage. This needs to be a memory type that can handle the needs of the buffer and be the most efficient type available.

We can safely ignore the parent memory heap as the only real data that this stores the available bytes.

## Memory type property flags

|  |  |  |
| --- | --- | --- |
| Name | Description | Usefulness |
| None | No flags are set. | High |
| Device Local | Most efficient for device access. | High |
| Host Visible | Can be mapped for host access. | Low |
| Host Coherent | Cache management isn’t needed | Medium |
| Host Cached | Host caches the memory allocated | Medium |
| Lazily Allocated | Memory is allocated on demand | Low |
| Protected | Can be accessed by protected queues | Low |

### None

We know nothing about this memory type, the safest course of action is to just ignore it.

### Device Local

This is memory that is easily accessible by the physical device, this means that it is fast and has the highest change on cache hits, this is thusly a high priority to check.

### Host Visible

The is memory that can be accessed by the physical device and the CPU. This type of memory should only be used for staging resources to more efficient memory types as it’s quite slow.

### Host Coherent

This flag is only set if the memory type is also Host Visible, this additional flag means that we don’t have to flush or invalidate the memory shared by the physical device and the CPU. This makes it even slower than Host Visible as the memory has to always be available to the physical device and the CPU.

### Host Cached

This means the CPU caches the memory allocated with this type. CPU access to this memory is faster fast in this way but, we hardly need this as buffers will probably only be created ones.

### Lazily Allocated

This means the physical device will only allocate the memory once it needs it. this can save some performance in niche environments. For instance, in tile-based rendering. If one tile encapsulates the entire surface, we don’t actually have to copy it to a new buffer we that data will never be allocated with this memory type. This will save some performance.

### Protected

This flag doesn’t affect performance, so we can safely ignore it.

## Useful properties for the algorithm

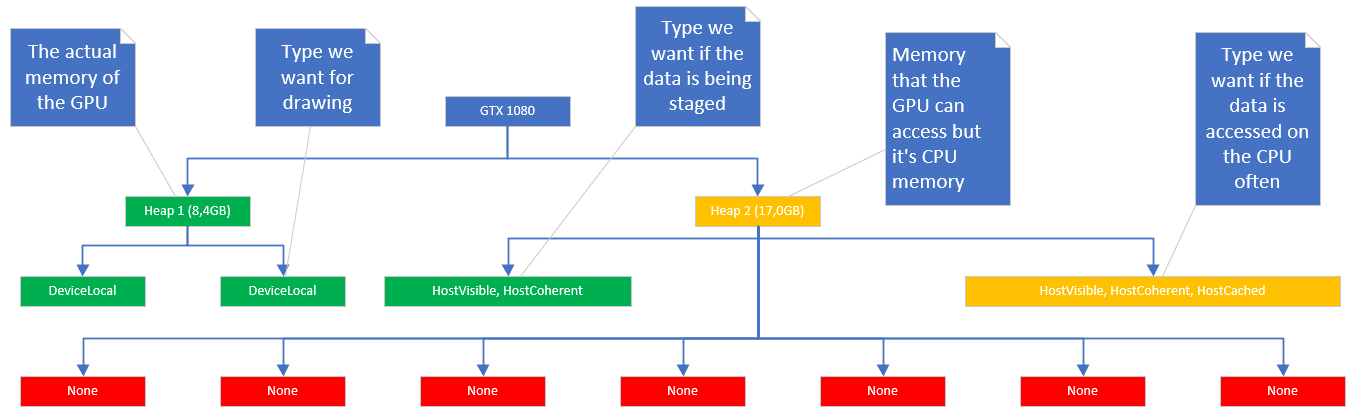
The flags we’ll be using to determine the most optimal memory type will be:

|  |  |  |
| --- | --- | --- |
| Name | Effect | Strength |
| None | Negative | High, fully ignore memory types with no flags |
| Device Local | Positive | High, highly prefer device local memory types |
| Host Visible | Negative | Low, we don’t really want it unless we need it |
| Host Coherent | Negative | Medium, quite slow, we want to ignore it |
| Host Cached | Positive | Low, only useful if CPU buffer |

The amount of memory available on the memory heap should not matter that much as most physical devices will only have one useful memory heap available.

## Desired execution

Here we’ll look at an example of getting the best memory types for two kinds of buffers, one staging buffer and one vertex buffer. This example will go through the available memory heaps and types on a Nvidia GTX 1080.



I colored coded how we want our algorithm to treat all the types and heaps.

* Green: Best suited for our purposes.
* Orange: Should only pick if greens aren’t available
* Red: Never pick these.

For buffers used in drawing commands we want to use any of the two device local memory types of heap 1.   
For staging buffers, we want to use the Host visible, host coherent memory type of heap 2.   
And for buffer that’re accessed by the CPU often we want to use the host visible, host coherent, host cached buffer on heap 2.

### Ranking

#### Vertex buffers (no host access needed)

* [Heap 1] Device local (3pts)
* [Heap 1] Device local (3pts)
* [Heap 2] Host visible, host coherent (0pts)
* [Heap 2] Host visible, host coherent, host cached (0pts)

#### Staging buffers (host access required)

* [Heap 2] Host visible, host coherent (0pts)
* [Heap 2] Host visible, host coherent, hot cached (0pts)

#### CPU buffers (host access required, caching preferred)

* [Heap 2] Host visible, host coherent, host cached (1pts)
* [Heap 2] Host visible, host coherent (0pts)

