ibd [Block] **ISMAC** [ISMAC Architecture]

Sensing

< >

< >

Comm.

:ISAC Controller

:S&C Time Division (TD) Allocation
:Sensing Data Control
:Communication Data Control

:Data Merging

:SAPR Result

< >

SAPR

:Sensing Data
Control (SDC)

:Communication
Data Control (CDC)

:Sensing Data
Processing

Frequency

Target 1   *Target N*   *User 1*   *User K*   *Time*

T_s T_s t_1 t_k

Sensing Phase   Cycle 1   *Communication Phase*

T