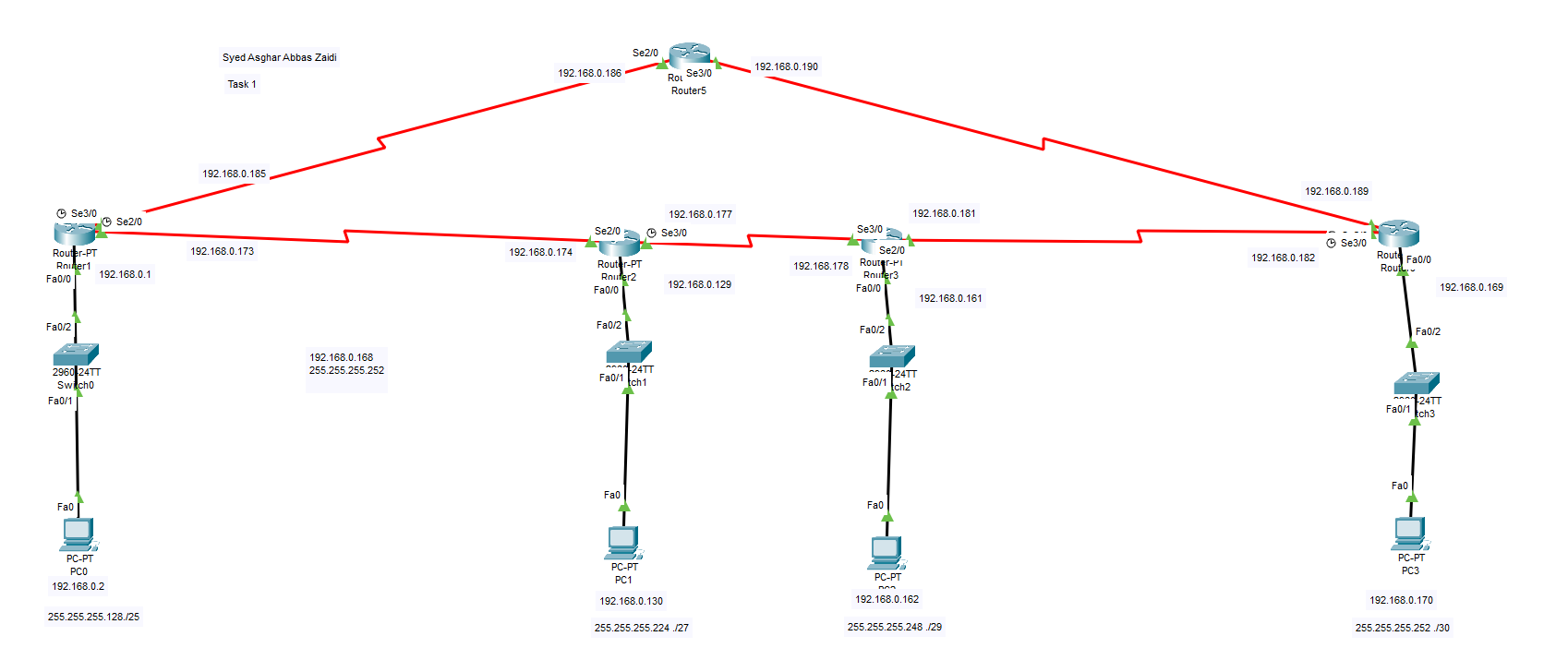
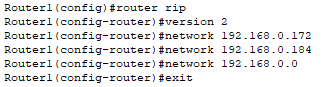
**Topology:**

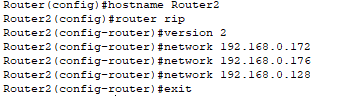


**Configuration:**

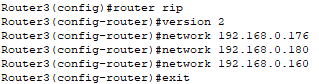
**R1:**

****

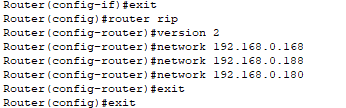
**R2:**

****

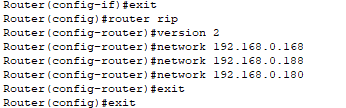
**R3:**

****

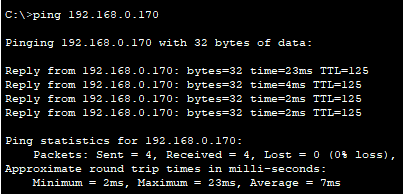
**R4:**

****

**R5:**

****

**Ping from PC0 to PC3:**

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**Side cases verified by Miss:**

