Huang Chen Ting

Senior Product Developer · Software Engineer

New Taipei City, Taiwan

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Summary

Senior Software Developer with 5 years at Synology, specializing in HA solutions, Linux development, and C/C++. Currently on a gap year, learning and open to opportunities. Always up for challenges !

Skills

Languages	C/C++, Python, Shell Scripting, JavaScript (Vue.js)
Technologies	Linux Development, Systemd, Jenkins CI/CD, Git, GDB, Docker
Specializations	HA Systems, Active-Active Clusters, Block-Level Backup, OS Internals, Concurrency, IPC

Work Experience

Senior Product Developer

Jan. 2024 - Feb 2025

Dual Active Cluster — *Synology Inc.*

- Engineered critical components supporting complex system operations, including boot/shutdown sequencing, RMA management procedures, configuration synchronization, and plugin integrations.
- Managed concurrency issues and improved system stability and performance in high-load, dual-active environments.
- Spearheaded proactive monitoring solutions to quickly identify, diagnose, and resolve potential bottlenecks and system vulnerabilities.

Product Developer

Dec. 2019 - Jan. 2024

High Availability System — *Synology Inc.*

- Developed robust modules for efficient system-level synchronization, reliable binding, and automated failover mechanisms.
- Leveraged critical technologies including DRBD, Pacemaker, Corosync, rsync, lsyncd, and systemd to ensure seamless high-availability solutions.
- Optimized system performance and minimized downtime through targeted enhancements and rigorous system testing.

Bare-Metal Backup & Restore for DSM

- Directed the integration and optimization of comprehensive block-level backup and restore operations, ensuring end-to-end data integrity.
- Designed and implemented advanced communication and coordination mechanisms leveraging IPC for efficient inter-module operations.
- Collaborated extensively with multiple teams to streamline disaster recovery processes and enhance user experience through improved reliability and faster recovery times.

Publications

Data-driven scheduling for the photolithography process in semiconductor

Mar. 2025

American Institute of Mathematical Science

- Co-authored a multi-objective optimization study employing NSGA-II and Data Envelopment Analysis to develop a composite dispatching rule for photolithography scheduling.

Education

National Chiao Tung University

Sep. 2017- Jun. 2019

Master of Sciene in Information Management — *Hsinchu, Taiwan*

National Chiao Tung University

Sep. 2013- Jun. 2017

Master of Business Administration in Management Science — *Hsinchu, Taiwan*