

TANVI SHARMA

202-375-7861

tanvi@umd.edu

#209, 8125, college park, MD-20740

LinkedIn profile: <https://www.linkedin.com/in/tanvi-sharma-6315a97b/>

Academics:

Master's in Telecommunication (expected may 2018) University of Maryland, College Park CGPA: **3.4**
Coursework: Network protocols, Internet of Things.

Bachelor's in Electronics and communication (march 2014) Jaypee University of Information Technology CGPA: **3.6**
Coursework: Electronics device and circuit, Digital Logic design, analog and Digital Circuit design, control systems, neural network.

Technical Skills:

Software: MapInfo, Win FIO, TEMS discovery, NEMO analyzer, Ericsson RAN Analyzer, Ericsson Cell and Frequency Optimization, Gladiator, mentum cell planner, Mat lab, Arduino, spectrum analyzer, Google cloud platform.

Networking: TCP/IP, BGP, OSPF, MPLS, RSVP, GSM, WCDMA, HSDPA+, LTE, VOLTE, Wi-Fi.

Languages: C, C++, Java, Python, HTML5, CSS, JavaScript, VLSI, Matlab, Linux, Django, MySQL.

Certificate: Ericsson certification of **Radio network optimization**.

Work Experience:

Network engineer (Ericsson, Radio Network Design and Optimization) (July 2014 - June 2016)

- Involved in planning designing and optimization of the radio network.
- Automation of a time consuming task to increase the cost effectiveness of the project and received appreciation for this work by the top leaders of the company.
- Understanding limitations posed by the customers and identifying technological limitations, thereafter coming to a tactical decision of establishing an efficient network.
- Have undergone training at **Ericsson** training center on: LTE radio functionality, LTE performance management, WCDMA performance management, **Small Cell Workshop and Software Design and Network**.

Lead the 4G planning and optimization in Himachal Pradesh (November 2015- June 2016)

Investigate and troubleshoot radio network performance issues by capturing ENodeB traces, MME traces and drive test logs. Daily monitoring and troubleshooting for issues like low uplink and downlink throughput issues, PS drop rate issue, low handover success rate for intra-LTE (X2/S1) handover, handover to UTRAN and TDD system, RRC success rate and other counter related issues.

LTE (4G) planning and optimization in Delhi and Hyderabad. (2015)

SCFT, cluster tuning and Clutter classification as per planning tool model.
Study new features and identify new business opportunities.

Spectrum reframing of 2G Vodafone, Idea 3G planning (Delhi) and Airtel 3G Punjab optimization. (December 2015)

To ensure the availability and efficiency of radio network elements by maintaining KPIs like Accessibility, Retain ability, Integrity, Availability and Mobility during spectrum transfers.

Small Cell and DAS projects (January 2016)

Responsible for designing, deploying, integrating and optimizing DAS and Small Cell Technologies (Ericsson DOT system) across a variety of clients. We used ERA (Ericsson radio analysis) and iBwave software tool to optimize the system.
Have undergone training at **Ericsson** training center on: LTE radio functionality, LTE performance management, WCDMA performance management, **Small Cell Workshop**

Wireless innovation Team-Spirent (September 2016-December 2016)

Currently working on the development and automation of testing scenarios for a Spirent product Elevate (for IMS and Volte) using **python**. The club focuses on test methodology in areas such as: Voice over LTE (VoLTE), voice and video over Wi-Fi. The automation of various test cases was embedded in the **Django** framework.

Projects:

Energy efficient routing protocol- research paper (2014)

Published in the **IEEE journal**: A Framework for Energy Efficient Routing Protocol for Homogeneous Wireless Sensor Network using Matlab.

IoT project (December 2016)

Working on Remote Controlled Door Lock using a Fingerprint Sensor using Arduino platform.

Socket Programming using Java (November 2016)

Reliability of the communication using integrity check, timeout and retransmission between client and a server using Java's UDP sockets.

Internship (Maruti Suzuki factory) (May - July 2013)

ANDON DISPLAY, a hardware project to display data on a seven segment from PIC13F communicated through hyper terminal. It was used to display instruction on the supply chain of Maruti car manufacturing factory. We developed a circuit diagram and implemented using C++.

Embedded systems project (Feb-2017)

LWDF for the CD to DAT Sample Rate Conversion application simulation using C: The project required an in depth knowledge of **Linux**.