Group 5 Project Report

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1 Waveforms

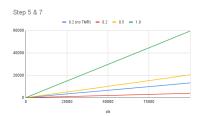


Figure 1: This is the LUT list of step 1

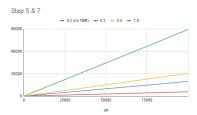


Figure 2: This is the LUT list of step 1

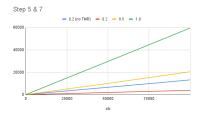


Figure 3: This is the LUT list of step 1

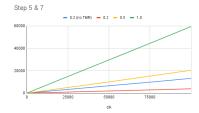


Figure 4: This is the LUT list of step 1

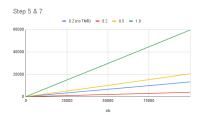


Figure 5: This is the LUT list of step 1

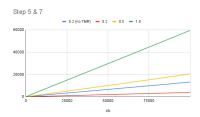


Figure 6: This is the LUT list of step 1

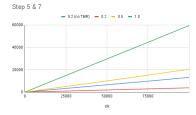


Figure 7: This is the LUT list of step 1

2 Graph

Step 5 & 7 - 0.2 (no TMR) - 0.2 - 0.5 - 1.0 40000 20000 25000 50000 75000 clk

Figure 8: The Y-axis being the number of errors. The X-Axis being the nr of clock cycles