

Group 5 Lab 2 Report

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1 Workflow

Step 5 is dependent on step 4, which contains the previous steps (FaultInjector, ArithLogic, Max4bit8to1). This allowed for each step to be worked on separately and up until the simulation and test stages in step 4 and step 5. For step 4 and step 5 it was assumed that the previous entities worked correctly. Working like this spread out the work load evenly amongst the group and if one completed their part they could help out the other group members, and lead to quickly being able to test and simulate.

2 LUT list

Report Cell Usage:			
+-----+-----+-----+			
	Cell	Count	
+-----+-----+-----+			
1	LUT6	8	
2	MUXF7	4	
3	IBUF	35	
4	OBUF	4	
+-----+-----+-----+			

Figure 1: This is the LUT list of step 1

Report Cell Usage:

	Cell	Count
1	LUT6	8
2	MUXF7	4
3	IBUF	35
4	OBUF	4

Figure 2: This is the LUT list of step 2

Report Cell Usage:

	Cell	Count
1	LUT6	8
2	MUXF7	4
3	IBUF	35
4	OBUF	4

Figure 3: This is the LUT list of step 3

Report Cell Usage:

	Cell	Count
1	LUT6	8
2	MUXF7	4
3	IBUF	35
4	OBUF	4

Figure 4: This is the LUT list of step 4

Report Cell Usage:

	Cell	Count
1	LUT6	8
2	MUXF7	4
3	IBUF	35
4	OBUF	4

Figure 5: This is the LUT list of step 5

3 Simulations

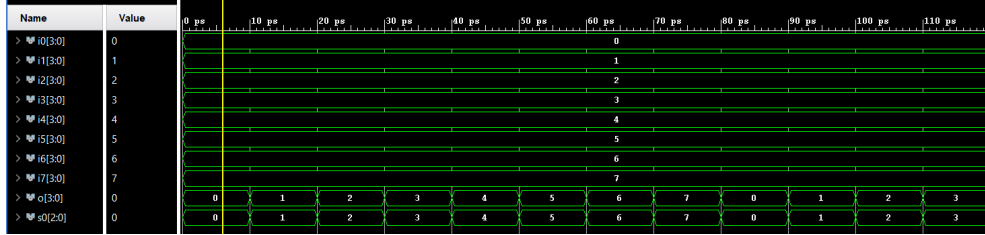


Figure 6: This is the simulation of step 1

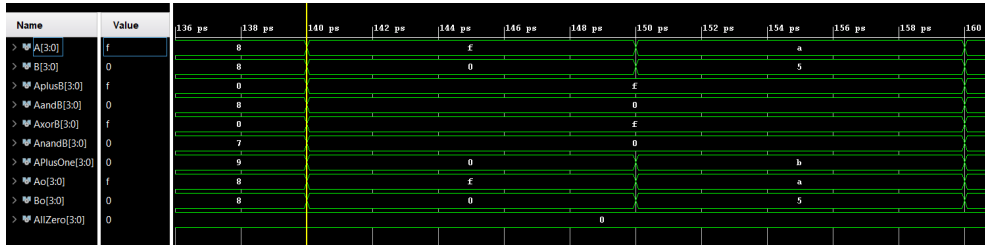


Figure 7: This is the simulation of step 2

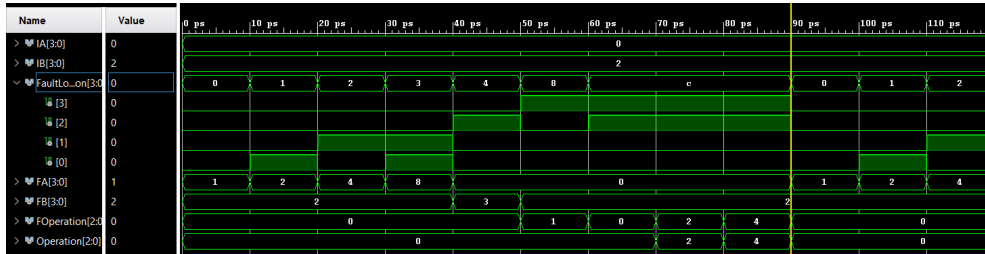


Figure 8: This is the simulation of step 3

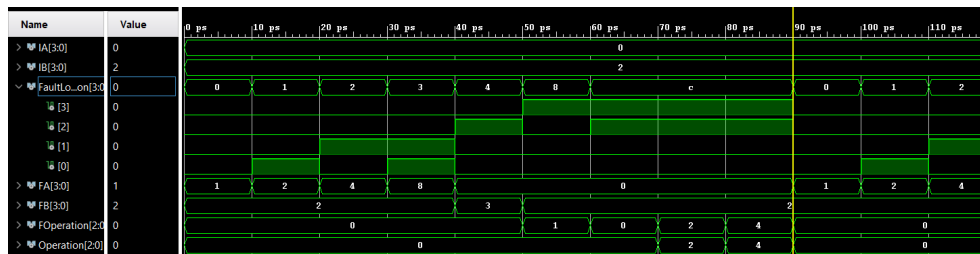


Figure 9: This is the simulation of step 4

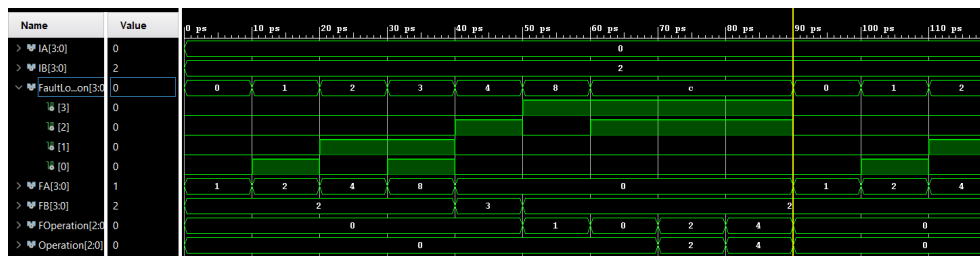


Figure 10: This is the simulation of step 5