

## MGGMBA003

```
@Log      < Share

[2021-10-17 17:19:45 EDT] iverilog '-wall' '-g2012' design.sv testbench.sv && unbuffer vvp a.out
A          B          opcode  out
VCD info: dumpfile dump.vcd opened for output.
00000100  00000001  0000    00000101
00000100  00000001  0110    00001001
00000100  00000001  1000    00000010
00000100  00000001  1111    00000000
Done
```

