



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No. : **C 0301901** BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No. : 21071027765

Name of the College : N2-NIET, CHOUTUPPAL

Name : RACHANKONDA SHIVACHARAN

Month & Year of Final Exam : May, 2013

Hall Ticket No. : 09N21A0485

Year of Admission : 2009-2010

Class Awarded : FIRST CLASS WITH DISTINCTION

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	20	47	67	4	2	MATHEMATICS - I	25	74	99	6
3	MATHEMATICAL METHODS	24	75	99	6	4	ENGINEERING PHYSICS	24	71	95	4
5	ENGINEERING CHEMISTRY	24	51	75	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	24	36	60	6
7	ENGINEERING DRAWING	23	40	63	4	8	COMPUTER PROGRAMMING LAB	25	46	71	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	25	50	75	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	41	64	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	22	44	66	4						
I SEMESTER						II SEMESTER					
1	MATHEMATICS-III	25	45	70	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	20	46	66	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	20	43	63	3	2	ELECTRONIC CIRCUIT ANALYSIS	17	36	53	4
3	ENVIRONMENTAL STUDIES	24	30	54	3	3	PULSE & DIGITAL CIRCUITS	23	59	82	4
4	ELECTRIC CIRCUITS	17	27	44*	4	4	SWITCHING THEORY & LOGIC DESIGN	23	37	60	4
5	ELECTRONIC DEVICES & CIRCUITS	19	28	47	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	20	48	68	4
6	SIGNALS & SYSTEMS	24	26	50	4	6	ELECTRICAL ENGINEERING LAB	25	49	74	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	22	45	67	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	24	49	73	2
8	BASIC SIMULATION LAB	21	44	65	2	8	PULSE & DIGITAL CIRCUITS LAB	22	45	67	2
I SEMESTER						II SEMESTER					
1	CONTROL SYSTEMS	25	65	90	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	22	45	67	4
2	COMPUTER ORGANIZATION	16	26	42*	4	2	OPERATING SYSTEMS	22	30	52	4
3	ANTENNAS & WAVE PROPAGATION	23	29	52	3	3	DIGITAL COMMUNICATIONS	23	32	55	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	21	39	60	4	4	MICROPROCESSORS & MICROCONTROLLERS	18	50	68	4
5	ANALOG COMMUNICATIONS	22	26	48	3	5	DIGITAL SIGNAL PROCESSING	24	47	71	4
6	IC APPLICATIONS	22	41	63	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	21	48	69	2
7	ANALOG COMMUNICATIONS (LAB)	21	36	57	2	7	DIGITAL SIGNAL PROCESSING (LAB)	23	47	70	2
8	IC APPLICATIONS (LAB)	21	46	67	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	20	42	62	2
I SEMESTER						II SEMESTER					
1	MANAGEMENT SCIENCE	20	35	55	3	1	CELLULAR & MOBILE COMMUNICATIONS	22	26	48	3
2	VLSI DESIGN	20	36	56	4	2	RADAR SYSTEMS	23	31	54	3
3	MICROWAVE ENGINEERING	24	41	65	3	3	WIRELESS COMMUNICATIONS & NETWORKS	23	31	54	3
4	COMPUTER NETWORKS	23	26	49	4	4	INDUSTRY ORIENTED MINI PROJECT	-	44	44	2
5	TELECOMMUNICATION SWITCHING SYSTEMS	22	36	58	3	5	SEMINAR	47	-	47	2
6	EMBEDDED SYSTEMS	24	26	50	4	6	MAJOR PROJECT	45	144	189	10
7	E-CAD AND VLSI LAB	24	49	73	2	7	COMPREHENSIVE VIVA	-	96	96	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	23	49	72	2						
(# Project Internal= 50, External=150)											

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Number of Credits registered for : 200 Aggregate Marks Secured for best : 192

Aggregate Marks Secured : 3754 OUT OF 5200 (72.19%)

Date of Issue : June 22, 2013

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)



CONTROLLER OF EXAMINATIONS