

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

(CONSOLIDATED MARKS MEMO / CREDIT SHEET)

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BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No.: Name

RACHANKONDA SHIVACHARAN

Name of the College: N2-NIET, CHOUTUPPAL

Month & Year of Final Exam: May, 2013

Maximum Marks in Theory ENGLISH MATHEMATICAL METHODS ENGINEERING CHEMISTRY ENGINEERING DRAWING ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB BASIC SIMULATION LAB	25 20 24 24 23 25 22 25 20 24 17 19 24 22	45 43 30 27	67 99 75 63 75 66 II 70 63 54 44	4 4 YEAI	2 4 6 8 10	MAXIMUM Marks in Lab MATHEMATICS - I ENGINEERING PHYSICS COMPUTER PROGRAMMING & DATA STRUCTURES COMPUTER PROGRAMMING LAB ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING ELECTRONIC CIRCUIT ANALYSIS	25 25 24 24 25 23 20 17	74 71 36	95 60 71 64
MATHEMATICAL METHODS ENGINEERING CHEMISTRY ENGINEERING DRAWING ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRORIC CIRCUITS ELECTRORIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	24 24 23 25 22 25 20 24 17 19 24	755 511 400 500 444 453 300 27	67 99 75 63 75 66 II 70 63 54 44	4 6 4 4 4 4 7 7 8 3	2 4 6 8 10	ENGINEERING PHYSICS COMPUTER PROGRAMMING & DATA STRUCTURES COMPUTER PROGRAMMING LAB ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	24 24 25 23 23	71 36 46 41	95 60 71 64
MATHEMATICAL METHODS ENGINEERING CHEMISTRY ENGINEERING DRAWING ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRORIC CIRCUITS ELECTRORIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	24 24 23 25 22 25 20 24 17 19 24	755 511 400 500 444 453 300 27	99 75 63 75 66 11 70 63 54 44	6 4 4 4 4 7 4 3 3	4 6 8 10	ENGINEERING PHYSICS COMPUTER PROGRAMMING & DATA STRUCTURES COMPUTER PROGRAMMING LAB ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	24 24 25 23 23	71 36 46 41	95 60 71 64
ENGINEERING CHEMISTRY ENGINEERING DRAWING ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	24 23 25 22 25 20 24 17 19 24	51 40 50 44 45 43 30 27	75 63 75 66 11 70 63 54 44	4 4 4 4 YEAI 3 3	6 8 10	COMPUTER PROGRAMMING & DATA STRUCTURES COMPUTER PROGRAMMING LAB ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	24 25 23 20 17	36 46 41 46	60 71 64 66
ENGINEERING DRAWING ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	23 25 22 25 20 24 17 19 24	40 50 44 45 43 30 27	63 75 66 11 70 63 54 44	4 4 4 YEAI 3 3	8 10	COMPUTER PROGRAMMING LAB ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	25 23 20 17	46 41 46	71 64 66
ENGG. PHYSICS & ENGG. CHEMISTRY LAB IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	25 22 25 20 24 17 19 24	45 43 30 27	75 66 11 70 63 54 44	4 4 YEAI 3 3	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	23 20 17	41	64
IT WORKSHOP & ENGINEERING WORKSHOP I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	25 20 24 17 19 24	45 43 30 27	70 63 54 44	4 YEAI 3 3	1 2	II SEMESTER PRINCIPLES OF ELECTRICAL ENGINEERING	20	46	66
I SEMESTER MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	25 20 24 17 19 24	45 43 30 27	70 63 54 44	3 3	1 2	PRINCIPLES OF ELECTRICAL ENGINEERING	17		
MATHEMATICS-III PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	20 24 17 19 24	43 30 27	70 63 54 44	3	1 2	PRINCIPLES OF ELECTRICAL ENGINEERING	17		
PROBABILITY THEORY & STOCHASTIC PROCESSES ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	20 24 17 19 24	43 30 27	63 54 44	3	2		17		
ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	20 24 17 19 24	43 30 27	63 54 44	3	120-00		17		
ENVIRONMENTAL STUDIES ELECTRIC CIRCUITS ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	24 17 19 24	30 27	54 44		_				
ELECTRONIC DEVICES & CIRCUITS SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	19 24		1		3	PULSE & DIGITAL CIRCUITS	23		82
SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS LAB	19 24		1	4	4	SWITCHING THEORY & LOGIC DESIGN	23	37	60
ELECTRONIC DEVICES & CIRCUITS LAB	- 1		47	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES			68
	- 1	26	50	4	6	ELECTRICAL ENGINEERING LAB	25	49	74
BASIC SIMULATION LAB	1 44	45		2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	24	49	73
	21	44	65	2	8	PULSE & DIGITAL CIRCUITS LAB	22	45	67
ISEMESTER		<u> </u>	II	YEA	R	II SEMESTER	ш		
CONTROL SYSTEMS	25	65	90	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	22	45	67
COMPUTER ORGANIZATION	16	26	42	4	2	OPERATING SYSTEMS	22	30	52
ANTENNAS & WAVE PROPAGATION	23	29		1	3	DIGITAL COMMUNICATIONS	23		55
ELECTRONIC MEASUREMENTS & INSTRUMENTATION	21	39	60	4	4	MICROPROCESSORS & MICROCONTROLLERS	18	50	68
ANALOG COMMUNICATIONS	22	26	48	3	5	DIGITAL SIGNAL PROCESSING	24		71
IC APPLICATIONS	22	41	63	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	21	48	69
ANALOG COMMUNICATIONS (LAB)	21	36	57	2	7	DIGITAL SIGNAL PROCESSING (LAB)	23		
IC APPLICATIONS (LAB)	21	46	67	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	20	42	62
ISEMESTER			<u> </u>	YEA	R.	II SEMESTER	Ь.		<u> </u>
MANAGEMENT SCIENCE	20	35	55	3	1	CELLULAR & MOBILE COMMUNICATIONS	22	26	48
VLSI DESIGN	20	36	56	4	2	RADAR SYSTEMS	23		
	24			3	3	WIRELESS COMMUNICATIONS & NETWORKS			1
	23	26	49	4	4	INDUSTRY ORIENTED MINI PROJECT	1.	1	44
	22		1	3	5	SEMINAR	47		47
COMPUTER NETWORKS	24			4	6	MAJOR PROJECT		1	1
COMPUTER NETWORKS TELECOMMUNICATION SWITCHING SYSTEMS	24			2	7	COMPREHENSIVE VIVA	-		1
COMPUTER NETWORKS TELECOMMUNICATION SWITCHING SYSTEMS EMBEDDED SYSTEMS	24								
V	MANAGEMENT SCIENCE /LSI DESIGN MICROWAVE ENGINEERING COMPUTER NETWORKS FELECOMMUNICATION SWITCHING SYSTEMS	MANAGEMENT SCIENCE ALSI DESIGN ALCROWAVE ENGINEERING COMPUTER NETWORKS ELECOMMUNICATION SWITCHING SYSTEMS EMBEDDED SYSTEMS 20 21 22 23 24 24 24 24 24 24 24	ANAGEMENT SCIENCE 20 35 ALSI DESIGN 20 36 AICROWAVE ENGINEERING 24 41 COMPUTER NETWORKS 23 26 ELECOMMUNICATION SWITCHING SYSTEMS 22 36 EMBEDDED SYSTEMS 24 26 E-CAD AND VLSI LAB 24 49	ANANAGEMENT SCIENCE 20 35 55 ALSI DESIGN 20 36 56 ALSI DESIGN 24 41 65 COMPUTER NETWORKS 23 26 49 ELECOMMUNICATION SWITCHING SYSTEMS 24 26 50 ELAD AND VLSI LAB 24 49 73	ANANAGEMENT SCIENCE 20 35 55 3 ALSI DESIGN 20 36 56 4 ALSI DESIGN 24 41 65 3 COMPUTER NETWORKS 23 26 49 4 ELECOMMUNICATION SWITCHING SYSTEMS 22 36 58 3 EMBEDDED SYSTEMS 24 26 50 4	MANAGEMENT SCIENCE 20 35 55 3 1 VLSI DESIGN 20 36 56 4 2 MICROWAVE ENGINEERING 24 41 65 3 3 COMPUTER NETWORKS 23 26 49 4 4 ELECOMMUNICATION SWITCHING SYSTEMS 22 36 58 3 5 EMBEDDED SYSTEMS 24 26 50 4 6 E-CAD AND VLSI LAB 24 49 73 2 7	ANAGEMENT SCIENCE ALSI DESIGN CICROWAVE ENGINEERING COMPUTER NETWORKS CIELECOMMUNICATION SWITCHING SYSTEMS COMPUTED SYSTEMS COMPUTED NETWORKS COMPUTED NETWORKS COMPUTED NETWORKS COMPUTED NETWORKS CIELECOMMUNICATION SWITCHING SYSTEMS COMPUTED NETWORKS CIELECOMMUNICATION SWITCHING SYSTEMS CIEDED SYSTEMS CIEDED SYSTEMS CIEDED NO VLSI LAB CIEDED SYSTEMS CIEDED NO VLSI LAB CIEDD NO VLSI LAB CIED	ANANAGEMENT SCIENCE ALSI DESIGN 20 35 55 3 1 CELLULAR & MOBILE COMMUNICATIONS 22 36 56 4 2 RADAR SYSTEMS 23 36 56 4 2 RADAR SYSTEMS 23 36 37 WIRELESS COMMUNICATIONS & NETWORKS 23 26 49 4 4 INDUSTRY ORIENTED MINI PROJECT 24 26 50 4 6 MAJOR PROJECT 25 COMPUTER SYSTEMS 26 49 73 2 7 COMPREHENSIVE VIVA 27 COMPREHENSIVE VIVA	ANANAGEMENT SCIENCE ANANAGEMENT SCIENCE ALSI DESIGN 20 36 56 4 2 RADAR SYSTEMS 23 31 ANICROWAVE ENGINEERING 24 41 65 3 3 WIRELESS COMMUNICATIONS & NETWORKS 23 26 49 4 4 INDUSTRY ORIENTED MINI PROJECT ELECOMMUNICATION SWITCHING SYSTEMS 24 26 50 4 6 MAJOR PROJECT E-CAD AND VLSI LAB 20 35 55 3 1 CELLULAR & MOBILE COMMUNICATIONS 22 26 26 3 3 31 ANDUSTRY ORIENTED MINI PROJECT 5 SEMINAR 47 - 45 144 46 MAJOR PROJECT 47 COMPREHENSIVE VIVA - 96

Number of Credits registered for : 200 Aggregate Marks Secured for best: 192

Aggregate Marks Secured: 3754 OUT OF 5200 (72.19%)

Date of Issue: June 22, 2013 (see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

CONTROLLER OF EXAMINATIONS