

## Experiment No. 03

### 3.1 Name of the Experiment

Analyzing the dynamics of a first order system using RC circuit & RL circuit in MATLAB Simulink.

### 3.2 Objectives

- To gather knowledge about the characteristics of a first order system using RL & RC circuit.
- To know how to obtain the characteristics of RC & RL circuit using Simulink platform.
- To observe the characteristics curve through Simulink.
- To command the gains in MATLAB file and run them in designed block diagram in Simulink.

### 3.3 Theory

#### 3.3.1 RL Circuit

Here, KVL is used in the RL circuit for current  $I$  and voltage  $V$ , and  $V_L$  is seen across the inductor. While the circuit is closed,  $V_R$  was dropped across the resistance.

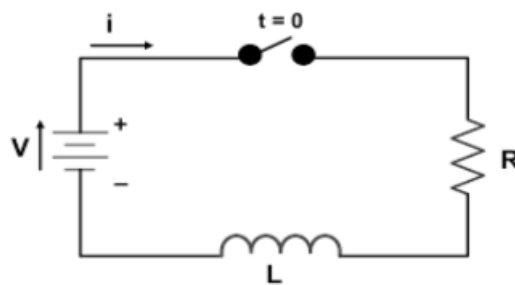


Fig.3.1. RL circuit

Here,

Voltage across resistance =  $V_R$

Voltage across inductor =  $V_L$

Inductance =  $L$

Resistance =  $R$

Current =  $i$

So, Voltage across Resistance,

$$V_R(t) = Ri(t)$$

$$i(t) = \frac{1}{R} V_R(t)$$

Voltage across inductor,

$$V_L(t) = L \frac{di(t)}{dt}$$

$$i(t) = \frac{1}{L} \int_0^t V_L(t) dt$$

Applying KVL in the RL circuit,

$$-V_i + V_R + V_L = 0$$

$$\text{Or, } V_R + V_L = V_i$$

$$\text{Or, } iR + L \frac{di}{dt} = V_i$$

$$\text{Or, } \frac{di}{dt} = \frac{1}{L} * V_i - \frac{iR}{L}$$

This equation defines the dynamics of the RL circuit.

### 3.3.2 RC Circuit

KCL is used in the RC circuit for current I and voltage V, and VC is measured across the capacitor. While the circuit is closed, VR was dropped across the resistance.

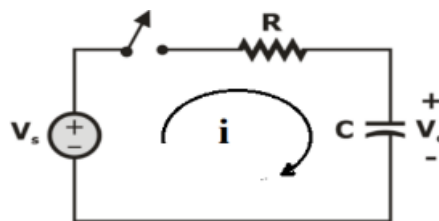


Fig.3.2. RC circuit

Here,

Voltage across resistance =  $V_R$

Voltage across capacitor =  $V_c$

Inductance =  $L$

Resistance =  $R$

Current across capacitor =  $i_c$

So, Voltage across Resistance,

$$V_R(t) = Ri(t)$$

$$i(t) = \frac{1}{R} V_R(t)$$

Voltage across capacitor,

$$V_C(t) = \frac{1}{C} \int i(dt)$$

$$i(t) = C \frac{dV_C(t)}{dt} = \frac{1}{R} V_R(t)$$

Applying KCL in the RC circuit,

$$\text{So, } iR = i_C$$

$$\text{Or, } \frac{1}{R} (V_i - V_C) = C \frac{d}{dt} V_C$$

$$\text{Or, } (V_i - V_C) = RC \frac{d}{dt} V_C$$

$$\text{Or, } V_C = V_i - RC \frac{d}{dt} V_C$$

$$\text{Or, } RC \frac{d}{dt} V_C = (V_i - V_C)$$

$$\text{Or, } \frac{d}{dt} V_C = \frac{1}{RC} V_i - \frac{1}{RC} V_C$$

This equation defines the dynamics of a RC circuit.

### 3.4 Apparatus

- Simulink
- MATLAB

### 3.5 Block Diagrams

- **RL Circuit**

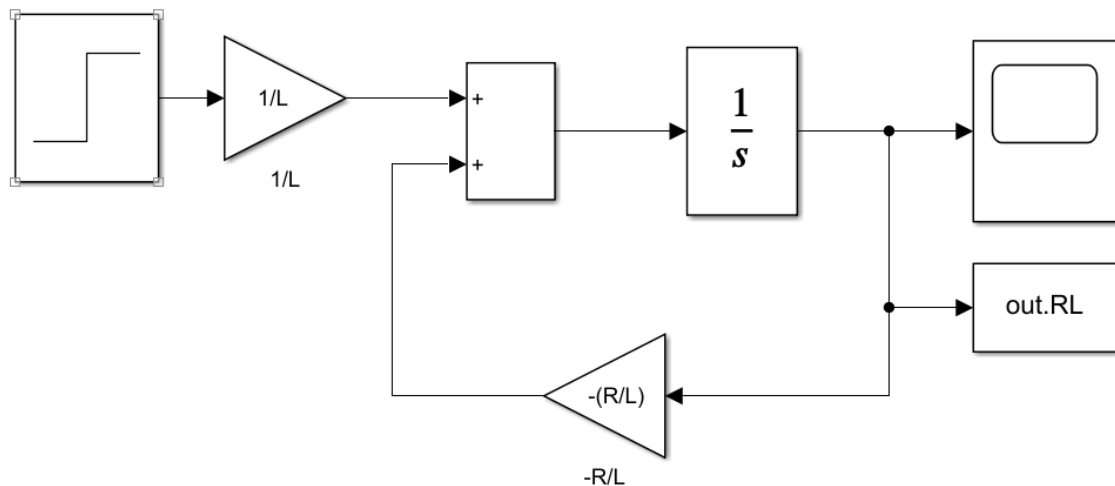


Fig 3.3: Block diagram for the RL circuit

- **RC Circuit**

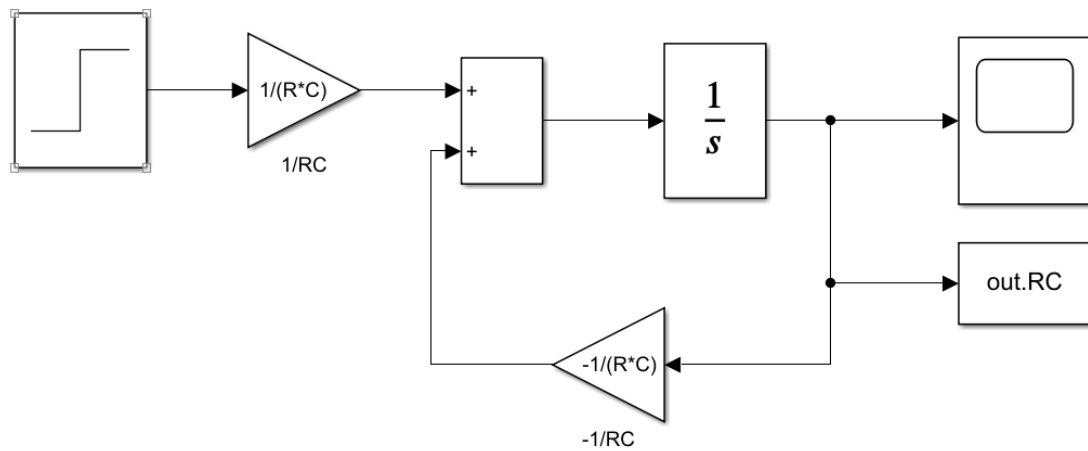


Fig 3.4: Block diagram for the RC circuit

### 3.6 MATLAB

- **RL Circuit Code**

```
clc;
R=input('R=');
L=input('L=');
disp(plot(out.RL));
ylabel('Inductor Current');
xlabel('Time');
title('RL Circuit');
```

#### Command window

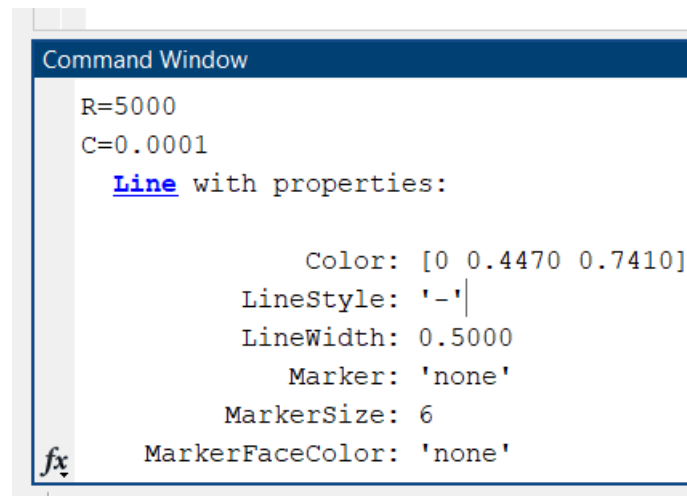
```
Command Window
R=5
L=1
Line with properties:
    Color: [0 0.4470 0.7410]
    LineStyle: '-'
    LineWidth: 0.5000
    Marker: 'none'
    MarkerSize: 6
    MarkerFaceColor: 'none'
```

- **RC Circuit Code**

```
clc;
R=input('R=');
C=input('C=');
x=plot(out.RC);
disp(plot(out.RC));
```

```
ylabel('Capacitor Voltage');  
xlabel('Time');  
title('RC Circuit');
```

#### Command Window



### 3.7 Characteristics Curves

- **RL Circuit**

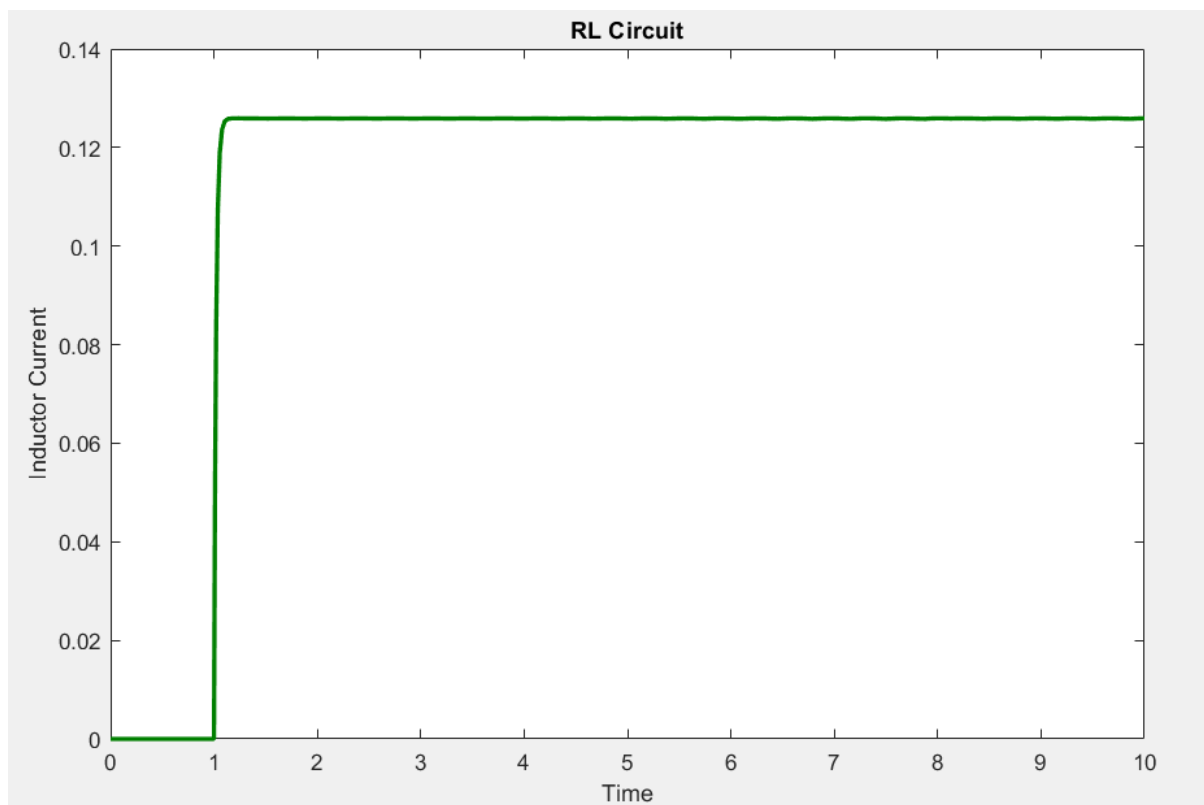


Fig 3.5: Inductor current vs time curve for the analysis of RL circuit

- **RC Circuit**

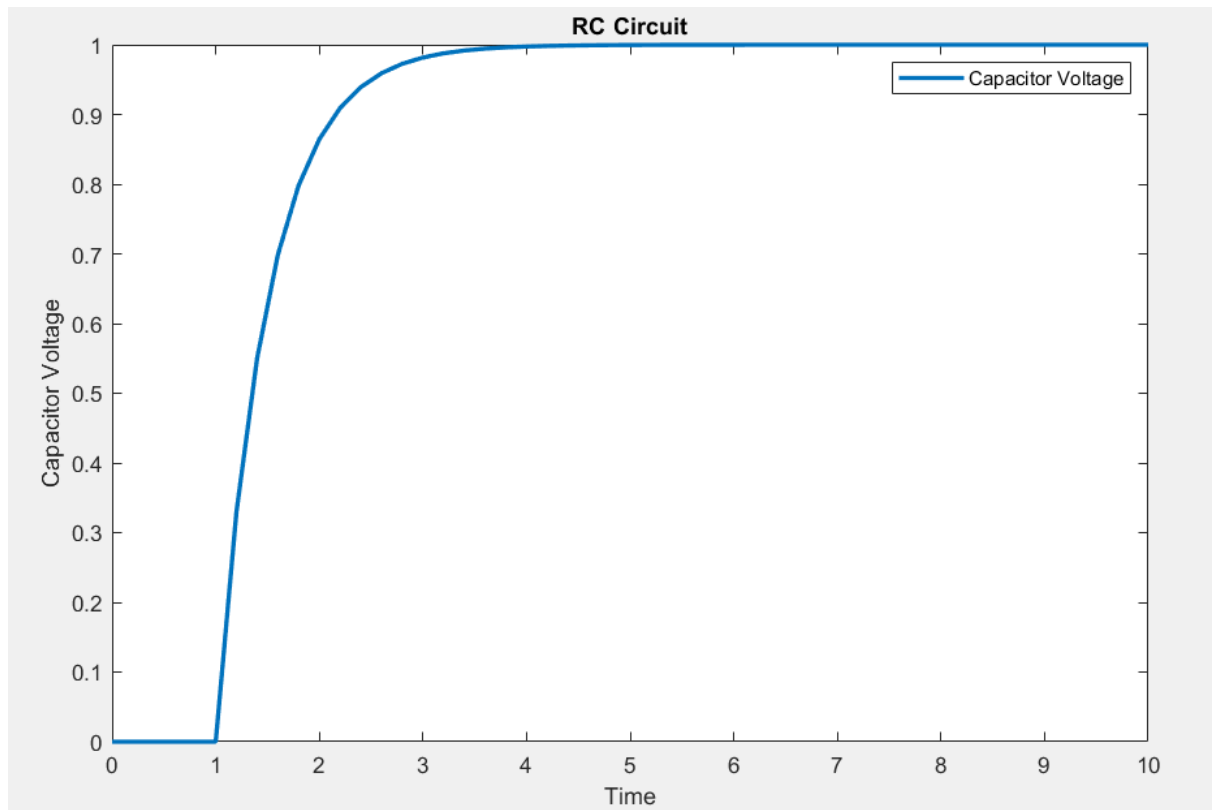


Fig 3.6: Capacitor voltage vs time curve for the analysis of RC circuit

### 3.8 Discussion & Conclusion

Here, we investigated the dynamics of a first order system in this experiment. As first order systems, the RL and RC circuits had one differential equations. It's because they had only one energy storage device.

Again, the obtained equations are acting as dynamics of the system. The equation was then depicted in a block diagram and then the appropriate values were provided via MATLAB (.m file). A graphical depiction of the dynamics was plotted and used to test the dynamics using unity step input.

The main goal of this experiment was to learn about the properties of a RL & RC circuit and how to use MATLAB Simulink to identify such qualities. Various blocks from the Simulink library were utilized in the experiment, and their functionalities were observed by taking certain inputs and graphing the outputs. As a result, the experiment was a success