cs. Ac vottage controllers: - controls the time value

of an Ac Voltage. Application:

- speed control of induction motors.

- varies the supply wortage.

61. WTO: (gade turn off thyristor)

- special type of thyristor

- high power semi-conductor device

- fally controllable switches.

65. Voltage sensore: Voltage monistoring device

66. Corrona - effect: The phenomenon of ionisation of air surrounding a conductor due to which luminous glow and hissing noise is rised is known as corrona-effect.

69. Tarrit: trade at which electrical energy is sold to a consumer.

## 70. Variable port addressing.

-> Allows data transfer between AL or Ax and a 16 bit port address.

fixed-point addressing |

-> 8 - bit

-> only AL is used

Varciable port addressing

→AX is used

### 71. SPWM: (Advantages):

-> switching treevenry constant

-> easy to calculate the losses of switching devices.

-> theremal design is easy.

## 72. Fuse

Cincuit Bricaker

- products devices from overloading and thous chemit only
- carril be revied
- -> protects device from shout circuit and overcloading
- can be neused.
- is a metal piece climate breakers is an interna that metts when over-Coading occurs.
  - switch that gets tripped when there is excess current

73. Software: Set of computer programs and associated documentation and data.

74. Algorithm of maximum powerepoint:

max. power point - the point at which the product of the cultitient and voltage equal the greatest value. Algorithm - artigal 1

75. program: -> set of instructions

( TDM: (time-division multiplexing)

Data, voice and video communication technique that interleaves several low speed signals into one highspeed transmission channel tone medium)

TDMA: litime division multiple access)

- -> charmel access method for shared medium networ
- Allows some the snare of instructions gwollA echannel.

77. single-line diagram: Isimplified representation of an electrical system)

# 79. GP. Robi TOTA Modulation technique use TOTO?

80. PCM

81. SCR: Sincon controlled nectifier.

- H is a three terminal thyristor that acts like a silicon nectitiere diode whose conductor is controlled by an input current.

Arode Controde

2. FDMA: Arequency Division Multiple Access.

SSB BSB

DSD SSB

- , removes the carriers but treamsmits both sidebands
- -> removes the corrier and one of the sidebo
- » Double side-band modulation technique.
- -> single side-band modulat technique
- ISSB is better than 1829
- -> utilizes half the bondwidth, the theren noise is reduced to half.

ssB -> single side-band DSB -> Double side-band.

84. Relative Addressing: JMP JNZ JE

85. - 2

86. Intinite Bus: An infinite bus is a power system so large that its voltage and frequency remain constant negaraless of how much real on reactive powers is drawn treom on supplied to it.

- Infinite bue - 20 Voltage Angle constant.

87. What is teding a channel ?

- intormation to other there capability.

88. Volatile memory: eram) remarca read + write 2 the von

89. Handshaking: interconnection between microprocesson and peripherials.

90. INT 21 H TOTA type- 20 interrrupt a

- tottware interrupt.

#### 91. Fuse

#### Relay

electrical circuit.

to run heavier equipments.

on. Diversity Factor: (greater than 1)

stratio of sum of the individual maximum demand of a system to the maximum demand of the whole system.

93. Total hatemonics to for tootable -> ?

#### 94. Hardware

#### software

-> physical & visible components. -> set of instructions
CPU. Keyboard, mouse.

#### 95. Maskable

ignorced / disabled by the CPU.

## non-maskable

-> can't be ignored/ disabled. The CPU must accept this interrupt. 06 Input CARTO code:

MOV AH : 1 INT 21 H

output display- 20 code:
MON AH, 2
ANT 21 H

97. Form factor: RMS Value
Average Value

half-wave - 80 form factor 1.57.

98. Ideal value of form tactor: =1

99. Transmission line-2 bounded wine took used my

, or restre will e

100. Difference between MATLAB and SIMULINK:

code. H

La graphical usen intendace

1. THD:

2. Vector number:

3. VSB modulation: Vestigial-Sideband modulation.

one sideband.

\$. SSB, DSB TOTA used ZZI ?

-> to extract the desired sideband.

7. Negative resistance zorozzi ettos ?

-> oscillatores, amplifieres.

9. Interrupt .

10. segmentation rona rola?

11. Stack Bus: -> provides neterience for the Nortage & angle. (Also called meterionie bus)

16. Noise: - unwanted sound

17 -> (1)

18. 3008 8086 16-bit mpn. 8-6H mpu

Kit mode 19. PC Mode

20 - (2)

21. read mode- so diagram- a gap ron arrow a

551 NEAK:

the procedure is defined in the same segment as the code.

the procedure is defined in some other segments. 231 Light of sight (LOS): A type of propagation that ean transmit and neceive data only where transmit and neceive etations are in view of each other, without any obstacles.

- 24 Boost conventen- a capaciton- so with too?
- -> for titlering purpose.
- 25. Solar panel- à voita converter use solà à
- -> Buck-boost converter.
- 26. Stack operation with still ( without PUCH & POP)