**Experiment No. 01**

* 1. **Experiment Name**

Study of sampling error vs frequency graph and quantization error vs ADC graph using Python Code

* 1. **Objectives**
* To get a better understanding of sampling error and quantization error procedure
* To get familiar with the procedure of sampling any signal using python
  1. **Apparatus**
* Jupyter Notebook
  1. **Theory**

**1.4.1 Sampling & sampling error**

Sampling is the digitization of the coordinate value. Sampling is done prior to the quantization process.

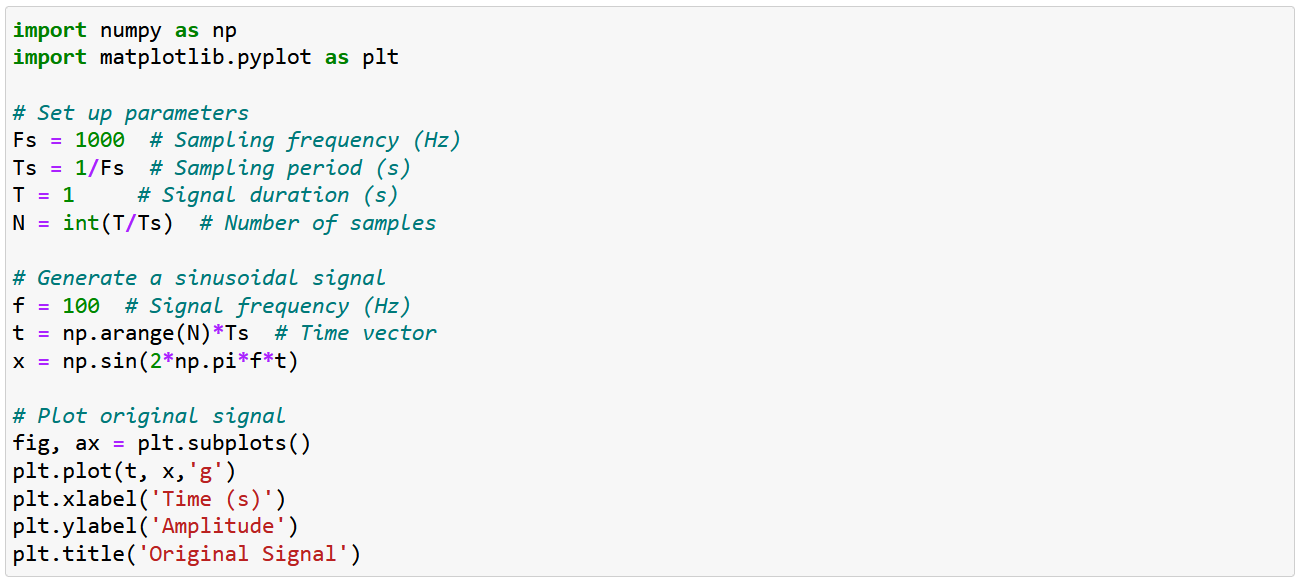
In digital signal processing, sampling error refers to the discrepancy between the original analog signal and the digital signal obtained by sampling the analog signal at a finite rate. Aliasing and quantization noise are two examples of the different distortions that can be caused by sampling inaccuracy.

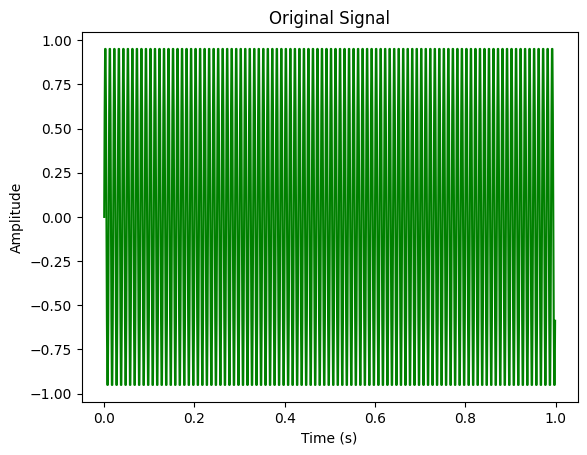
**1.4.2 Quantization & quantization error**

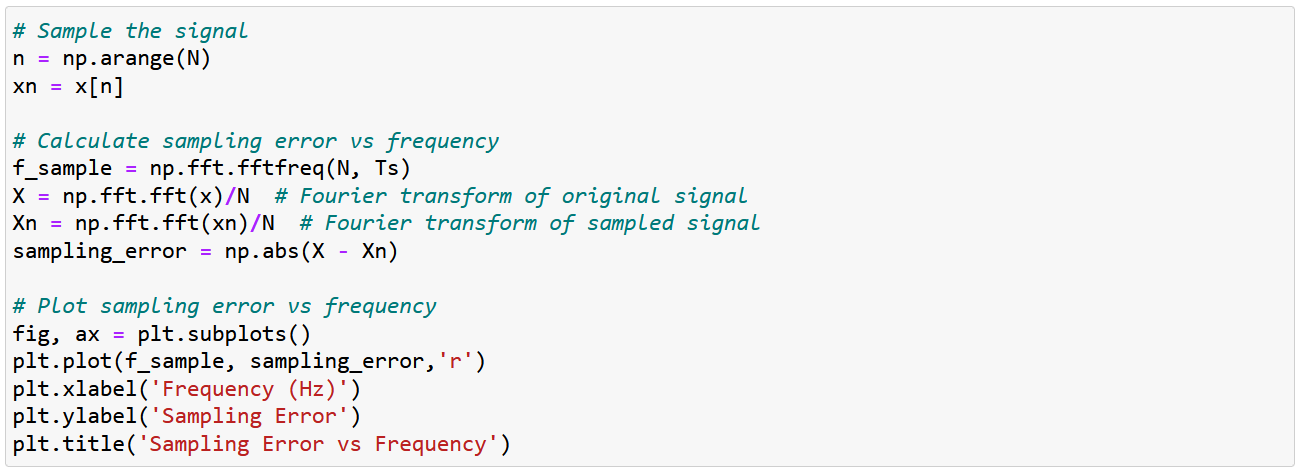
Quantization is the digitization of the amplitude value. Quantization is done after the sampling process.

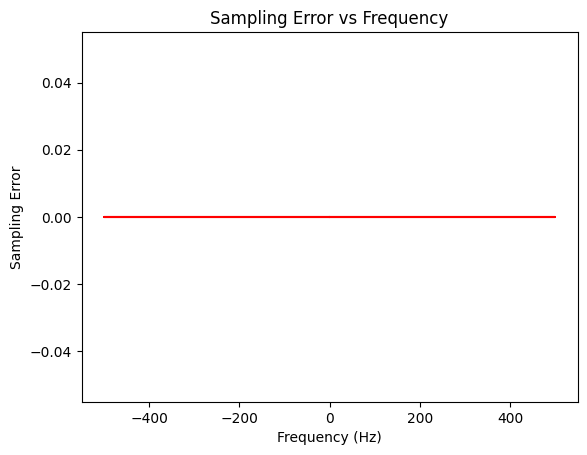
Quantization error in digital signal processing is the difference between the continuous signal's analog and digital components after being rounded to the nearest quantization level. The quantization error is proportional to the quantization step size, which is the distance between adjacent quantization levels.

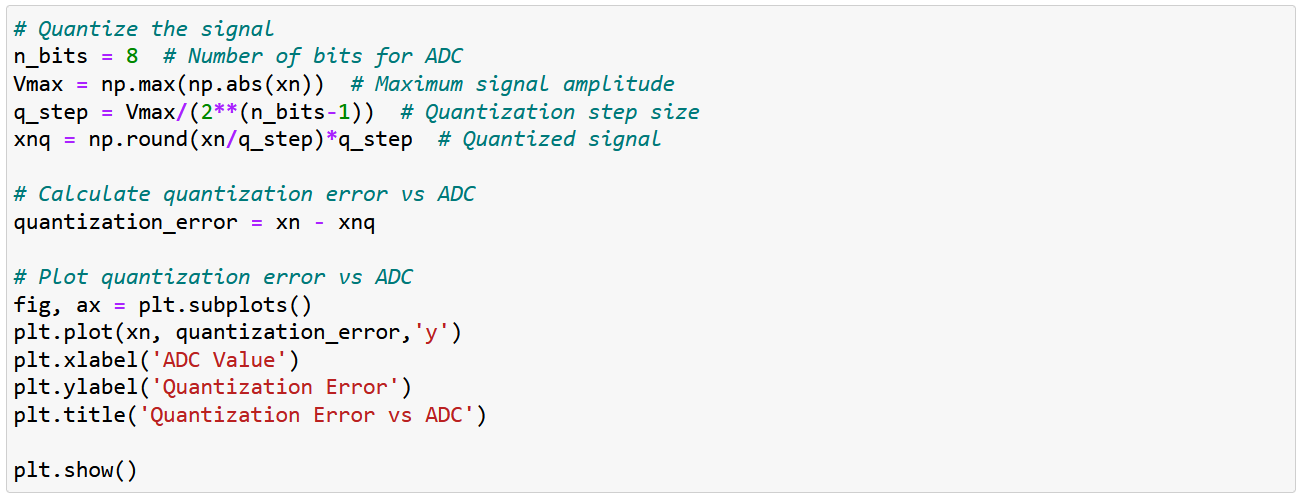
* 1. **Python code & graph**

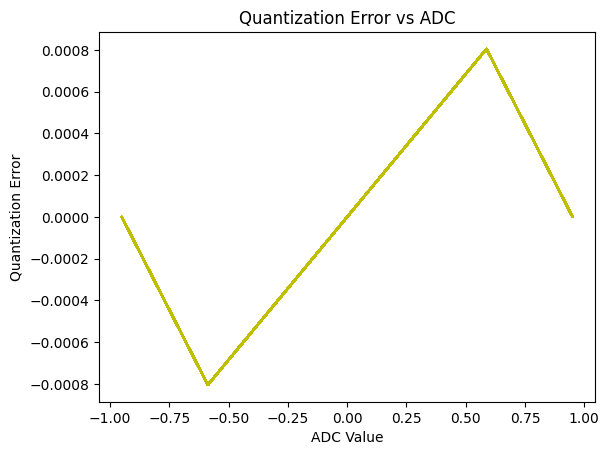
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* 1. **Discussion & Conclusion**

In this experiment, we used the python to analyze sampling error vs frequency and quantization error vs ADC. The signal is then sampled by selecting every Nth sample, where N is the number of samples. The sampling error vs. frequency is then calculated by taking the absolute difference between the Fourier transform of the original signal and the Fourier transform of the sampled signal, using NumPy functions. The signal is then quantized by converting the signal values to a limited number of discrete levels using an analog-to-digital converter (ADC). The number of bits used for the ADC is set to 8, and the maximum signal amplitude is calculated.