

Bangladesh University of Business and Technology



PROJECT TITLE

Logic Operator

Course Name: Software Development I

Course No: CSE 100

Course Teacher: Saifur Rahman (Assistant Professor)

Team Name: Fill in the blanks

Team Member

Mohammad Ashraful Islam

ID: 15162103013

Salahuddin Gazi Fahim

ID: 15162103019

Dween Mohammad

ID: 15162103038

B.Sc. Engg in CSE

Intake: 32nd (Sec: 01)

1. About the project:

This project will work on logic gate operation (AND, OR, NAND, NOR, EX_OR, EX_NOR, NOT) .Which will be done in “C” programming language. The main purpose of this project to show output for four input quantity (AND, OR, NAND, NOR, EX_OR, EX_NOR, NOT) logic gates operation. Outputs of this project will be showed by creating output file. Before showing outputs, input (quantity) will be given after build and run the program. Then the project will be got it desire result.

2. Topics that it (project) will cover:

For this projects function:

1. It needs integer data types.
2. It needs one “While Loop” condition.
3. Under the “While Loop” condition it will be need to declare “Pointer files for individual (AND, OR, NAND, NOR, EX_OR, EX_NOR, NOT) logic gates for showing output in individual output files.
4. Under the “While Loop” if else conditions will be needed to apply for individual (AND, OR, NAND, NOR, EX_OR, EX_NOR, NOT) logic gates condition for it.
5. After applying condition, pointer files have to close.

3. Flow Chart:



