

Lab on March 2, 2023

Implementing logic gates using Universal Gates(NAND & NOR).

Please refer to the example program attached that implements OR gate using NAND.

To run the above code:

```
iverilog -o out nand.v Nand_or.v Nand_or_tb.v  
vvp out
```

Exercises:

1. Implement AND,OR,NOT,XOR,XNOR using NAND gates in Verilog
2. Implement AND,OR,NOT,XOR,XNOR using NOR gates in Verilog

AUMS links will be made available by 10.30 for uploading your work.