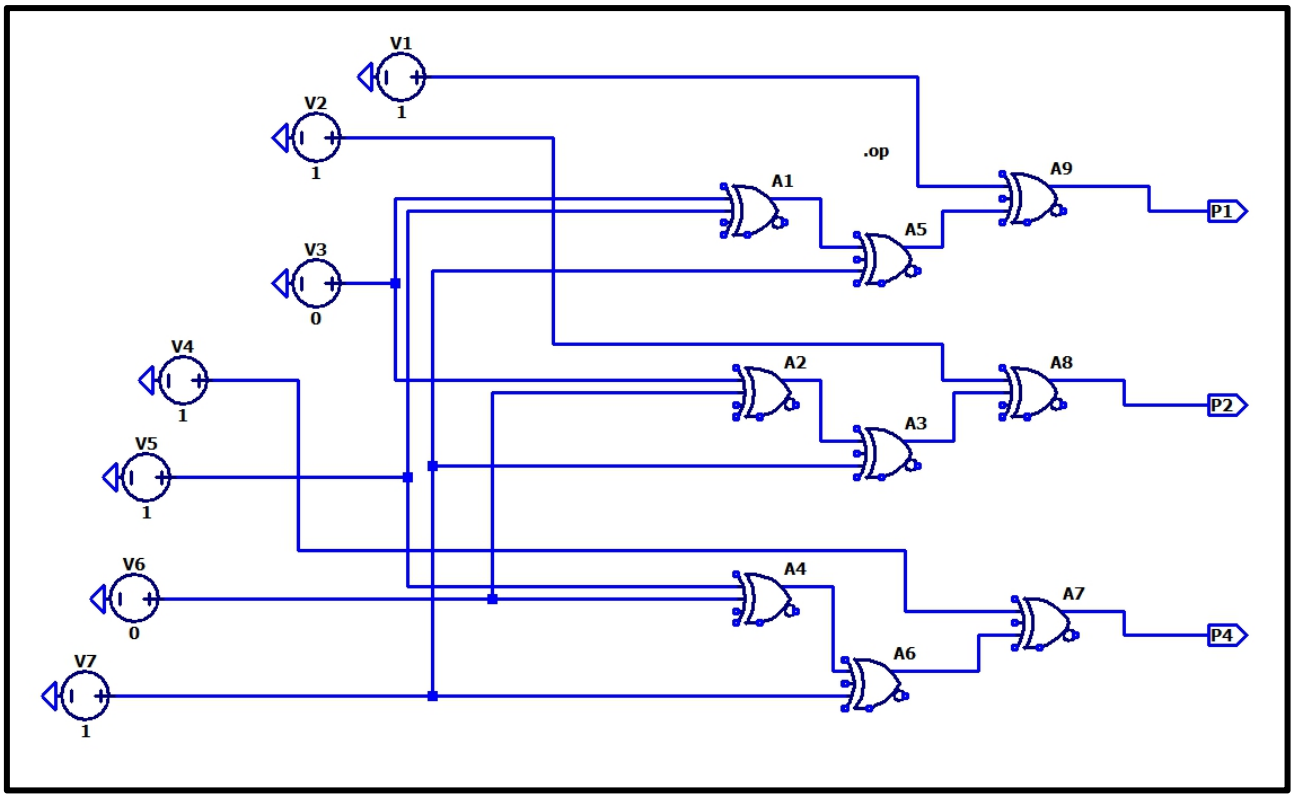
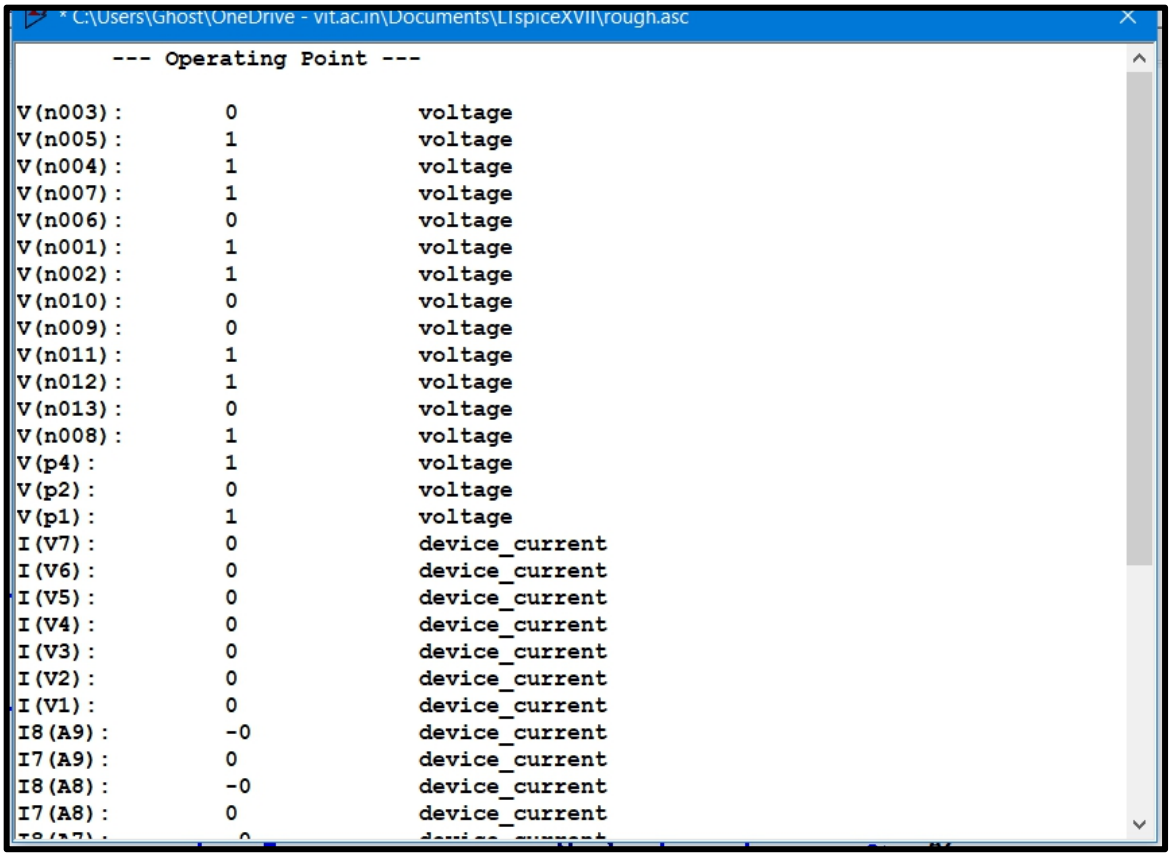
CIRCUIT



OUTPUT



* C:\Users\Ghost\OneDrive - vit.ac.in\Documents\LTspiceXVII\rough.asc

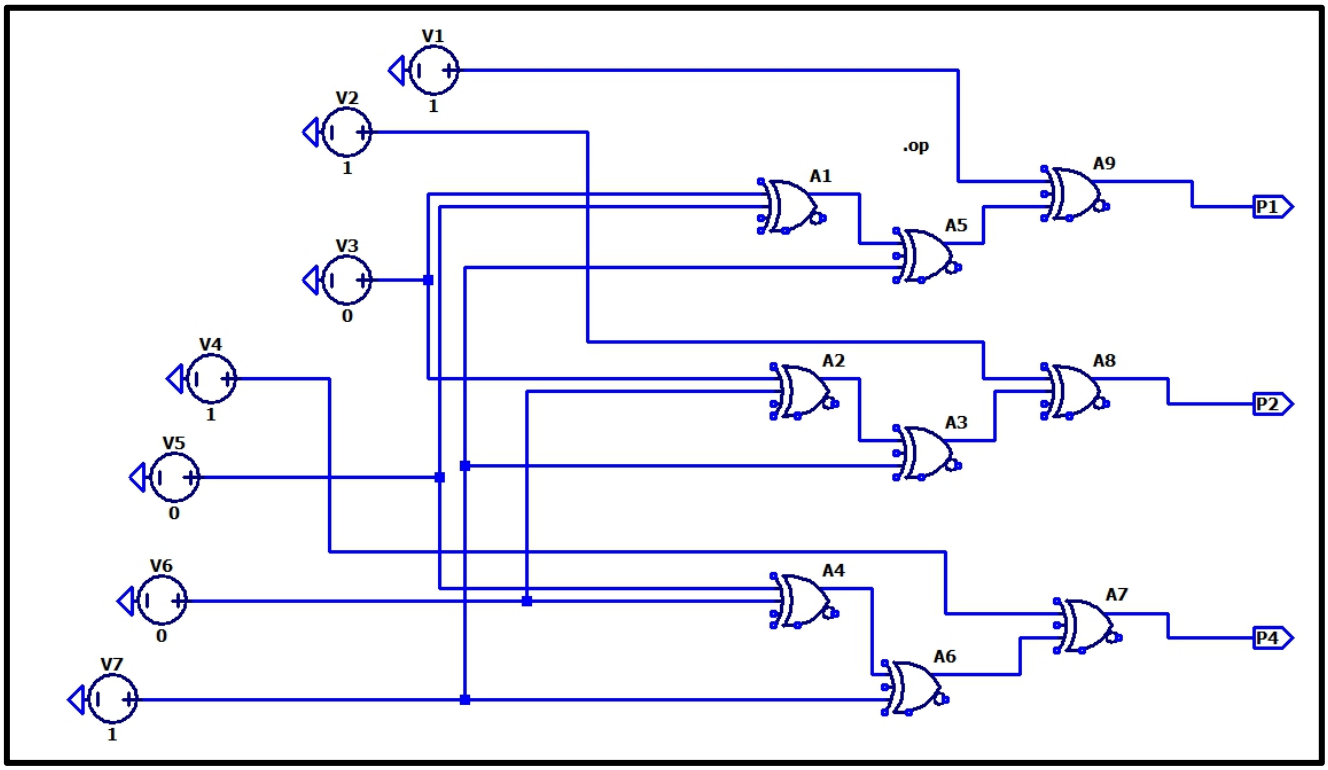
--- Operating Point ---

V(n003):	0	voltage
V(n005):	1	voltage
V(n004):	1	voltage
V(n007):	1	voltage
V(n006):	0	voltage
V(n001):	1	voltage
V(n002):	1	voltage
V(n010):	0	voltage
V(n009):	0	voltage
V(n011):	1	voltage
V(n012):	1	voltage
V(n013):	0	voltage
V(n008):	1	voltage
V(p4):	1	voltage
V(p2):	0	voltage
V(p1):	1	voltage
I(V7):	0	device_current
I(V6):	0	device_current
I(V5):	0	device_current
I(V4):	0	device_current
I(V3):	0	device_current
I(V2):	0	device_current
I(V1):	0	device_current
I8(A9):	-0	device_current
I7(A9):	0	device_current
I8(A8):	-0	device_current
I7(A8):	0	device_current

AS YOU CAN SEE IN THE ABOVE PHOTO THE BIT AT POSITION 101 (P4.P2.P1) IS CORRUPTED.

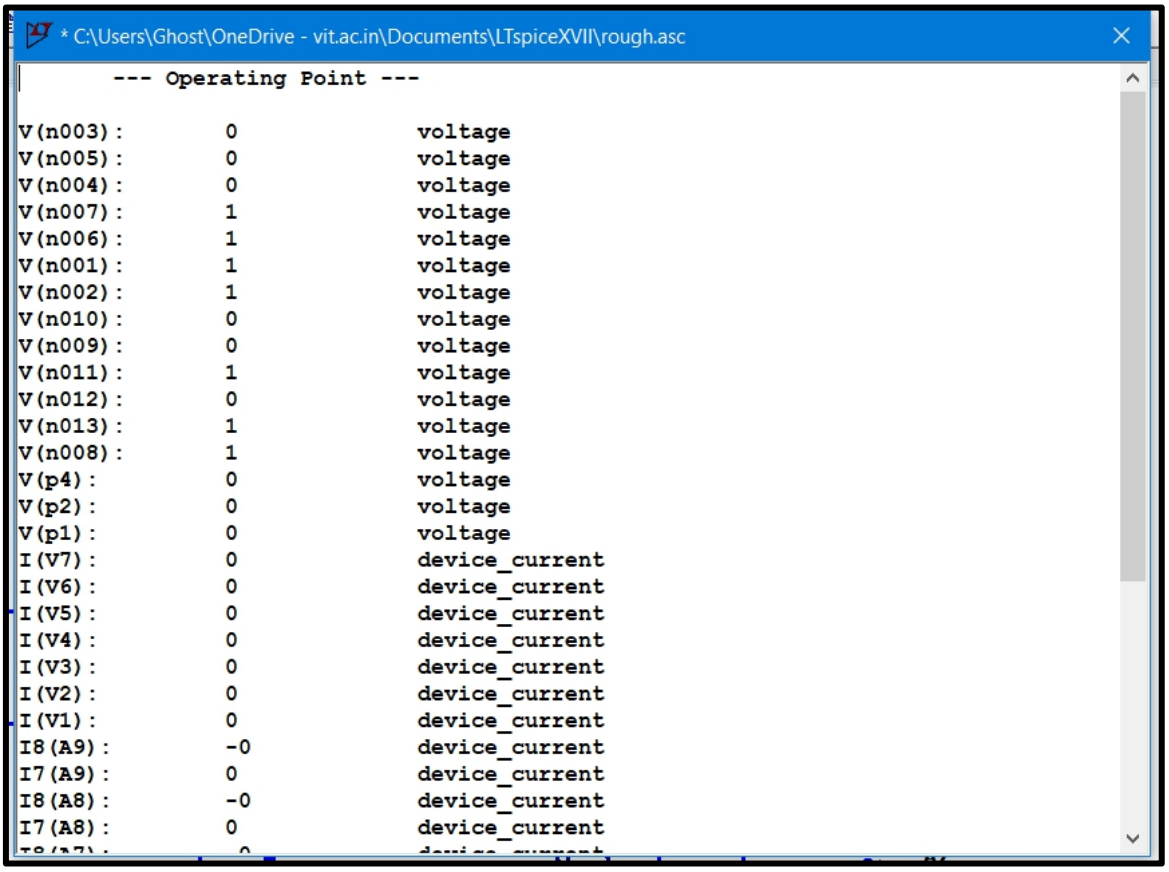
SO NOW WE CORRECT THAT BIT BY FLIPPING IT

NOW WE FLIP V5 FROM 1 TO 0



NOW AS YOU CAN SEE AS WE FLIPPED V5 THE HAMMING CODE IS NOW CORRECT, THAT IS THE REASON WE GOT (P4, P2, P1) AS 000

MEANING NO BIT IS CORRUPTED



RESULT AND INFERENCE

AS FOR THE RECEIVED ‘1 1 0 1 1 0 1’ AFTER ERROR CHECKING THE CIRCUIT THE BIT AT THE POSITION 1 0 1 (5) IS CORRUPTED.

SO V5 SHOULD FLIPPED TO 0 INSTEAD OF 1.