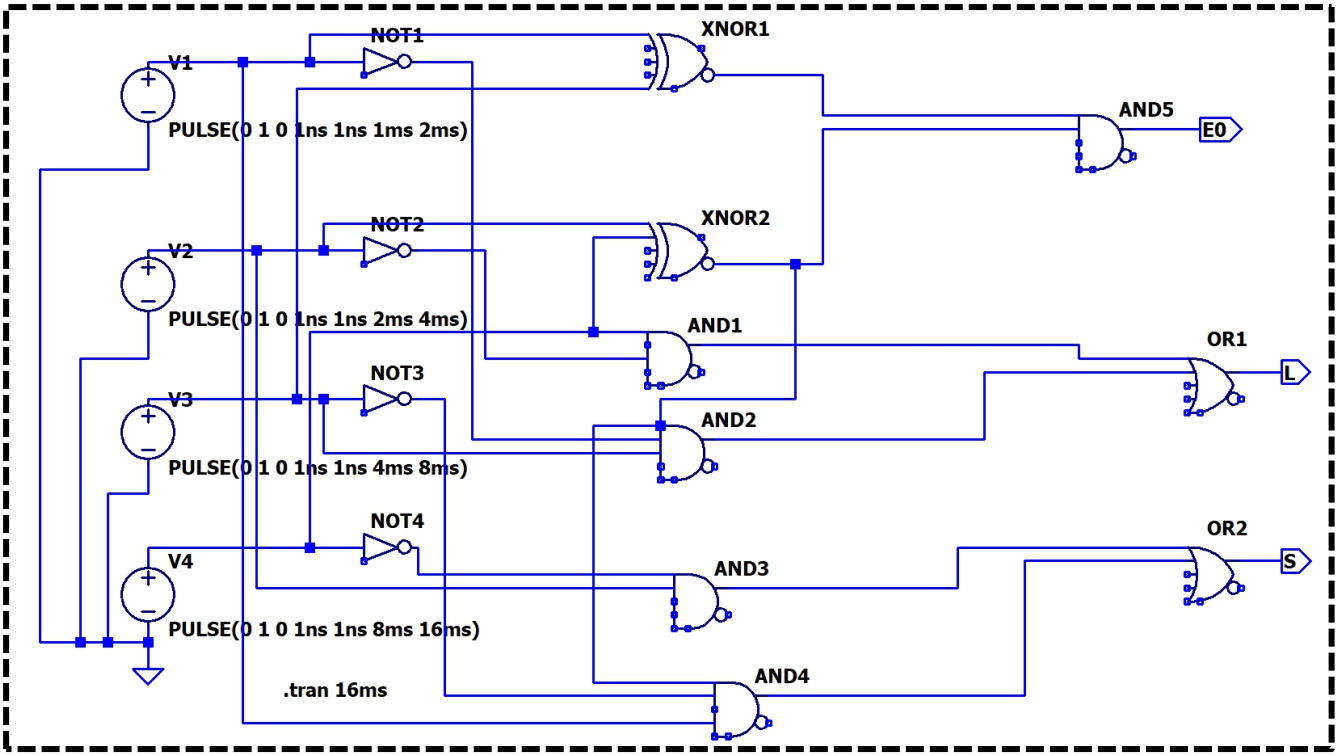
CSE1003 LAB5

BY ASHUTOSH ARDU

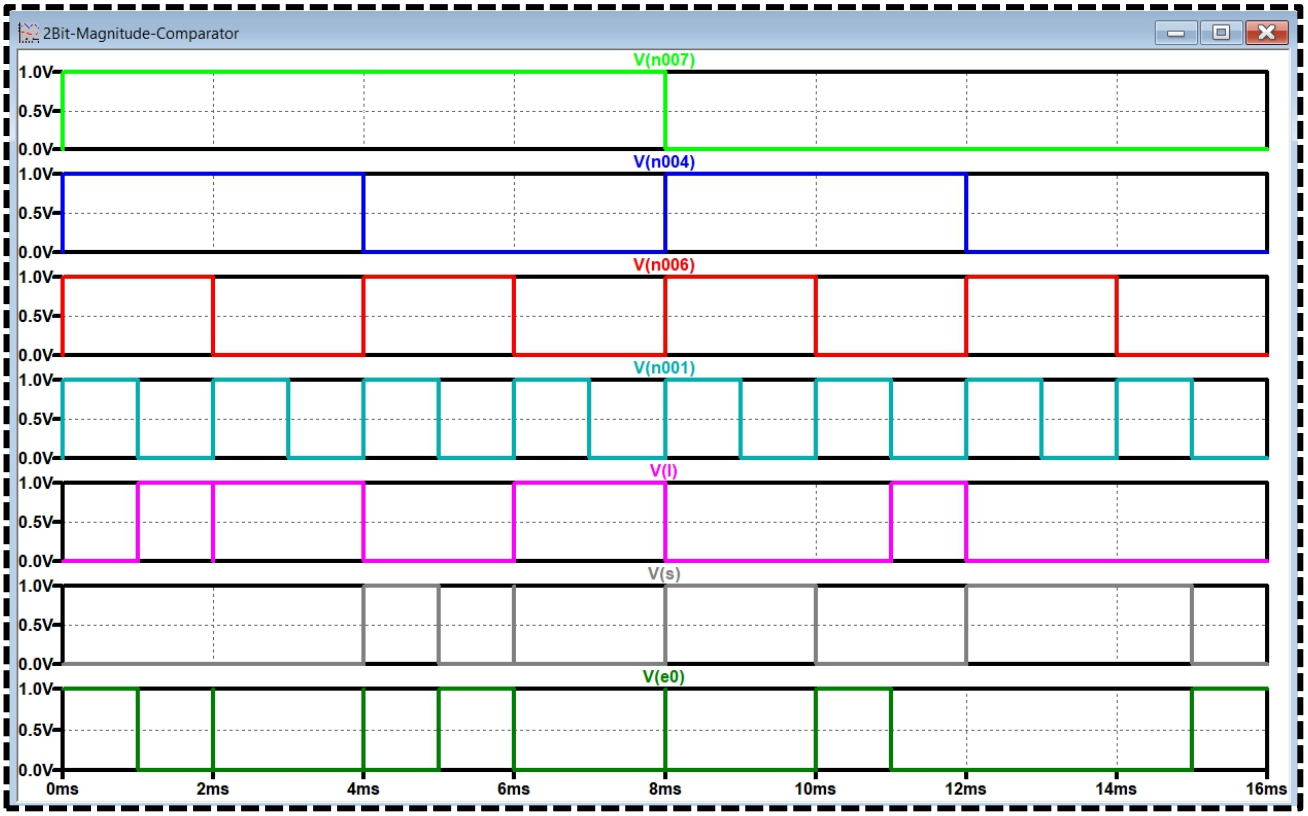
REGISTRATION NO.- 20BRS1262

TWO BIT COMPARATOR

CIRCUIT DIAGRAM:



OUTPUT:


2Bit-Magnitude-Comparator
V(n007)
V(n004)
V(n006)
V(n001)
V(I)
V(s)
V(e0)
1.0V
0.5V
0.0V
0ms
2ms
4ms
6ms
8ms
10ms
12ms
14ms
16ms