NAME – ASHUTOSH ARDU

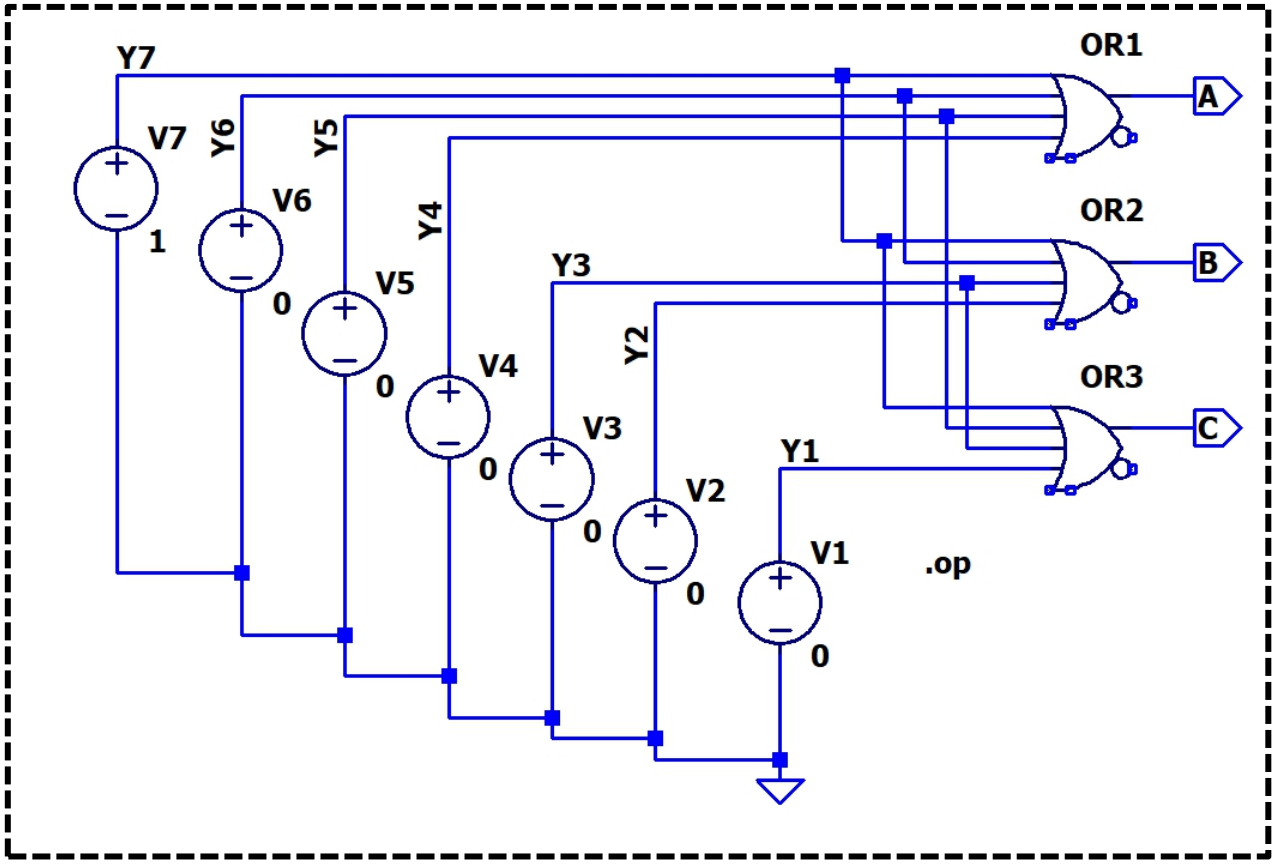
REG NO – 20BRS1262

DATE – 17-5-2021

DLD LAB – 8

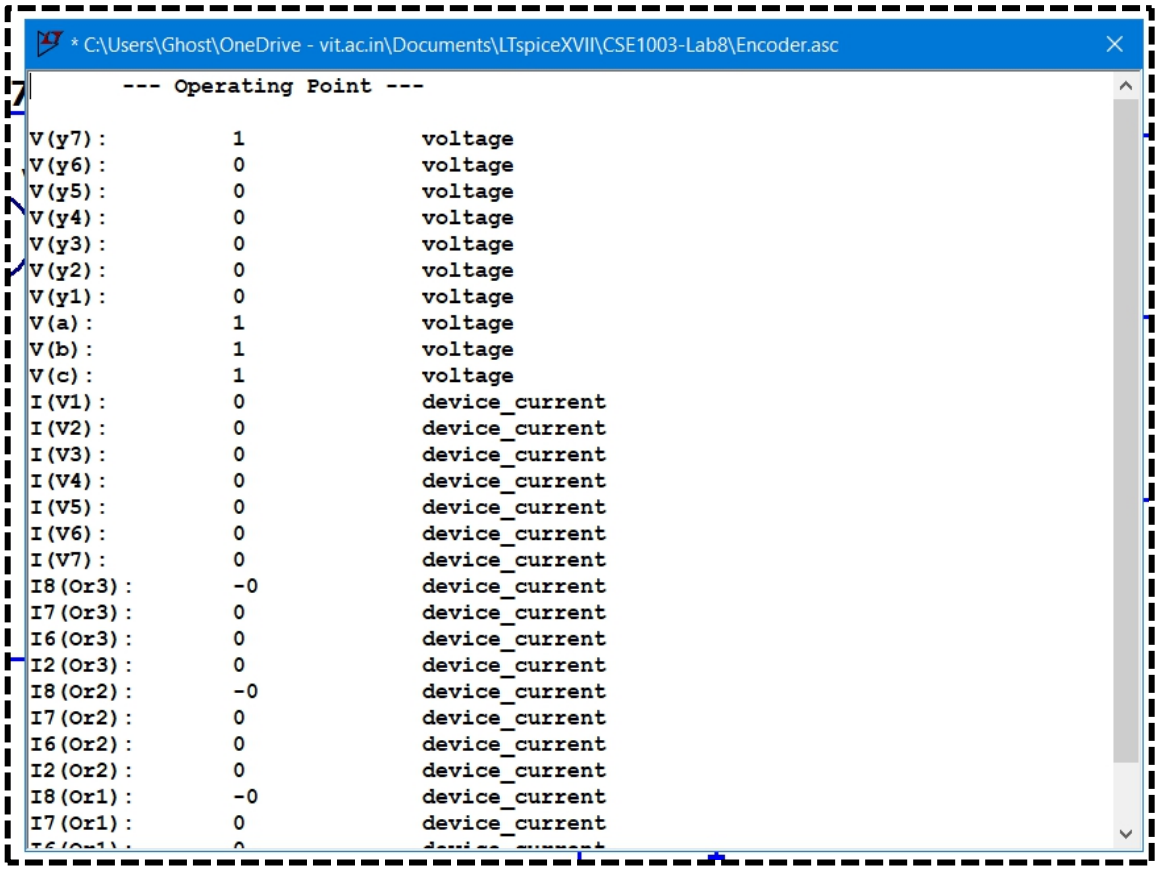
ENCODER

CIRCUIT DIAGRAM

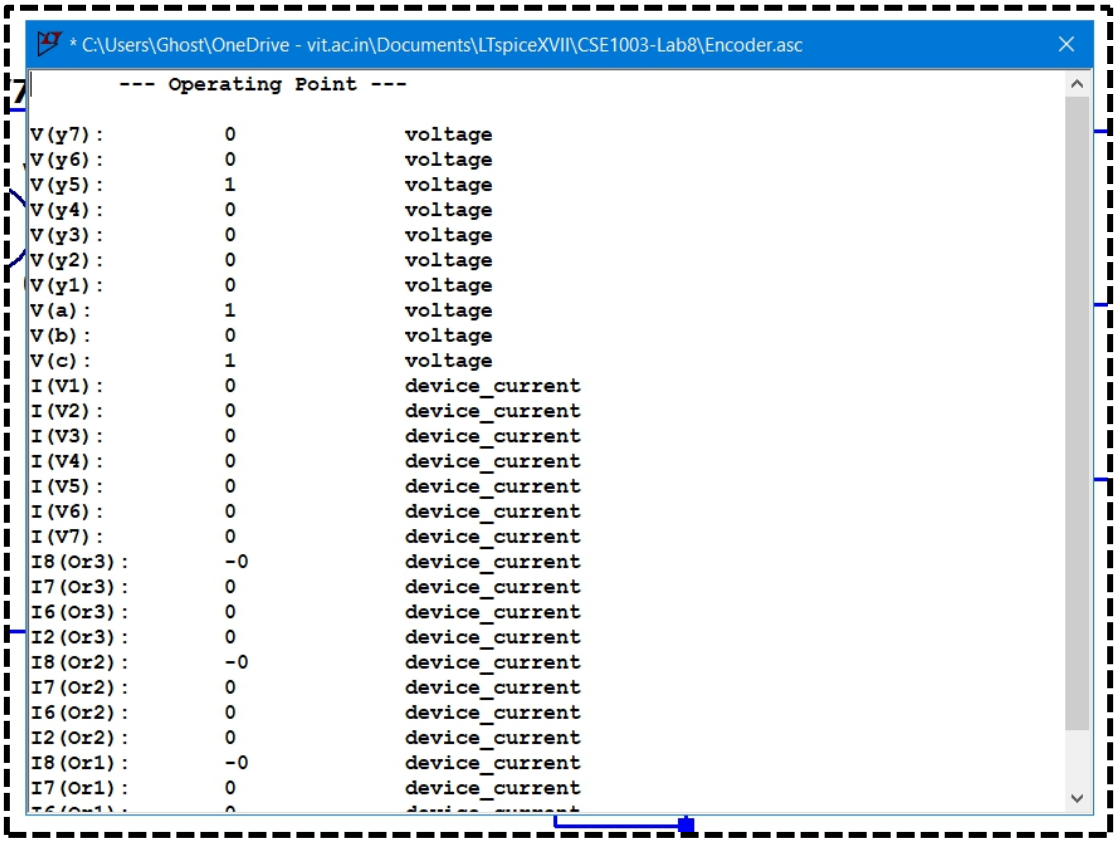


OUTPUTS

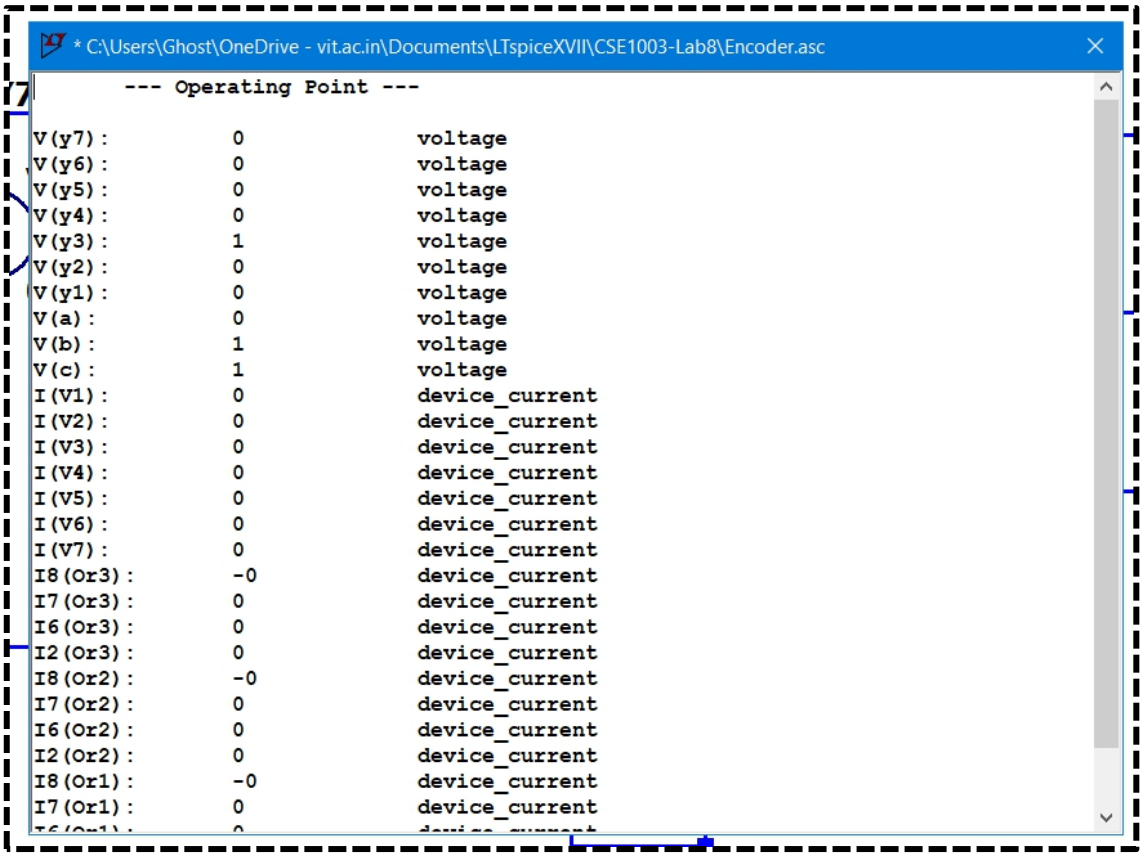
V(Y7) =1 A=1, B=1 & C=1



V(Y5)=1 A=1, B=0 & C=1

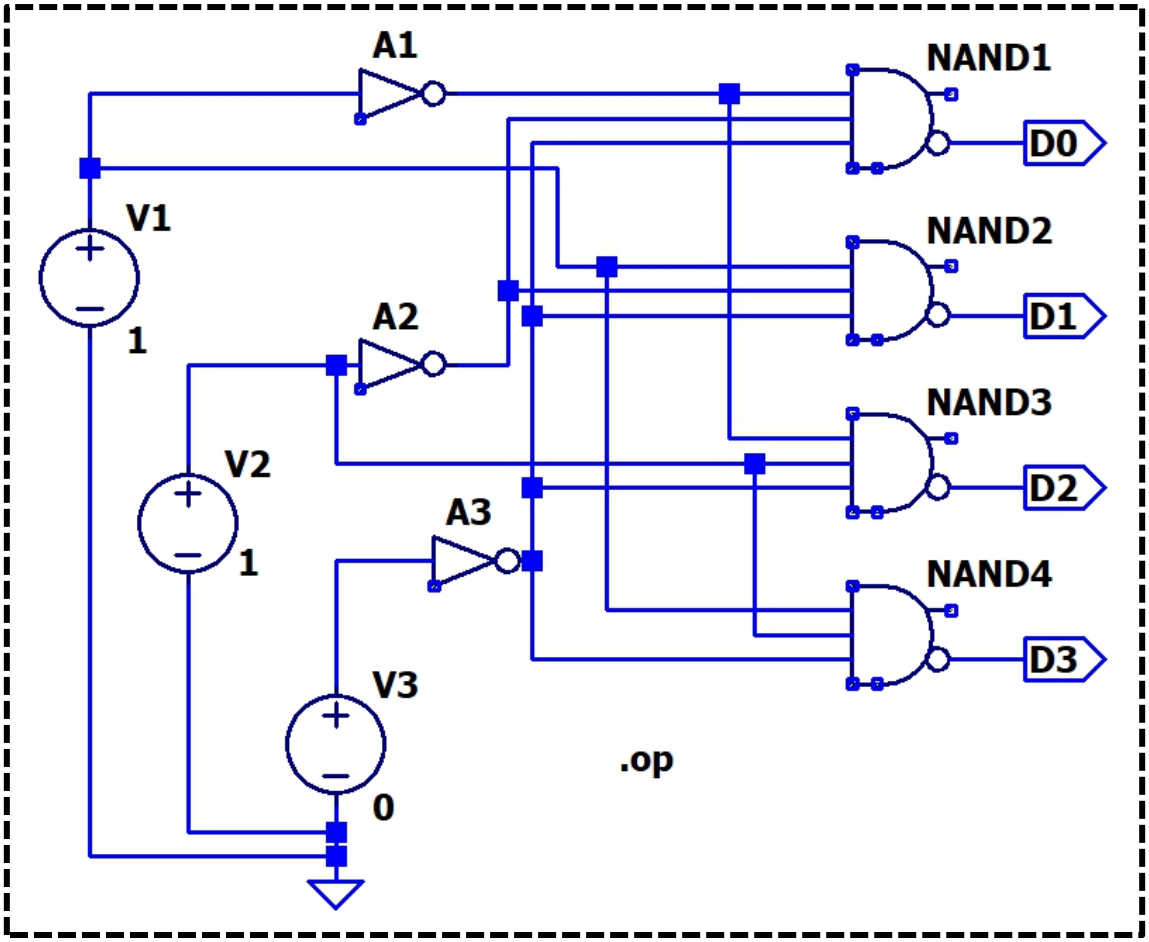


V(Y3)=1 A=0, B=1 & C=1



DECODER

CIRCUIT DIAGRAM



OUTPUTS

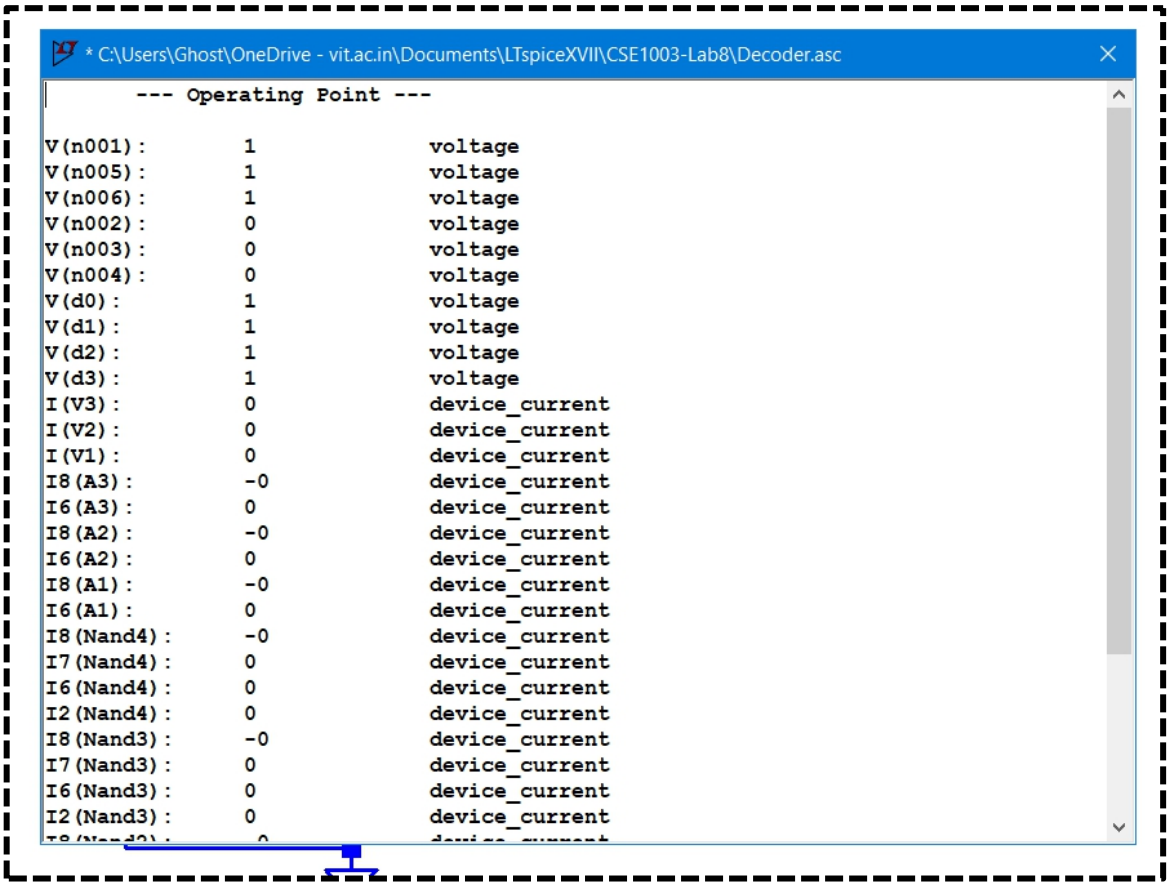
V1=1 V2=1 V3=0

* C:\Users\Ghost\OneDrive - vit.ac.in\Documents\LTspiceXVII\CSE1003-Lab8\Decoder.asc

```
      --- Operating Point ---

V(n001):        1               voltage
V(n005):        1               voltage
V(n006):        0               voltage
V(n002):        0               voltage
V(n003):        0               voltage
V(n004):        1               voltage
V(d0):          1               voltage
V(d1):          1               voltage
V(d2):          1               voltage
V(d3):          0               voltage
I(V3):          0               device_current
I(V2):          0               device_current
I(V1):          0               device_current
I8(A3):         -0              device_current
I6(A3):         0               device_current
I8(A2):         -0              device_current
I6(A2):         0               device_current
I8(A1):         -0              device_current
I6(A1):         0               device_current
I8(Nand4):      -0              device_current
I7(Nand4):      0               device_current
I6(Nand4):      0               device_current
I2(Nand4):      0               device_current
I8(Nand3):      -0              device_current
I7(Nand3):      0               device_current
I6(Nand3):      0               device_current
I2(Nand3):      0               device_current
```

V1=1 V2=1 V3=1



V1=0 V2=0 V3=0