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MODULE *t2pc*

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EXTENDS *Integers, Sequences, FiniteSets, TLC*

CONSTANT *RM*,

*RMMAYFAIL*,

*TMMAYFAIL*

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--algorithm TransactionCommit{
  variable rmState = [rm ∈ RM ↦ "working"],
         tmState = "init" ;
  define {
    canCommit ≜ ∀ rm ∈ RM : rmState[rm] ∈ {"prepared", "committed", "hidden"} ∧ tmState ≠ "abort"
    canAbort ≜ ∀ rm ∈ RM : rmState[rm] ≠ "committed" ∧ tmState ≠ "commit"
  }
  macro Prepare( p ) {
    when rmState[p] = "working" ;
    rmState[p] := "prepared" ;
  }

  macro Decide( p ) {
    either { when ∧ rmState[p] = "prepared"
              ∧ tmState = "commit" ;
              rmState[p] := "committed" ;
            }
    or { when ∧ rmState[p] ∈ {"working", "prepared"}
           ∧ tmState = "abort" ;
          rmState[p] := "aborted" ;
        }
  }

  macro Fail( p ) {
    if ( RMMAYFAIL )
    {
      when rmState[p] ∈ {"working", "prepared"};
      rmState[p] := "hidden" ;
    }
  }

  fair process ( RManager ∈ RM ) {
    RS: while ( rmState[self] ∈ {"working", "prepared"} ) {
      either Prepare(self) or Decide(self) or Fail(self) }
    }

  fair process ( TManager = 0 ) {
    TS: either { await canCommit ;
                TC: tmState := "commit" ;

                F1: if ( TMMAYFAIL ) tmState := "hidden" ;
              }
  }

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    or { await canAbort ;
          TA: tmState := "abort" ;

          F2: if ( TMMAYFAIL ) tmState := "hidden" ;
        }
  }

  fair process ( BTManager = 1 ) {
    BS: either { await (canCommit ∧ tmState = "hidden") ∨ (∀ rm ∈ RM : rmState[rm] ∈ { "hidden",
                                                                                      "committed" }) ;

                BC: tmState := "commit" ;
              }
    or { await (canAbort ∧ tmState = "hidden") ∨ (∀ rm ∈ RM : rmState[rm] ∈ { "hidden", "aborted" }) ;
        BA: tmState := "abort" ;
      }
  }
}

```

BEGIN TRANSLATION

VARIABLES  $rmState$ ,  $tmState$ ,  $pc$

define statement

$canCommit \triangleq \forall rm \in RM : rmState[rm] \in \{ \text{"prepared"}, \text{"committed"}, \text{"hidden"} \} \wedge tmState \neq \text{"abort"}$   
 $canAbort \triangleq \forall rm \in RM : rmState[rm] \neq \text{"committed"} \wedge tmState \neq \text{"commit"}$

$vars \triangleq \langle rmState, tmState, pc \rangle$

$ProcSet \triangleq (RM) \cup \{0\} \cup \{1\}$

$Init \triangleq$  Global variables

$\wedge rmState = [rm \in RM \mapsto \text{"working"}]$

$\wedge tmState = \text{"init"}$

$\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in RM \rightarrow \text{"RS"}$

$\square \quad self = 0 \rightarrow \text{"TS"}$

$\square \quad self = 1 \rightarrow \text{"BS"}]$

$RS(self) \triangleq \wedge pc[self] = \text{"RS"}$

$\wedge \text{IF } rmState[self] \in \{ \text{"working"}, \text{"prepared"} \}$

THEN  $\wedge \vee \wedge rmState[self] = \text{"working"}$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"prepared"}]$

$\vee \wedge \vee \wedge \wedge rmState[self] = \text{"prepared"}$

$\wedge tmState = \text{"commit"}$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"committed"}]$

$\vee \wedge \wedge rmState[self] \in \{ \text{"working"}, \text{"prepared"} \}$

$\wedge tmState = \text{"abort"}$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"aborted"}]$

$$\begin{aligned}
& \vee \wedge \text{IF } RMMAYFAIL \\
& \quad \text{THEN } \wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"hidden"}] \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \wedge \text{UNCHANGED } rmState \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"RS"}] \\
& \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}] \\
& \quad \wedge \text{UNCHANGED } rmState \\
& \wedge \text{UNCHANGED } tmState
\end{aligned}$$

$$RManager(self) \triangleq RS(self)$$

$$\begin{aligned}
TS & \triangleq \wedge pc[0] = \text{"TS"} \\
& \wedge \vee \wedge canCommit \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TC"}] \\
& \quad \vee \wedge canAbort \\
& \quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TA"}] \\
& \wedge \text{UNCHANGED } \langle rmState, tmState \rangle
\end{aligned}$$

$$\begin{aligned}
TC & \triangleq \wedge pc[0] = \text{"TC"} \\
& \wedge tmState' = \text{"commit"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F1"}] \\
& \wedge \text{UNCHANGED } rmState
\end{aligned}$$

$$\begin{aligned}
F1 & \triangleq \wedge pc[0] = \text{"F1"} \\
& \wedge \text{IF } TMMAYFAIL \\
& \quad \text{THEN } \wedge tmState' = \text{"hidden"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \wedge \text{UNCHANGED } tmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState
\end{aligned}$$

$$\begin{aligned}
TA & \triangleq \wedge pc[0] = \text{"TA"} \\
& \wedge tmState' = \text{"abort"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F2"}] \\
& \wedge \text{UNCHANGED } rmState
\end{aligned}$$

$$\begin{aligned}
F2 & \triangleq \wedge pc[0] = \text{"F2"} \\
& \wedge \text{IF } TMMAYFAIL \\
& \quad \text{THEN } \wedge tmState' = \text{"hidden"} \\
& \quad \text{ELSE } \wedge \text{TRUE} \\
& \quad \quad \wedge \text{UNCHANGED } tmState \\
& \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState
\end{aligned}$$

$$TManager \triangleq TS \vee TC \vee F1 \vee TA \vee F2$$

$$BS \triangleq \wedge pc[1] = \text{"BS"}$$

$$\begin{aligned}
& \wedge \vee \wedge (canCommit \wedge tmState = \text{"hidden"}) \vee (\forall rm \in RM : rmState[rm] \in \{\text{"hidden"}, \text{"committed"}\}) \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"BC"}] \\
& \vee \wedge (canAbort \wedge tmState = \text{"hidden"}) \vee (\forall rm \in RM : rmState[rm] \in \{\text{"hidden"}, \text{"aborted"}\}) \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"BA"}] \\
& \wedge \text{UNCHANGED } \langle rmState, tmState \rangle \\
\\
BC & \triangleq \wedge pc[1] = \text{"BC"} \\
& \wedge tmState' = \text{"commit"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
\\
BA & \triangleq \wedge pc[1] = \text{"BA"} \\
& \wedge tmState' = \text{"abort"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } rmState \\
\\
BTManager & \triangleq BS \vee BC \vee BA \\
\\
Next & \triangleq TManager \vee BTManager \\
& \vee (\exists self \in RM : RManager(self)) \\
& \vee \text{Disjunct to prevent deadlock on termination} \\
& ((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars) \\
\\
Spec & \triangleq \wedge Init \wedge \Box [Next]_{vars} \\
& \wedge \forall self \in RM : WF_{vars}(RManager(self)) \\
& \wedge WF_{vars}(TManager) \\
& \wedge WF_{vars}(BTManager) \\
\\
Termination & \triangleq \Diamond (\forall self \in ProcSet : pc[self] = \text{"Done"}) \\
\\
& \text{END TRANSLATION} \\
\\
\hline
Consistent & \triangleq \forall rm1, rm2 \in RM : \neg \wedge rmState[rm1] = \text{"aborted"} \wedge rmState[rm2] = \text{"committed"} \\
\hline
\\
\backslash * & \text{Modification History} \\
\backslash * & \text{Last modified Tue Dec 05 03:38:00 EST 2017 by ashutoshahmadalexandar} \\
\backslash * & \text{Created Wed Nov 29 15:16:19 EST 2017 by ashutoshahmadalexandar}
\end{aligned}$$